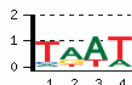





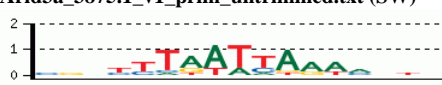






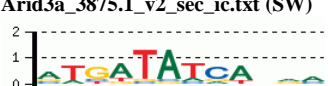
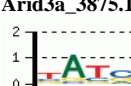

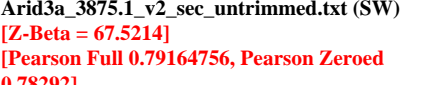
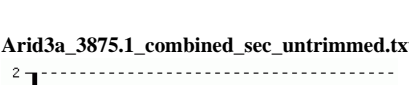
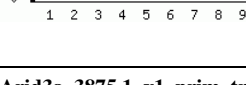
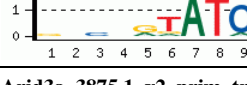



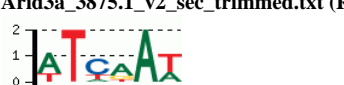
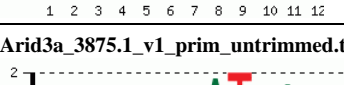
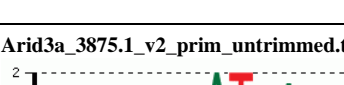

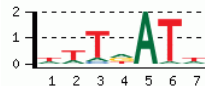


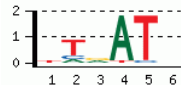


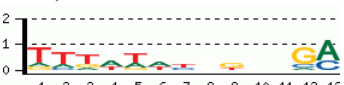
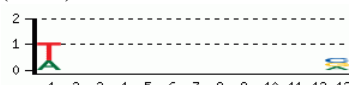
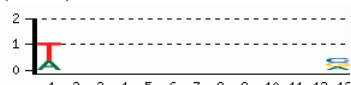
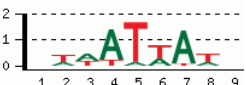


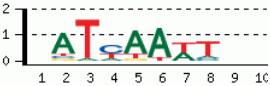










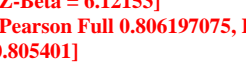

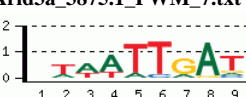


**Logos for all motifs generated from each array design separately and from the combined array data.** The combined array data are the final data.

# Arid3a\_3875.1

Array Version 1	Array Version 2	Combined
<p>Arid3a_3875.1_v1_prim_auc.txt (SW)  [Z-Beta = 125.406]  [Pearson Full 0.806197075, Pearson Zeroed 0.784572]</p> 	<p>Arid3a_3875.1_v2_prim_auc.txt (SW)</p> 	<p>Arid3a_3875.1_combined_prim_auc.txt (SW)</p> 
<p>Arid3a_3875.1_v1_prim_ic.txt (SW)</p> 	<p>Arid3a_3875.1_v2_prim_ic.txt (SW)</p> 	<p>Arid3a_3875.1_combined_prim_ic_trimmed.txt (SW)</p> 
<p>Arid3a_3875.1_v1_prim_untrimmed.txt (SW)</p> 	<p>Arid3a_3875.1_v2_prim_untrimmed.txt (SW)</p> 	<p>Arid3a_3875.1_combined_prim_untrimmed.txt (SW)</p> 
<p>Arid3a_3875.1_v1_sec_auc.txt (SW)</p> 	<p>Arid3a_3875.1_v2_sec_auc.txt (SW)</p> 	<p>Arid3a_3875.1_combined_sec_auc.txt (SW)</p> 
<p>Arid3a_3875.1_v1_sec_ic.txt (SW)</p> 	<p>Arid3a_3875.1_v2_sec_ic.txt (SW)</p> 	<p>Arid3a_3875.1_combined_sec_ic_trimmed.txt (SW)</p> 
<p>Arid3a_3875.1_v1_sec_untrimmed.txt (SW)</p> 	<p>Arid3a_3875.1_v2_sec_untrimmed.txt (SW)  [Z-Beta = 67.5214]  [Pearson Full 0.79164756, Pearson Zeroed 0.78292]</p> 	<p>Arid3a_3875.1_combined_sec_untrimmed.txt (SW)</p> 
<p>Arid3a_3875.1_v1_prim_trimmed.txt (RM8)</p> 	<p>Arid3a_3875.1_v2_prim_trimmed.txt (RM8)</p> 	<p>Arid3a_3875.1_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Arid3a_3875.1_v1_sec_trimmed.txt (RM8)  [Z-Beta = 95.6877]  [Pearson Full 0.806197075, Pearson Zeroed 0.78343]</p> 	<p>Arid3a_3875.1_v2_sec_trimmed.txt (RM8)</p> 	<p>Arid3a_3875.1_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Arid3a_3875.1_v1_prim_untrimmed.txt (RM8)</p> 	<p>Arid3a_3875.1_v2_prim_untrimmed.txt (RM8)</p> 	<p>Arid3a_3875.1_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Arid3a_3875.1_v1_sec_untrimmed.txt (RM8)  [Z-Beta = 236.47]  [Pearson Full 0.806197075, Pearson Zeroed 0.753462]</p> 	<p>Arid3a_3875.1_v2_sec_untrimmed.txt (RM8)  [Z-Beta = 67.5214]  [Pearson Full 0.79164756, Pearson Zeroed 0.78292]</p> 	<p>Arid3a_3875.1_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>




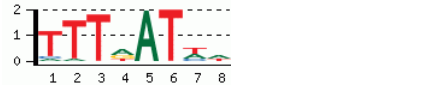

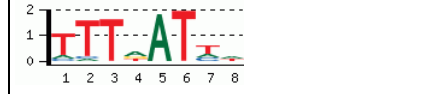

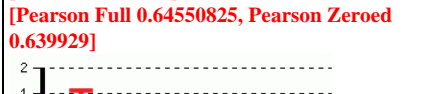
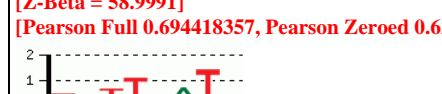


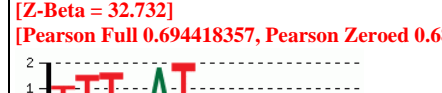
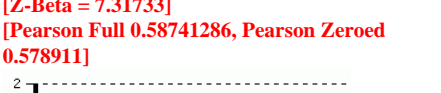


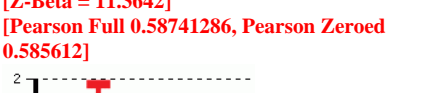

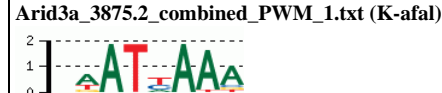


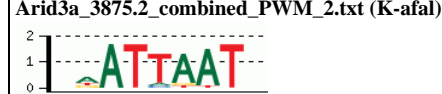
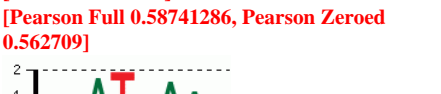


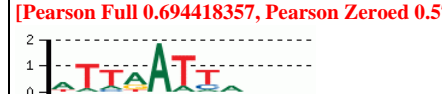
<p>Arid3a_3875.1_v1_RM35_prim_trimmed.txt (RM35)</p> 	<p>Arid3a_3875.1_v2_RM35_prim_trimmed.txt (RM35)</p> 	<p>Arid3a_3875.1_RM_combined_prim_trimmed.txt (RM35)</p> 
<p>Arid3a_3875.1_v1_RM35_sec_trimmed.txt (RM35)</p> <p>[Z-Beta = 108.272] [Pearson Full 0.806197075, Pearson Zeroed 0.78338]</p> 	<p>Arid3a_3875.1_v2_RM35_sec_trimmed.txt (RM35)</p> <p>[Z-Beta = 21.2383] [Pearson Full 0.79164756, Pearson Zeroed 0.790265]</p> 	<p>Arid3a_3875.1_RM_combined_sec_trimmed.txt (RM35)</p> <p>[Z-Beta = 102.665] [Pearson Full 0.838814794, Pearson Zeroed 0.824433]</p> 
<p>Arid3a_3875.1_v1_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Arid3a_3875.1_v2_RM35_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = 591.441] [Pearson Full 0.79164756, Pearson Zeroed 0.745566]</p> 	<p>Arid3a_3875.1_RM_combined_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = 502.414] [Pearson Full 0.838814794, Pearson Zeroed 0.748442]</p> 
<p>Arid3a_3875.1_v1_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Arid3a_3875.1_v2_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Arid3a_3875.1_RM_combined_sec_untrimmed.txt (RM35)</p> 
<p>Arid3a_3875.1_PWM_1.txt (K-afal)</p> <p>[Z-Beta = 22.9734] [Pearson Full 0.806197075, Pearson Zeroed 0.801818]</p> 	<p>Arid3a_3875.1_PWM_1.txt (K-afal)</p> <p>[Z-Beta = 14.8292] [Pearson Full 0.79164756, Pearson Zeroed 0.790106]</p> 	<p>Arid3a_3875.1_combined_PWM_1.txt (K-afal)</p> 
<p>Arid3a_3875.1_PWM_2.txt (K-afal)</p> <p>[Z-Beta = 19.0388] [Pearson Full 0.806197075, Pearson Zeroed 0.805506]</p> 	<p>Arid3a_3875.1_PWM_2.txt (K-afal)</p> 	<p>Arid3a_3875.1_combined_PWM_2.txt (K-afal)</p> 
<p>Arid3a_3875.1_PWM_3.txt (K-afal)</p> 	<p>Arid3a_3875.1_PWM_3.txt (K-afal)</p> 	<p>Arid3a_3875.1_combined_PWM_3.txt (K-afal)</p> <p>[Z-Beta = 15.3953] [Pearson Full 0.838814794, Pearson Zeroed 0.836184]</p> 
<p>Arid3a_3875.1_PWM_4.txt (K-afal)</p> 	<p>Arid3a_3875.1_PWM_4.txt (K-afal)</p> 	<p>Arid3a_3875.1_combined_PWM_4.txt (K-afal)</p> 
<p>Arid3a_3875.1_PWM_5.txt (K-afal)</p> <p>[Z-Beta = 13.6443] [Pearson Full 0.806197075, Pearson Zeroed 0.803801]</p> 	<p>Arid3a_3875.1_PWM_5.txt (K-afal)</p> <p>[Z-Beta = 32.9792] [Pearson Full 0.79164756, Pearson Zeroed 0.787332]</p> 	<p>Arid3a_3875.1_combined_PWM_5.txt (K-afal)</p> <p>No PWM Available</p>
<p>Arid3a_3875.1_PWM_6.txt (K-afal)</p> <p>[Z-Beta = 6.12153] [Pearson Full 0.806197075, Pearson Zeroed 0.805401]</p> 	<p>Arid3a_3875.1_PWM_6.txt (K-afal)</p> <p>No PWM Available</p>	<p>Arid3a_3875.1_combined_PWM_6.txt (K-afal)</p> <p>No PWM Available</p>





 <p>A sequence logo plot for Arid3a_3875.1_PWM_7.txt (K-afal). The x-axis represents positions 1 through 9. The y-axis represents information content from 0 to 2. The sequence ATGAAA is highlighted, with 'A' at position 3, 'T' at position 4, 'G' at position 6, and 'A's at positions 7, 8, and 9.</p>		
<p><b>Arid3a_3875.1_PWM_7.txt (K-afal)</b></p>  <p>A sequence logo plot for Arid3a_3875.1_PWM_7.txt (K-afal). The x-axis represents positions 1 through 10. The y-axis represents information content from 0 to 2. The sequence TAAATTGAT is highlighted, with 'T' at position 3, 'A's at positions 4 and 5, 'T's at positions 6 and 7, 'G' at position 8, and 'A' at position 9.</p>	<p><b>Arid3a_3875.1_PWM_7.txt (K-afal)</b>  <b>No PWM Available</b></p>	<p><b>Arid3a_3875.1_combined_PWM_7.txt (K-afal)</b>  <b>No PWM Available</b></p>



# Arid3a\_3875.2

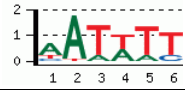
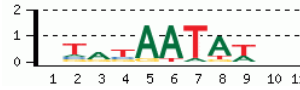
Array Version 1	Array Version 2	Combined
<p>Arid3a_3875.2_v1_prim_auc.txt (SW)</p>	<p>Arid3a_3875.2_v2_prim_auc.txt (SW)</p>	<p>Arid3a_3875.2_combined_prim_auc.txt (SW)</p>
<p>Arid3a_3875.2_v1_prim_ic.txt (SW)</p>	<p>Arid3a_3875.2_v2_prim_ic.txt (SW)</p>	<p>Arid3a_3875.2_combined_prim_ic_trimmed.txt (SW)</p>
<p>Arid3a_3875.2_v1_prim_untrimmed.txt (SW)</p>	<p>Arid3a_3875.2_v2_prim_untrimmed.txt (SW)</p>	<p>Arid3a_3875.2_combined_prim_untrimmed.txt (SW)</p>
<p>Arid3a_3875.2_v1_sec_auc.txt (SW)</p>	<p>Arid3a_3875.2_v2_sec_auc.txt (SW)  [Z-Beta = 4.79765]  [Pearson Full 0.64550825, Pearson Zeroed 0.641002]</p>	<p>Arid3a_3875.2_combined_sec_auc.txt (SW)</p>
<p>Arid3a_3875.2_v1_sec_ic.txt (SW)</p>	<p>Arid3a_3875.2_v2_sec_ic.txt (SW)</p>	<p>Arid3a_3875.2_combined_sec_ic_trimmed.txt (SW)  [Z-Beta = 23.1198]  [Pearson Full 0.694418357, Pearson Zeroed 0.655615]</p>
<p>Arid3a_3875.2_v1_sec_untrimmed.txt (SW)</p>	<p>Arid3a_3875.2_v2_sec_untrimmed.txt (SW)  [Z-Beta = 97.6841]  [Pearson Full 0.64550825, Pearson Zeroed 0.592722]</p>	<p>Arid3a_3875.2_combined_sec_untrimmed.txt (SW)</p>
<p>Arid3a_3875.2_v1_prim_trimmed.txt (RM8)</p>	<p>Arid3a_3875.2_v2_prim_trimmed.txt (RM8)  [Z-Beta = 16.5756]  [Pearson Full 0.64550825, Pearson Zeroed 0.636757]</p>	<p>Arid3a_3875.2_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Arid3a_3875.2_v1_sec_trimmed.txt (RM8)  [Z-Beta = 30.3994]  [Pearson Full 0.58741286, Pearson Zeroed 0.583109]</p>	<p>Arid3a_3875.2_v2_sec_trimmed.txt (RM8)</p>	<p>Arid3a_3875.2_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Arid3a_3875.2_v1_prim_untrimmed.txt (RM8)</p>	<p>Arid3a_3875.2_v2_prim_untrimmed.txt (RM8)</p>	<p>Arid3a_3875.2_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Arid3a_3875.2_v1_sec_untrimmed.txt (RM8)  [Z-Beta = 37.0066]  [Pearson Full 0.58741286, Pearson Zeroed 0.536549]</p>	<p>Arid3a_3875.2_v2_sec_untrimmed.txt (RM8)  [Z-Beta = 97.6841]  [Pearson Full 0.64550825, Pearson Zeroed 0.592722]</p>	<p>Arid3a_3875.2_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>

		
Arid3a_3875.2_v1_RM35_prim_trimmed.txt (RM35) 	Arid3a_3875.2_v2_RM35_prim_trimmed.txt (RM35) [Z-Beta = 7.82431] [Pearson Full 0.64550825, Pearson Zeroed 0.643144] 	Arid3a_3875.2_RM_combined_prim_trimmed.txt (RM35) 
Arid3a_3875.2_v1_RM35_sec_trimmed.txt (RM35) 	Arid3a_3875.2_v2_RM35_sec_trimmed.txt (RM35) [Z-Beta = 7.82068] [Pearson Full 0.64550825, Pearson Zeroed 0.639929] 	Arid3a_3875.2_RM_combined_sec_trimmed.txt (RM35) [Z-Beta = 58.9991] [Pearson Full 0.694418357, Pearson Zeroed 0.6211] 
Arid3a_3875.2_v1_RM35_prim_untrimmed.txt (RM35) 	Arid3a_3875.2_v2_RM35_prim_untrimmed.txt (RM35) 	Arid3a_3875.2_RM_combined_prim_untrimmed.txt (RM35) [Z-Beta = 32.732] [Pearson Full 0.694418357, Pearson Zeroed 0.686815] 
Arid3a_3875.2_v1_RM35_sec_untrimmed.txt (RM35) [Z-Beta = 7.31733] [Pearson Full 0.58741286, Pearson Zeroed 0.578911] 	Arid3a_3875.2_v2_RM35_sec_untrimmed.txt (RM35) 	Arid3a_3875.2_RM_combined_sec_untrimmed.txt (RM35) 
Arid3a_3875.2_PWM_1.txt (K-afal) [Z-Beta = 11.3642] [Pearson Full 0.58741286, Pearson Zeroed 0.585612] 	Arid3a_3875.2_PWM_1.txt (K-afal) [Z-Beta = 46.8338] [Pearson Full 0.64550825, Pearson Zeroed 0.625027] 	Arid3a_3875.2_combined_PWM_1.txt (K-afal) 
Arid3a_3875.2_PWM_2.txt (K-afal) [Z-Beta = 20.7015] [Pearson Full 0.58741286, Pearson Zeroed 0.580595] 	Arid3a_3875.2_PWM_2.txt (K-afal) [Z-Beta = 18.8678] [Pearson Full 0.64550825, Pearson Zeroed 0.643807] 	Arid3a_3875.2_combined_PWM_2.txt (K-afal) 
Arid3a_3875.2_PWM_3.txt (K-afal) [Z-Beta = 61.8078] [Pearson Full 0.58741286, Pearson Zeroed 0.562709] 	Arid3a_3875.2_PWM_3.txt (K-afal) No PWM Available	Arid3a_3875.2_combined_PWM_3.txt (K-afal) 
Arid3a_3875.2_PWM_4.txt (K-afal) 	Arid3a_3875.2_PWM_4.txt (K-afal) No PWM Available	Arid3a_3875.2_combined_PWM_4.txt (K-afal) [Z-Beta = 72.1654] [Pearson Full 0.694418357, Pearson Zeroed 0.578223] 

<p><b>Arid3a_3875.2_PWM_5.txt (K-afal)</b></p> 	<p><b>Arid3a_3875.2_PWM_5.txt (K-afal)</b> No PWM Available</p>	<p><b>Arid3a_3875.2_combined_PWM_5.txt (K-afal)</b></p> 
<p><b>Arid3a_3875.2_PWM_6.txt (K-afal)</b></p> 	<p><b>Arid3a_3875.2_PWM_6.txt (K-afal)</b> No PWM Available</p>	<p><b>Arid3a_3875.2_combined_PWM_6.txt (K-afal)</b></p> 

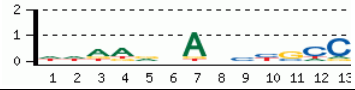

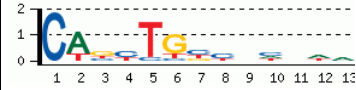

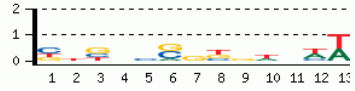



# Arid5a\_3770.2

Array Version 1	Array Version 2	Combined
<p>Arid5a_3770.2_v1_prim_auc.txt (SW)</p>	<p>Arid5a_3770.2_v2_prim_auc.txt (SW)</p>	<p>Arid5a_3770.2_combined_prim_auc.txt (SW)</p>
<p>Arid5a_3770.2_v1_prim_ic.txt (SW)</p>	<p>Arid5a_3770.2_v2_prim_ic.txt (SW)</p>	<p>Arid5a_3770.2_combined_prim_ic_trimmed.txt (SW)</p>
<p>Arid5a_3770.2_v1_prim_untrimmed.txt (SW)</p>	<p>Arid5a_3770.2_v2_prim_untrimmed.txt (SW)</p>	<p>Arid5a_3770.2_combined_prim_untrimmed.txt (SW)</p>
<p>Arid5a_3770.2_v1_sec_auc.txt (SW)</p>	<p>Arid5a_3770.2_v2_sec_auc.txt (SW)</p>	<p>Arid5a_3770.2_combined_sec_auc.txt (SW)</p>
<p>Arid5a_3770.2_v1_sec_ic.txt (SW)</p>	<p>Arid5a_3770.2_v2_sec_ic.txt (SW)</p>	<p>Arid5a_3770.2_combined_sec_ic_trimmed.txt (SW)</p>
<p>Arid5a_3770.2_v1_sec_untrimmed.txt (SW)</p>	<p>Arid5a_3770.2_v2_sec_untrimmed.txt (SW)  [Z-Beta = 128.127]  [Pearson Full 0.632801486, Pearson Zeroed NaN]</p>	<p>Arid5a_3770.2_combined_sec_untrimmed.txt (SW)</p>
<p>Arid5a_3770.2_v1_prim_trimmed.txt (RM8)  [Z-Beta = 37.514]  [Pearson Full 0.705449235, Pearson Zeroed 0.687684]</p>	<p>Arid5a_3770.2_v2_prim_trimmed.txt (RM8)</p>	<p>Arid5a_3770.2_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Arid5a_3770.2_v1_sec_trimmed.txt (RM8)</p>	<p>Arid5a_3770.2_v2_sec_trimmed.txt (RM8)</p>	<p>Arid5a_3770.2_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Arid5a_3770.2_v1_prim_untrimmed.txt (RM8)</p>	<p>Arid5a_3770.2_v2_prim_untrimmed.txt (RM8)</p>	<p>Arid5a_3770.2_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Arid5a_3770.2_v1_sec_untrimmed.txt (RM8)  [Z-Beta = 60.4034]  [Pearson Full 0.705449235, Pearson Zeroed 0.671456]</p>	<p>Arid5a_3770.2_v2_sec_untrimmed.txt (RM8)  [Z-Beta = 128.127]  [Pearson Full 0.632801486, Pearson Zeroed NaN]</p>	<p>Arid5a_3770.2_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>
<p>Arid5a_3770.2_v1_RM35_prim_trimmed.txt (RM35)</p>	<p>Arid5a_3770.2_v2_RM35_prim_trimmed.txt (RM35)</p>	<p>Arid5a_3770.2_RM_combined_prim_trimmed.txt (RM35)  [Z-Beta = 33.0028]  [Pearson Full 0.694547124, Pearson Zeroed 0.67282]</p>

 <p>Arid5a_3770.2_v1_RM35_sec_trimmed.txt (RM35) [Z-Beta = 43.7605] [Pearson Full 0.705449235, Pearson Zeroed 0.675045]</p>	 <p>Arid5a_3770.2_v2_RM35_sec_trimmed.txt (RM35)</p>	 <p>Arid5a_3770.2_RM_combined_sec_trimmed.txt (RM35) [Z-Beta = 71.6373] [Pearson Full 0.694547124, Pearson Zeroed 0.589911]</p>
 <p>Arid5a_3770.2_v1_RM35_prim_untrimmed.txt (RM35)</p>	 <p>Arid5a_3770.2_v2_RM35_prim_untrimmed.txt (RM35)</p>	 <p>Arid5a_3770.2_RM_combined_prim_untrimmed.txt (RM35) [Z-Beta = 7.99248] [Pearson Full 0.694547124, Pearson Zeroed 0.693863]</p>
 <p>Arid5a_3770.2_v1_RM35_sec_untrimmed.txt (RM35)</p>	 <p>Arid5a_3770.2_v2_RM35_sec_untrimmed.txt (RM35)</p>	 <p>Arid5a_3770.2_RM_combined_sec_untrimmed.txt (RM35)</p>
 <p>Arid5a_3770.2_PWM_1.txt (K-afal) [Z-Beta = 68.0277] [Pearson Full 0.705449235, Pearson Zeroed 0.677422]</p>	 <p>Arid5a_3770.2_PWM_1.txt (K-afal)</p>	 <p>Arid5a_3770.2_combined_PWM_1.txt (K-afal)</p>
 <p>Arid5a_3770.2_PWM_2.txt (K-afal) [Z-Beta = 29.6454] [Pearson Full 0.705449235, Pearson Zeroed 0.701346]</p>	 <p>Arid5a_3770.2_PWM_2.txt (K-afal)</p>	 <p>Arid5a_3770.2_combined_PWM_2.txt (K-afal)</p>
<p>Arid5a_3770.2_PWM_3.txt (Kafal) No PWM Available</p>	 <p>Arid5a_3770.2_PWM_3.txt (Kafal)</p>	 <p>Arid5a_3770.2_combined_PWM_3.txt (Kafal)</p>
<p>Arid5a_3770.2_PWM_4.txt (Kafal) No PWM Available</p>	 <p>Arid5a_3770.2_PWM_4.txt (Kafal)</p>	 <p>Arid5a_3770.2_combined_PWM_4.txt (Kafal) [Z-Beta = 34.2132] [Pearson Full 0.694547124, Pearson Zeroed 0.683118]</p>

# Ascl2\_2654.2

Array Version 1	Array Version 2	Combined
<p>Ascl2_2654.2_v1_prim_auc.txt (SW)  [Z-Beta = 1.71484]  [Pearson Full 0.299447021, Pearson Zeroed 0.299236]</p>	<p>Ascl2_2654.2_v2_prim_auc.txt (SW)</p>	<p>Ascl2_2654.2_combined_prim_auc.txt (SW)</p>
<p>Ascl2_2654.2_v1_prim_ic.txt (SW)  [Z-Beta = 11.8231]  [Pearson Full 0.299447021, Pearson Zeroed 0.290102]</p>	<p>Ascl2_2654.2_v2_prim_ic.txt (SW)</p>	<p>Ascl2_2654.2_combined_prim_ic_trimmed.txt (SW)  [Z-Beta = 12.9658]  [Pearson Full 0.40221127, Pearson Zeroed 0.393763]</p>
<p>Ascl2_2654.2_v1_prim_untrimmed.txt (SW)</p>	<p>Ascl2_2654.2_v2_prim_untrimmed.txt (SW)</p>	<p>Ascl2_2654.2_combined_prim_untrimmed.txt (SW)</p>
<p>Ascl2_2654.2_v1_sec_auc.txt (SW)</p>	<p>Ascl2_2654.2_v2_sec_auc.txt (SW)</p>	<p>Ascl2_2654.2_combined_sec_auc.txt (SW)</p>
<p>Ascl2_2654.2_v1_sec_ic.txt (SW)</p>	<p>Ascl2_2654.2_v2_sec_ic.txt (SW)  [Z-Beta = 3.04784]  [Pearson Full 0.289584882, Pearson Zeroed 0.283821]</p>	<p>Ascl2_2654.2_combined_sec_ic_trimmed.txt (SW)</p>
<p>Ascl2_2654.2_v1_sec_untrimmed.txt (SW)</p>	<p>Ascl2_2654.2_v2_sec_untrimmed.txt (SW)  [Z-Beta = 7.8086]  [Pearson Full 0.289584882, Pearson Zeroed 0.280066]</p>	<p>Ascl2_2654.2_combined_sec_untrimmed.txt (SW)</p>
<p>Ascl2_2654.2_v1_prim_trimmed.txt (RM8)</p>	<p>Ascl2_2654.2_v2_prim_trimmed.txt (RM8)</p>	<p>Ascl2_2654.2_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Ascl2_2654.2_v1_sec_trimmed.txt (RM8)</p>	<p>Ascl2_2654.2_v2_sec_trimmed.txt (RM8)</p>	<p>Ascl2_2654.2_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Ascl2_2654.2_v1_prim_untrimmed.txt (RM8)</p>	<p>Ascl2_2654.2_v2_prim_untrimmed.txt (RM8)</p>	<p>Ascl2_2654.2_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Ascl2_2654.2_v1_sec_untrimmed.txt (RM8)  [Z-Beta = 1.29355]  [Pearson Full 0.299447021, Pearson Zeroed 0.296796]</p>	<p>Ascl2_2654.2_v2_sec_untrimmed.txt (RM8)  [Z-Beta = 7.8086]  [Pearson Full 0.289584882, Pearson Zeroed 0.280066]</p>	<p>Ascl2_2654.2_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>

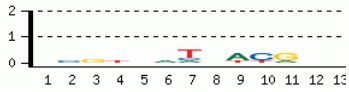
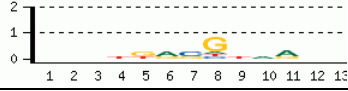
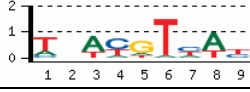


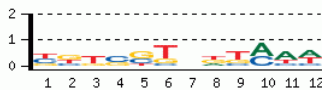







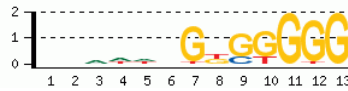
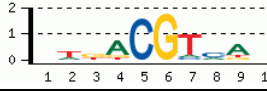
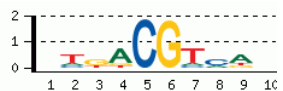



		
Ascl2_2654.2_v1_RM35_prim_trimmed.txt (RM35) 	Ascl2_2654.2_v2_RM35_prim_trimmed.txt (RM35) [Z-Beta = 30.7953] [Pearson Full 0.289584882, Pearson Zeroed 0.279035] 	Ascl2_2654.2_RM_combined_prim_trimmed.txt (RM35) 
Ascl2_2654.2_v1_RM35_sec_trimmed.txt (RM35) 	Ascl2_2654.2_v2_RM35_sec_trimmed.txt (RM35) 	Ascl2_2654.2_RM_combined_sec_trimmed.txt (RM35) 
Ascl2_2654.2_v1_RM35_prim_untrimmed.txt (RM35) [Z-Beta = 36.3958] [Pearson Full 0.299447021, Pearson Zeroed 0.287552] 	Ascl2_2654.2_v2_RM35_prim_untrimmed.txt (RM35) 	Ascl2_2654.2_RM_combined_prim_untrimmed.txt (RM35) [Z-Beta = 59.6602] [Pearson Full 0.40221127, Pearson Zeroed 0.362758] 
Ascl2_2654.2_v1_RM35_sec_untrimmed.txt (RM35) 	Ascl2_2654.2_v2_RM35_sec_untrimmed.txt (RM35) 	Ascl2_2654.2_RM_combined_sec_untrimmed.txt (RM35) 
Ascl2_2654.2_PWM_1.txt (K-afal) [Z-Beta = 34.8303] [Pearson Full 0.299447021, Pearson Zeroed 0.286021] 	Ascl2_2654.2_PWM_1.txt (K-afal) [Z-Beta = 10.3267] [Pearson Full 0.289584882, Pearson Zeroed 0.289141] 	Ascl2_2654.2_combined_PWM_1.txt (K-afal) [Z-Beta = 1.92115] [Pearson Full 0.40221127, Pearson Zeroed 0.396877] 
Ascl2_2654.2_PWM_2.txt (Kafal) No PWM Available	Ascl2_2654.2_PWM_2.txt (Kafal) [Z-Beta = 44.7955] [Pearson Full 0.289584882, Pearson Zeroed 0.283397] 	Ascl2_2654.2_combined_PWM_2.txt (Kafal) [Z-Beta = 9.6677] [Pearson Full 0.40221127, Pearson Zeroed 0.391786] 
Ascl2_2654.2_PWM_3.txt (Kafal) No PWM Available	Ascl2_2654.2_PWM_3.txt (Kafal) 	Ascl2_2654.2_combined_PWM_3.txt (Kafal) 



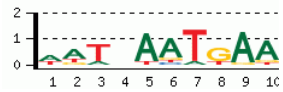
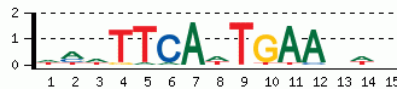
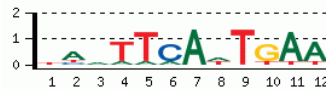
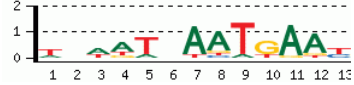
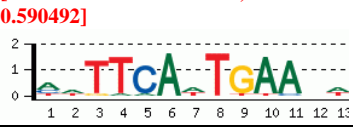
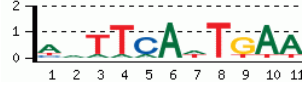

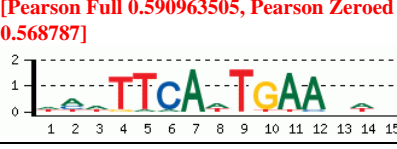
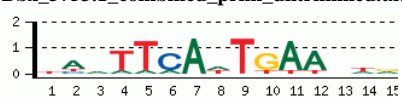

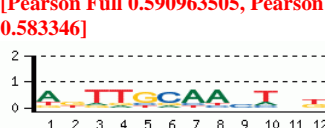
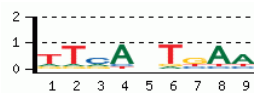
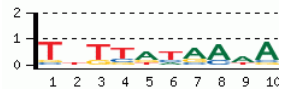
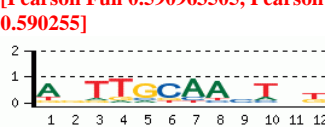


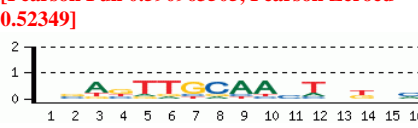



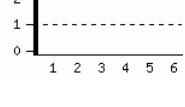

# Atf1\_3026.3

Array Version 1	Array Version 2	Combined
<p>Atf1_3026.3_v1_prim_auc.txt (SW)</p>	<p>Atf1_3026.3_v2_prim_auc.txt (SW)</p>	<p>Atf1_3026.3_combined_prim_auc.txt (SW)</p>
<p>Atf1_3026.3_v1_prim_ic.txt (SW)</p>	<p>Atf1_3026.3_v2_prim_ic.txt (SW)</p>	<p>Atf1_3026.3_combined_prim_ic_trimmed.txt (SW)  [Z-Beta = 559.84]  [Pearson Full 0.372377496, Pearson Zeroed 0.0401788]</p>
<p>Atf1_3026.3_v1_prim_untrimmed.txt (SW)</p>	<p>Atf1_3026.3_v2_prim_untrimmed.txt (SW)</p>	<p>Atf1_3026.3_combined_prim_untrimmed.txt (SW)  [Z-Beta = 32.5797]  [Pearson Full 0.372377496, Pearson Zeroed 0.340981]</p>
<p>Atf1_3026.3_v1_sec_auc.txt (SW)  [Z-Beta = 2.9934]  [Pearson Full 0.308245428, Pearson Zeroed 0.302457]</p>	<p>Atf1_3026.3_v2_sec_auc.txt (SW)</p>	<p>Atf1_3026.3_combined_sec_auc.txt (SW)</p>
<p>Atf1_3026.3_v1_sec_ic.txt (SW)</p>	<p>Atf1_3026.3_v2_sec_ic.txt (SW)</p>	<p>Atf1_3026.3_combined_sec_ic_trimmed.txt (SW)  [Z-Beta = 95.7553]  [Pearson Full 0.372377496, Pearson Zeroed 0.118114]</p>
<p>Atf1_3026.3_v1_sec_untrimmed.txt (SW)</p>	<p>Atf1_3026.3_v2_sec_untrimmed.txt (SW)  [Z-Beta = 36.5332]  [Pearson Full 0.224363076, Pearson Zeroed 0.18157]</p>	<p>Atf1_3026.3_combined_sec_untrimmed.txt (SW)  [Z-Beta = 24.8606]  [Pearson Full 0.372377496, Pearson Zeroed 0.35634]</p>
<p>Atf1_3026.3_v1_prim_trimmed.txt (RM8)</p>	<p>Atf1_3026.3_v2_prim_trimmed.txt (RM8)</p>	<p>Atf1_3026.3_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Atf1_3026.3_v1_sec_trimmed.txt (RM8)  [Z-Beta = 7.6473]  [Pearson Full 0.308245428, Pearson Zeroed 0.296094]</p>	<p>Atf1_3026.3_v2_sec_trimmed.txt (RM8)</p>	<p>Atf1_3026.3_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Atf1_3026.3_v1_prim_untrimmed.txt (RM8)  [Z-Beta = 3.37072]  [Pearson Full 0.308245428, Pearson Zeroed 0.30432]</p>	<p>Atf1_3026.3_v2_prim_untrimmed.txt (RM8)</p>	<p>Atf1_3026.3_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>



<p>Atf1_3026.3_v1_sec_untrimmed.txt (RM8)</p> 	<p>Atf1_3026.3_v2_sec_untrimmed.txt (RM8)  [Z-Beta = 36.5332]  [Pearson Full 0.224363076, Pearson Zeroed 0.18157]</p> 	<p>Atf1_3026.3_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>
<p>Atf1_3026.3_v1_RM35_prim_trimmed.txt (RM35)  [Z-Beta = 19.2843]  [Pearson Full 0.308245428, Pearson Zeroed 0.311416]</p> 	<p>Atf1_3026.3_v2_RM35_prim_trimmed.txt (RM35)</p> 	<p>Atf1_3026.3_RM_combined_prim_trimmed.txt (RM35)  [Z-Beta = 27.7694]  [Pearson Full 0.372377496, Pearson Zeroed 0.363259]</p> 
<p>Atf1_3026.3_v1_RM35_sec_trimmed.txt (RM35)</p> 	<p>Atf1_3026.3_v2_RM35_sec_trimmed.txt (RM35)</p> 	<p>Atf1_3026.3_RM_combined_sec_trimmed.txt (RM35)  [Z-Beta = 7.47331]  [Pearson Full 0.372377496, Pearson Zeroed 0.370742]</p> 
<p>Atf1_3026.3_v1_RM35_prim_untrimmed.txt (RM35)  [Z-Beta = 47.8804]  [Pearson Full 0.308245428, Pearson Zeroed 0.310186]</p> 	<p>Atf1_3026.3_v2_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Atf1_3026.3_RM_combined_prim_untrimmed.txt (RM35)  [Z-Beta = 40.6742]  [Pearson Full 0.372377496, Pearson Zeroed 0.348854]</p> 
<p>Atf1_3026.3_v1_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Atf1_3026.3_v2_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Atf1_3026.3_RM_combined_sec_untrimmed.txt (RM35)  [Z-Beta = 5.42783]  [Pearson Full 0.372377496, Pearson Zeroed 0.371464]</p> 
<p>Atf1_3026.3_PWM_1.txt (K-afal)  [Z-Beta = 4.72713]  [Pearson Full 0.308245428, Pearson Zeroed 0.305351]</p> 	<p>Atf1_3026.3_PWM_1.txt (K-afal)</p> 	<p>Atf1_3026.3_combined_PWM_1.txt (K-afal)  [Z-Beta = 29.1447]  [Pearson Full 0.372377496, Pearson Zeroed 0.354231]</p> 
<p>Atf1_3026.3_PWM_2.txt (Kafal)  No PWM Available</p>	<p>Atf1_3026.3_PWM_2.txt (Kafal)</p> 	<p>Atf1_3026.3_combined_PWM_2.txt (Kafal)  No PWM Available</p>
<p>Atf1_3026.3_PWM_3.txt (Kafal)  No PWM Available</p>	<p>Atf1_3026.3_PWM_3.txt (Kafal)</p> 	<p>Atf1_3026.3_combined_PWM_3.txt (Kafal)  No PWM Available</p>
<p>Atf1_3026.3_PWM_4.txt (Kafal)  No PWM Available</p>	<p>Atf1_3026.3_PWM_4.txt (Kafal)  [Z-Beta = 10.6944]  [Pearson Full 0.224363076, Pearson Zeroed 0.211838]</p> 	<p>Atf1_3026.3_combined_PWM_4.txt (Kafal)  No PWM Available</p>

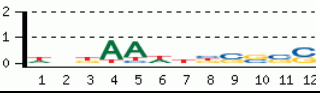
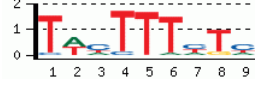


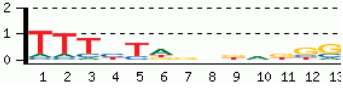
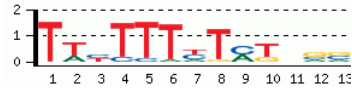
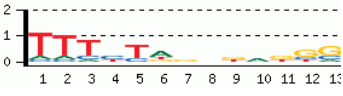
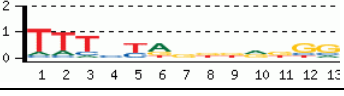



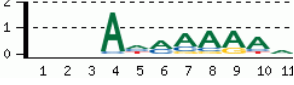
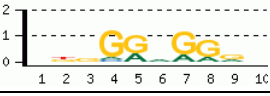
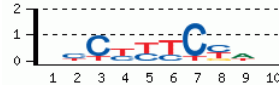





# Bbx\_3753.1

Array Version 1	Array Version 2	Combined
<b>Bbx_3753.1_v1_prim_auc.txt (SW)</b> 	<b>Bbx_3753.1_v2_prim_auc.txt (SW)</b> 	<b>Bbx_3753.1_combined_prim_auc.txt (SW)</b> 
<b>Bbx_3753.1_v1_prim_ic.txt (SW)</b> 	<b>Bbx_3753.1_v2_prim_ic.txt (SW)</b> <b>[Z-Beta = 3.99555]</b> <b>[Pearson Full 0.590963505, Pearson Zeroed 0.590492]</b> 	<b>Bbx_3753.1_combined_prim_ic_trimmed.txt (SW)</b> 
<b>Bbx_3753.1_v1_prim_untrimmed.txt (SW)</b> 	<b>Bbx_3753.1_v2_prim_untrimmed.txt (SW)</b> <b>[Z-Beta = 55.8149]</b> <b>[Pearson Full 0.590963505, Pearson Zeroed 0.568787]</b> 	<b>Bbx_3753.1_combined_prim_untrimmed.txt (SW)</b> 
<b>Bbx_3753.1_v1_sec_auc.txt (SW)</b> 	<b>Bbx_3753.1_v2_sec_auc.txt (SW)</b> <b>[Z-Beta = 16.1466]</b> <b>[Pearson Full 0.590963505, Pearson Zeroed 0.583346]</b> 	<b>Bbx_3753.1_combined_sec_auc.txt (SW)</b> 
<b>Bbx_3753.1_v1_sec_ic.txt (SW)</b> 	<b>Bbx_3753.1_v2_sec_ic.txt (SW)</b> <b>[Z-Beta = -4.7529]</b> <b>[Pearson Full 0.590963505, Pearson Zeroed 0.590255]</b> 	<b>Bbx_3753.1_combined_sec_ic_trimmed.txt (SW)</b> 
<b>Bbx_3753.1_v1_sec_untrimmed.txt (SW)</b> 	<b>Bbx_3753.1_v2_sec_untrimmed.txt (SW)</b> <b>[Z-Beta = 78.9233]</b> <b>[Pearson Full 0.590963505, Pearson Zeroed 0.52349]</b> 	<b>Bbx_3753.1_combined_sec_untrimmed.txt (SW)</b> 
<b>Bbx_3753.1_v1_prim_trimmed.txt (RM8)</b> 	<b>Bbx_3753.1_v2_prim_trimmed.txt (RM8)</b> <b>[Z-Beta = 6.52499]</b> <b>[Pearson Full 0.590963505, Pearson Zeroed 0.590549]</b> 	<b>Bbx_3753.1_RM8_combined_prim_trimmed.txt (RM8)</b> No PWM Available
<b>Bbx_3753.1_v1_sec_trimmed.txt (RM8)</b> 	<b>Bbx_3753.1_v2_sec_trimmed.txt (RM8)</b> <b>[Z-Beta = -4.43884]</b> <b>[Pearson Full 0.590963505, Pearson Zeroed 0.590772]</b> 	<b>Bbx_3753.1_RM8_combined_sec_trimmed.txt (RM8)</b> No PWM Available
<b>Bbx_3753.1_v1_prim_untrimmed.txt (RM8)</b> <b>[Z-Beta = 26.6552]</b> <b>[Pearson Full 0.660751626, Pearson Zeroed 0.660751626]</b>	<b>Bbx_3753.1_v2_prim_untrimmed.txt (RM8)</b> <b>[Z-Beta = 55.8149]</b> <b>[Pearson Full 0.590963505, Pearson Zeroed 0.590963505]</b>	<b>Bbx_3753.1_RM8_combined_prim_untrimmed.txt (RM8)</b> No PWM Available

<p>0.639151]</p>	<p>0.568787]</p>	
<p><b>Bbx_3753.1_v1_sec_untrimmed.txt (RM8)</b>  [Z-Beta = 101.579]  [Pearson Full 0.660751626, Pearson Zeroed 0.535889]</p>	<p><b>Bbx_3753.1_v2_sec_untrimmed.txt (RM8)</b>  [Z-Beta = 78.9233]  [Pearson Full 0.590963505, Pearson Zeroed 0.52349]</p>	<p><b>Bbx_3753.1_RM8_combined_sec_untrimmed.txt (RM8)</b>  No PWM Available</p>
<p><b>Bbx_3753.1_v1_RM35_prim_trimmed.txt (RM35)</b></p>	<p><b>Bbx_3753.1_v2_RM35_prim_trimmed.txt (RM35)</b>  [Z-Beta = 4.30751]  [Pearson Full 0.590963505, Pearson Zeroed 0.59057]</p>	<p><b>Bbx_3753.1_RM_combined_prim_trimmed.txt (RM35)</b>  [Z-Beta = 41.7835]  [Pearson Full 0.426470844, Pearson Zeroed 0.441639]</p>
<p><b>Bbx_3753.1_v1_RM35_sec_trimmed.txt (RM35)</b></p>	<p><b>Bbx_3753.1_v2_RM35_sec_trimmed.txt (RM35)</b>  [Z-Beta = 1.30139]  [Pearson Full 0.590963505, Pearson Zeroed 0.590909]</p>	<p><b>Bbx_3753.1_RM_combined_sec_trimmed.txt (RM35)</b></p>
<p><b>Bbx_3753.1_v1_RM35_prim_untrimmed.txt (RM35)</b></p>	<p><b>Bbx_3753.1_v2_RM35_prim_untrimmed.txt (RM35)</b>  [Z-Beta = 2.69099]  [Pearson Full 0.590963505, Pearson Zeroed 0.590808]</p>	<p><b>Bbx_3753.1_RM_combined_prim_untrimmed.txt (RM35)</b>  [Z-Beta = 6.66693]  [Pearson Full 0.426470844, Pearson Zeroed 0.428838]</p>
<p><b>Bbx_3753.1_v1_RM35_sec_untrimmed.txt (RM35)</b></p>	<p><b>Bbx_3753.1_v2_RM35_sec_untrimmed.txt (RM35)</b>  [Z-Beta = 13.582]  [Pearson Full 0.590963505, Pearson Zeroed 0.58571]</p>	<p><b>Bbx_3753.1_RM_combined_sec_untrimmed.txt (RM35)</b></p>
<p><b>Bbx_3753.1_PWM_1.txt (K-afal)</b></p>	<p><b>Bbx_3753.1_PWM_1.txt (K-afal)</b>  [Z-Beta = 5.17597]  [Pearson Full 0.590963505, Pearson Zeroed 0.59051]</p>	<p><b>Bbx_3753.1_combined_PWM_1.txt (K-afal)</b>  [Z-Beta = 6.49389]  [Pearson Full 0.426470844, Pearson Zeroed 0.396905]</p>
<p><b>Bbx_3753.1_PWM_2.txt (K-afal)</b></p>	<p><b>Bbx_3753.1_PWM_2.txt (K-afal)</b>  No PWM Available</p>	<p><b>Bbx_3753.1_combined_PWM_2.txt (K-afal)</b></p>

# Bcl6b\_0961.2

Array Version 1	Array Version 2	Combined
<p>Bcl6b_0961.2_v1_prim_auc.txt (SW)</p>	<p>Bcl6b_0961.2_v2_prim_auc.txt (SW)</p>	<p>Bcl6b_0961.2_combined_prim_auc.txt (SW)</p>
<p>Bcl6b_0961.2_v1_prim_ic.txt (SW)</p>	<p>Bcl6b_0961.2_v2_prim_ic.txt (SW)</p>	<p>Bcl6b_0961.2_combined_prim_ic_trimmed.txt (SW)</p>
<p>Bcl6b_0961.2_v1_prim_untrimmed.txt (SW)  [Z-Beta = 32.8491]  [Pearson Full 0.302327004, Pearson Zeroed 0.278309]</p>	<p>Bcl6b_0961.2_v2_prim_untrimmed.txt (SW)</p>	<p>Bcl6b_0961.2_combined_prim_untrimmed.txt (SW)  [Z-Beta = 17.321]  [Pearson Full 0.271686239, Pearson Zeroed 0.223363]</p>
<p>Bcl6b_0961.2_v1_sec_auc.txt (SW)  [Z-Beta = 2.66645]  [Pearson Full 0.302327004, Pearson Zeroed 0.301736]</p>	<p>Bcl6b_0961.2_v2_sec_auc.txt (SW)</p>	<p>Bcl6b_0961.2_combined_sec_auc.txt (SW)</p>
<p>Bcl6b_0961.2_v1_sec_ic.txt (SW)</p>	<p>Bcl6b_0961.2_v2_sec_ic.txt (SW)</p>	<p>Bcl6b_0961.2_combined_sec_ic_trimmed.txt (SW)</p>
<p>Bcl6b_0961.2_v1_sec_untrimmed.txt (SW)  [Z-Beta = 2.93209]  [Pearson Full 0.302327004, Pearson Zeroed 0.301701]</p>	<p>Bcl6b_0961.2_v2_sec_untrimmed.txt (SW)</p>	<p>Bcl6b_0961.2_combined_sec_untrimmed.txt (SW)</p>
<p>Bcl6b_0961.2_v1_prim_trimmed.txt (RM8)</p>	<p>Bcl6b_0961.2_v2_prim_trimmed.txt (RM8)</p>	<p>Bcl6b_0961.2_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Bcl6b_0961.2_v1_sec_trimmed.txt (RM8)  [Z-Beta = 39.5216]  [Pearson Full 0.302327004, Pearson Zeroed 0.267562]</p>	<p>Bcl6b_0961.2_v2_sec_trimmed.txt (RM8)</p>	<p>Bcl6b_0961.2_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Bcl6b_0961.2_v1_prim_untrimmed.txt (RM8)</p>	<p>Bcl6b_0961.2_v2_prim_untrimmed.txt (RM8)</p>	<p>Bcl6b_0961.2_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Bcl6b_0961.2_v1_sec_untrimmed.txt (RM8)  [Z-Beta = 8.50513]  [Pearson Full 0.302327004, Pearson Zeroed 0.300343]</p>	<p>Bcl6b_0961.2_v2_sec_untrimmed.txt (RM8)</p>	<p>Bcl6b_0961.2_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>

		
<b>Bcl6b_0961.2_v1_RM35_prim_trimmed.txt (RM35)</b> [Z-Beta = 5.20021] [Pearson Full 0.302327004, Pearson Zeroed 0.302135]	<b>Bcl6b_0961.2_v2_RM35_prim_trimmed.txt (RM35)</b> 	<b>Bcl6b_0961.2_RM_combined_prim_trimmed.txt (RM35)</b> [Z-Beta = 47.2025] [Pearson Full 0.271686239, Pearson Zeroed 0.126541]
	<b>Bcl6b_0961.2_v2_RM35_sec_trimmed.txt (RM35)</b> 	<b>Bcl6b_0961.2_RM_combined_sec_trimmed.txt (RM35)</b> 
<b>Bcl6b_0961.2_v1_RM35_sec_trimmed.txt (RM35)</b>	<b>Bcl6b_0961.2_v2_RM35_sec_trimmed.txt (RM35)</b>	<b>Bcl6b_0961.2_RM_combined_sec_trimmed.txt (RM35)</b>
<b>Bcl6b_0961.2_v1_RM35_prim_untrimmed.txt (RM35)</b> [Z-Beta = 38.8647] [Pearson Full 0.302327004, Pearson Zeroed 0.281821]	<b>Bcl6b_0961.2_v2_RM35_prim_untrimmed.txt (RM35)</b> 	<b>Bcl6b_0961.2_RM_combined_prim_untrimmed.txt (RM35)</b> 
	<b>Bcl6b_0961.2_v2_RM35_sec_untrimmed.txt (RM35)</b> 	<b>Bcl6b_0961.2_RM_combined_sec_untrimmed.txt (RM35)</b> 
<b>Bcl6b_0961.2_v1_RM35_sec_untrimmed.txt (RM35)</b>	<b>Bcl6b_0961.2_v2_RM35_sec_untrimmed.txt (RM35)</b>	<b>Bcl6b_0961.2_RM_combined_sec_untrimmed.txt (RM35)</b>
<b>Bcl6b_0961.2_PWM_1.txt (K-afal)</b> [Z-Beta = 3.53257] [Pearson Full 0.302327004, Pearson Zeroed 0.301676]	<b>Bcl6b_0961.2_PWM_1.txt (K-afal)</b> 	<b>Bcl6b_0961.2_combined_PWM_1.txt (K-afal)</b> 
	<b>Bcl6b_0961.2_PWM_2.txt (K-afal)</b> 	<b>Bcl6b_0961.2_combined_PWM_2.txt (K-afal)</b> 
<b>Bcl6b_0961.2_PWM_2.txt (K-afal)</b>	<b>Bcl6b_0961.2_PWM_3.txt (K-afal)</b> 	<b>Bcl6b_0961.2_combined_PWM_3.txt (K-afal)</b> 
<b>Bcl6b_0961.2_PWM_3.txt (K-afal)</b> [Z-Beta = 37.3493] [Pearson Full 0.302327004, Pearson Zeroed 0.266085]	<b>Bcl6b_0961.2_PWM_4.txt (Kafal)</b> [Z-Beta = 35.2883] [Pearson Full 0.170944378, Pearson Zeroed NaN]	<b>Bcl6b_0961.2_combined_PWM_4.txt (Kafal)</b> 
<b>Bcl6b_0961.2_PWM_4.txt (Kafal)</b> No PWM Available		

# Bhlhb2\_1274.3

Array Version 1	Array Version 2	Combined
<p>Bhlhb2_1274.3_v1_prim_auc.txt (SW)  [Z-Beta = 118.986]  [Pearson Full 0.808073294, Pearson Zeroed 0.808681]</p>	<p>Bhlhb2_1274.3_v2_prim_auc.txt (SW)  [Z-Beta = 684.357]  [Pearson Full 0.802871633, Pearson Zeroed 0.623879]</p>	<p>Bhlhb2_1274.3_combined_prim_auc.txt (SW)  No PWM Available</p>
<p>Bhlhb2_1274.3_v1_prim_ic.txt (SW)  [Z-Beta = 256.068]  [Pearson Full 0.808073294, Pearson Zeroed 0.779633]</p>	<p>Bhlhb2_1274.3_v2_prim_ic.txt (SW)</p>	<p>Bhlhb2_1274.3_combined_prim_ic_trimmed.txt (SW)  [Z-Beta = 344.463]  [Pearson Full 0.825935229, Pearson Zeroed 0.773169]</p>
<p>Bhlhb2_1274.3_v1_prim_untrimmed.txt (SW)</p>	<p>Bhlhb2_1274.3_v2_prim_untrimmed.txt (SW)</p>	<p>Bhlhb2_1274.3_combined_prim_untrimmed.txt (SW)</p>
<p>Bhlhb2_1274.3_v1_sec_auc.txt (SW)</p>	<p>Bhlhb2_1274.3_v2_sec_auc.txt (SW)</p>	<p>Bhlhb2_1274.3_combined_sec_auc.txt (SW)</p>
<p>Bhlhb2_1274.3_v1_sec_ic.txt (SW)</p>	<p>Bhlhb2_1274.3_v2_sec_ic.txt (SW)</p>	<p>Bhlhb2_1274.3_combined_sec_ic_trimmed.txt (SW)</p>
<p>Bhlhb2_1274.3_v1_sec_untrimmed.txt (SW)</p>	<p>Bhlhb2_1274.3_v2_sec_untrimmed.txt (SW)</p>	<p>Bhlhb2_1274.3_combined_sec_untrimmed.txt (SW)</p>
<p>Bhlhb2_1274.3_v1_prim_trimmed.txt (RM8)</p>	<p>Bhlhb2_1274.3_v2_prim_trimmed.txt (RM8)</p>	<p>Bhlhb2_1274.3_RM8_combined_prim_trimmed.txt  (RM8)  No PWM Available</p>
<p>Bhlhb2_1274.3_v1_sec_trimmed.txt (RM8)</p>	<p>Bhlhb2_1274.3_v2_sec_trimmed.txt (RM8)</p>	<p>Bhlhb2_1274.3_RM8_combined_sec_trimmed.txt  (RM8)  No PWM Available</p>
<p>Bhlhb2_1274.3_v1_prim_untrimmed.txt (RM8)  [Z-Beta = 31.359]  [Pearson Full 0.808073294, Pearson Zeroed 0.804479]</p>	<p>Bhlhb2_1274.3_v2_prim_untrimmed.txt (RM8)</p>	<p>Bhlhb2_1274.3_RM8_combined_prim_untrimmed.txt  (RM8)  No PWM Available</p>
<p>Bhlhb2_1274.3_v1_sec_untrimmed.txt (RM8)</p>	<p>Bhlhb2_1274.3_v2_sec_untrimmed.txt (RM8)</p>	<p>Bhlhb2_1274.3_RM8_combined_sec_untrimmed.txt  (RM8)  No PWM Available</p>
<p>Bhlhb2_1274.3_v1_RM35_prim_trimmed.txt  (RM35)</p>	<p>Bhlhb2_1274.3_v2_RM35_prim_trimmed.txt  (RM35)</p>	<p>Bhlhb2_1274.3_RM_combined_prim_trimmed.txt  (RM35)</p>














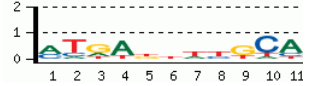


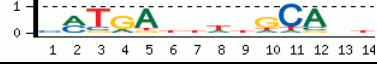







		
<p>Bhlhb2_1274.3_v1_RM35_sec_trimmed.txt (RM35)</p> 	<p>Bhlhb2_1274.3_v2_RM35_sec_trimmed.txt (RM35)</p> 	<p>Bhlhb2_1274.3_RM_combined_sec_trimmed.txt (RM35)</p> 
<p>Bhlhb2_1274.3_v1_RM35_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = 841.434] [Pearson Full 0.808073294, Pearson Zeroed 0.74174]</p> 	<p>Bhlhb2_1274.3_v2_RM35_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = 664.511] [Pearson Full 0.802871633, Pearson Zeroed 0.630301]</p> 	<p>Bhlhb2_1274.3_RM_combined_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = 863.989] [Pearson Full 0.825935229, Pearson Zeroed 0.668655]</p> 
<p>Bhlhb2_1274.3_v1_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Bhlhb2_1274.3_v2_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Bhlhb2_1274.3_RM_combined_sec_untrimmed.txt (RM35)</p> 
<p>Bhlhb2_1274.3_PWM_10.txt (K-afal)</p> 	<p>Bhlhb2_1274.3_PWM_10.txt (K-afal)</p> <p>No PWM Available</p>	<p>Bhlhb2_1274.3_combined_PWM_10.txt (K-afal)</p> <p>No PWM Available</p>
<p>Bhlhb2_1274.3_PWM_11.txt (K-afal)</p> 	<p>Bhlhb2_1274.3_PWM_11.txt (K-afal)</p> <p>No PWM Available</p>	<p>Bhlhb2_1274.3_combined_PWM_11.txt (K-afal)</p> <p>No PWM Available</p>
<p>Bhlhb2_1274.3_PWM_12.txt (K-afal)</p> 	<p>Bhlhb2_1274.3_PWM_12.txt (K-afal)</p> <p>No PWM Available</p>	<p>Bhlhb2_1274.3_combined_PWM_12.txt (K-afal)</p> <p>No PWM Available</p>
<p>Bhlhb2_1274.3_PWM_13.txt (K-afal)</p> 	<p>Bhlhb2_1274.3_PWM_13.txt (K-afal)</p> <p>No PWM Available</p>	<p>Bhlhb2_1274.3_combined_PWM_13.txt (K-afal)</p> <p>No PWM Available</p>
<p>Bhlhb2_1274.3_PWM_14.txt (K-afal)</p> 	<p>Bhlhb2_1274.3_PWM_14.txt (K-afal)</p> <p>No PWM Available</p>	<p>Bhlhb2_1274.3_combined_PWM_14.txt (K-afal)</p> <p>No PWM Available</p>
<p>Bhlhb2_1274.3_PWM_15.txt (K-afal)</p> 	<p>Bhlhb2_1274.3_PWM_15.txt (K-afal)</p> <p>No PWM Available</p>	<p>Bhlhb2_1274.3_combined_PWM_15.txt (K-afal)</p> <p>No PWM Available</p>
<p>Bhlhb2_1274.3_PWM_16.txt (K-afal)</p> 	<p>Bhlhb2_1274.3_PWM_16.txt (K-afal)</p> <p>No PWM Available</p>	<p>Bhlhb2_1274.3_combined_PWM_16.txt (K-afal)</p> <p>No PWM Available</p>
<p>Bhlhb2_1274.3_PWM_17.txt (K-afal)</p> <p>[Z-Beta = 38.604] [Pearson Full 0.808073294, Pearson Zeroed 0.805317]</p> 	<p>Bhlhb2_1274.3_PWM_17.txt (K-afal)</p> <p>No PWM Available</p>	<p>Bhlhb2_1274.3_combined_PWM_17.txt (K-afal)</p> <p>No PWM Available</p>

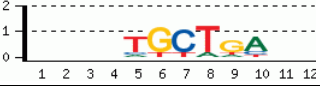





<p><b>Bhlhb2_1274.3_PWM_1.txt (K-afal)</b></p>	<p><b>Bhlhb2_1274.3_PWM_1.txt (K-afal)</b></p>	<p><b>Bhlhb2_1274.3_combined_PWM_1.txt (K-afal)</b></p>
<p><b>Bhlhb2_1274.3_PWM_2.txt (K-afal)</b></p>	<p><b>Bhlhb2_1274.3_PWM_2.txt (K-afal)</b></p>	<p><b>Bhlhb2_1274.3_combined_PWM_2.txt (K-afal)</b></p>
<p><b>Bhlhb2_1274.3_PWM_3.txt (K-afal)</b></p>	<p><b>Bhlhb2_1274.3_PWM_3.txt (K-afal)</b></p>	<p><b>Bhlhb2_1274.3_combined_PWM_3.txt (K-afal)</b></p>
<p><b>Bhlhb2_1274.3_PWM_4.txt (K-afal)</b></p>	<p><b>Bhlhb2_1274.3_PWM_4.txt (K-afal)</b></p>	<p><b>Bhlhb2_1274.3_combined_PWM_4.txt (K-afal)</b></p>
<p><b>Bhlhb2_1274.3_PWM_5.txt (K-afal)</b></p>	<p><b>Bhlhb2_1274.3_PWM_5.txt (K-afal)</b></p>	<p><b>Bhlhb2_1274.3_combined_PWM_5.txt (K-afal)</b></p>
<p><b>Bhlhb2_1274.3_PWM_6.txt (K-afal)</b></p>	<p><b>Bhlhb2_1274.3_PWM_6.txt (K-afal)</b></p>	<p><b>Bhlhb2_1274.3_combined_PWM_6.txt (K-afal)</b></p>
<p><b>Bhlhb2_1274.3_PWM_7.txt (K-afal)</b></p>	<p><b>Bhlhb2_1274.3_PWM_7.txt (K-afal)</b></p>	<p><b>Bhlhb2_1274.3_combined_PWM_7.txt (K-afal)</b> No PWM Available</p>
<p><b>Bhlhb2_1274.3_PWM_8.txt (K-afal)</b></p>	<p><b>Bhlhb2_1274.3_PWM_8.txt (K-afal)</b></p>	<p><b>Bhlhb2_1274.3_combined_PWM_8.txt (K-afal)</b> No PWM Available</p>
<p><b>Bhlhb2_1274.3_PWM_9.txt (K-afal)</b></p>	<p><b>Bhlhb2_1274.3_PWM_9.txt (K-afal)</b> No PWM Available</p>	<p><b>Bhlhb2_1274.3_combined_PWM_9.txt (K-afal)</b> No PWM Available</p>










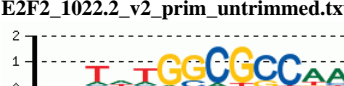


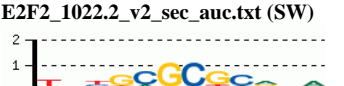

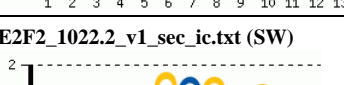
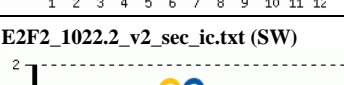
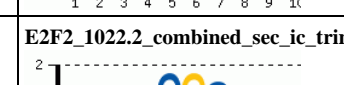

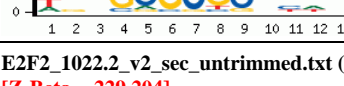
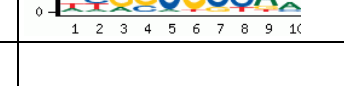


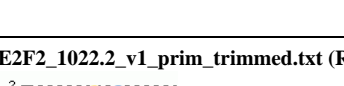
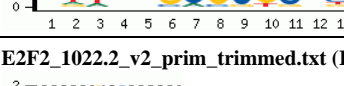

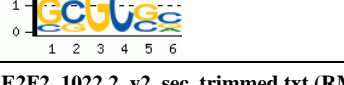
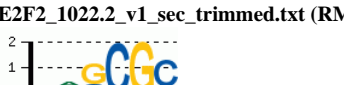
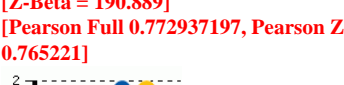
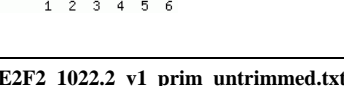
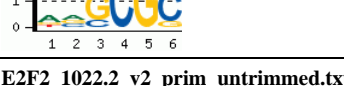
# Cic\_3454.1




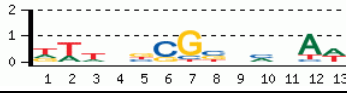
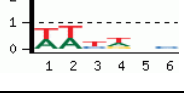
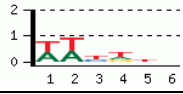



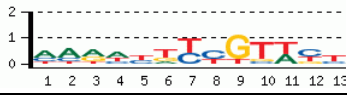
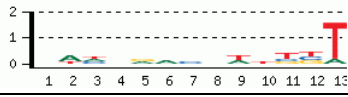
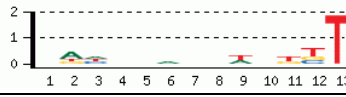




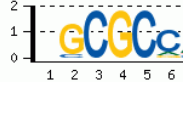
Array Version 1	Array Version 2	Combined
<p>Cic_3454.1_v1_prim_auc.txt (SW)  [Z-Beta = 9.7165]  [Pearson Full 0.448169479, Pearson Zeroed 0.439511]</p> 	<p>Cic_3454.1_v2_prim_auc.txt (SW)  </p>	<p>Cic_3454.1_combined_prim_auc.txt (SW)  </p>
<p>Cic_3454.1_v1_prim_ic.txt (SW)  </p>	<p>Cic_3454.1_v2_prim_ic.txt (SW)  [Z-Beta = -198.41]  [Pearson Full 0.616714703, Pearson Zeroed 0.503536]  </p>	<p>Cic_3454.1_combined_prim_ic_trimmed.txt (SW)  </p>
<p>Cic_3454.1_v1_prim_untrimmed.txt (SW)  </p>	<p>Cic_3454.1_v2_prim_untrimmed.txt (SW)  </p>	<p>Cic_3454.1_combined_prim_untrimmed.txt (SW)  </p>
<p>Cic_3454.1_v1_sec_auc.txt (SW)  [Z-Beta = 5.38863]  [Pearson Full 0.448169479, Pearson Zeroed 0.438856]  </p>	<p>Cic_3454.1_v2_sec_auc.txt (SW)  [Z-Beta = 5.87593]  [Pearson Full 0.616714703, Pearson Zeroed 0.61621]  </p>	<p>Cic_3454.1_combined_sec_auc.txt (SW)  </p>
<p>Cic_3454.1_v1_sec_ic.txt (SW)  </p>	<p>Cic_3454.1_v2_sec_ic.txt (SW)  [Z-Beta = -22.7521]  [Pearson Full 0.616714703, Pearson Zeroed 0.608792]  </p>	<p>Cic_3454.1_combined_sec_ic_trimmed.txt (SW)  </p>
<p>Cic_3454.1_v1_sec_untrimmed.txt (SW)  [Z-Beta = 0.988592]  [Pearson Full 0.448169479, Pearson Zeroed 0.446496]  </p>	<p>Cic_3454.1_v2_sec_untrimmed.txt (SW)  [Z-Beta = 33.6868]  [Pearson Full 0.616714703, Pearson Zeroed 0.603939]  </p>	<p>Cic_3454.1_combined_sec_untrimmed.txt (SW)  </p>
<p>Cic_3454.1_v1_prim_trimmed.txt (RM8)  </p>	<p>Cic_3454.1_v2_prim_trimmed.txt (RM8)  [Z-Beta = 5.99253]  [Pearson Full 0.616714703, Pearson Zeroed 0.616301]  </p>	<p>Cic_3454.1_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Cic_3454.1_v1_sec_trimmed.txt (RM8)  </p>	<p>Cic_3454.1_v2_sec_trimmed.txt (RM8)  [Z-Beta = 0.609012]  [Pearson Full 0.616714703, Pearson Zeroed 0.616714]  </p>	<p>Cic_3454.1_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Cic_3454.1_v1_prim_untrimmed.txt (RM8)  [Z-Beta = 17.9479]  [Pearson Full 0.448169479, Pearson Zeroed]</p>	<p>Cic_3454.1_v2_prim_untrimmed.txt (RM8)</p>	<p>Cic_3454.1_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>

<p>0.433894]</p>		
<p>Cic_3454.1_v1_sec_untrimmed.txt (RM8) [Z-Beta = 1.78021] [Pearson Full 0.448169479, Pearson Zeroed 0.446568]</p>	<p>Cic_3454.1_v2_sec_untrimmed.txt (RM8) [Z-Beta = 33.6868] [Pearson Full 0.616714703, Pearson Zeroed 0.603939]</p>	<p>Cic_3454.1_RM8_combined_sec_untrimmed.txt (RM8) No PWM Available</p>
<p>Cic_3454.1_v1_RM35_prim_trimmed.txt (RM35)</p>	<p>Cic_3454.1_v2_RM35_prim_trimmed.txt (RM35) [Z-Beta = -91.6643] [Pearson Full 0.616714703, Pearson Zeroed 0.567603]</p>	<p>Cic_3454.1_RM_combined_prim_trimmed.txt (RM35)</p>
<p>Cic_3454.1_v1_RM35_sec_trimmed.txt (RM35) [Z-Beta = 21.3475] [Pearson Full 0.448169479, Pearson Zeroed 0.410033]</p>	<p>Cic_3454.1_v2_RM35_sec_trimmed.txt (RM35) [Z-Beta = 14.1831] [Pearson Full 0.616714703, Pearson Zeroed 0.613836]</p>	<p>Cic_3454.1_RM_combined_sec_trimmed.txt (RM35) [Z-Beta = 30.4206] [Pearson Full 0.604802188, Pearson Zeroed 0.545944]</p>
<p>Cic_3454.1_v1_RM35_prim_untrimmed.txt (RM35) [Z-Beta = 39.5642] [Pearson Full 0.448169479, Pearson Zeroed 0.437892]</p>	<p>Cic_3454.1_v2_RM35_prim_untrimmed.txt (RM35) [Z-Beta = 102.566] [Pearson Full 0.616714703, Pearson Zeroed 0.456199]</p>	<p>Cic_3454.1_RM_combined_prim_untrimmed.txt (RM35) [Z-Beta = 40.4071] [Pearson Full 0.604802188, Pearson Zeroed 0.558354]</p>
<p>Cic_3454.1_v1_RM35_sec_untrimmed.txt (RM35)</p>	<p>Cic_3454.1_v2_RM35_sec_untrimmed.txt (RM35) [Z-Beta = 29.5247] [Pearson Full 0.616714703, Pearson Zeroed 0.605303]</p>	<p>Cic_3454.1_RM_combined_sec_untrimmed.txt (RM35) [Z-Beta = 15.1286] [Pearson Full 0.604802188, Pearson Zeroed 0.58672]</p>
<p>Cic_3454.1_PWM_1.txt (K-afal)</p>	<p>Cic_3454.1_PWM_1.txt (K-afal) [Z-Beta = 46.7471] [Pearson Full 0.616714703, Pearson Zeroed 0.585083]</p>	<p>Cic_3454.1_combined_PWM_1.txt (K-afal) [Z-Beta = 54.1563] [Pearson Full 0.604802188, Pearson Zeroed 0.548205]</p>
<p>Cic_3454.1_PWM_2.txt (K-afal) [Z-Beta = 10.3427] [Pearson Full 0.448169479, Pearson Zeroed 0.447161]</p>	<p>Cic_3454.1_PWM_2.txt (K-afal) [Z-Beta = 69.0937] [Pearson Full 0.616714703, Pearson Zeroed 0.567115]</p>	<p>Cic_3454.1_combined_PWM_2.txt (K-afal) [Z-Beta = 16.9576] [Pearson Full 0.604802188, Pearson Zeroed 0.613243]</p>
<p>Cic_3454.1_PWM_3.txt (K-afal) [Z-Beta = 45.4935] [Pearson Full 0.448169479, Pearson Zeroed 0.435774]</p>	<p>Cic_3454.1_PWM_3.txt (K-afal) [Z-Beta = 8.67247] [Pearson Full 0.616714703, Pearson Zeroed 0.616206]</p>	<p>Cic_3454.1_combined_PWM_3.txt (K-afal)</p>






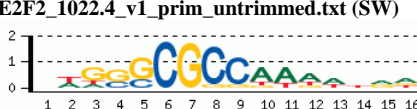
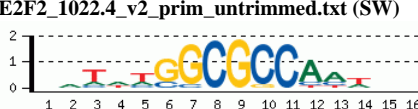
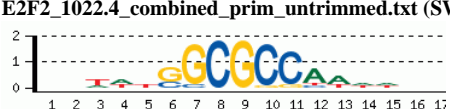

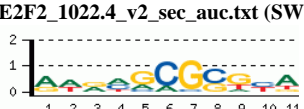
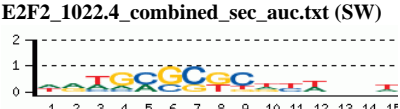
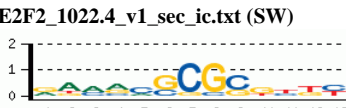
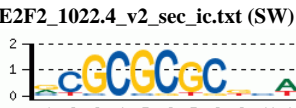
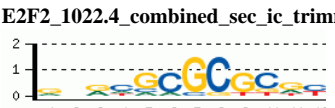




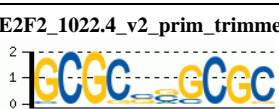
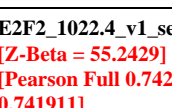
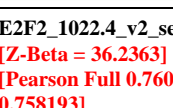

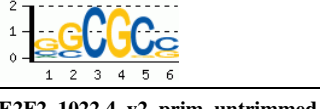


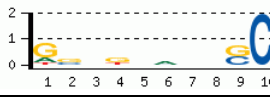

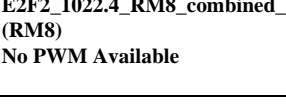
		
<p>Cic_3454.1_PWM_4.txt (Kafal) No PWM Available</p>	<p>Cic_3454.1_PWM_4.txt (Kafal) [Z-Beta = 5.18509] [Pearson Full 0.616714703, Pearson Zeroed 0.61648]</p> 	<p>Cic_3454.1_combined_PWM_4.txt (Kafal) No PWM Available</p>
<p>Cic_3454.1_PWM_5.txt (Kafal) No PWM Available</p>	<p>Cic_3454.1_PWM_5.txt (Kafal) [Z-Beta = 10.3222] [Pearson Full 0.616714703, Pearson Zeroed 0.615355]</p> 	<p>Cic_3454.1_combined_PWM_5.txt (Kafal) No PWM Available</p>
<p>Cic_3454.1_PWM_6.txt (Kafal) No PWM Available</p>	<p>Cic_3454.1_PWM_6.txt (Kafal) [Z-Beta = 16.6314] [Pearson Full 0.616714703, Pearson Zeroed 0.613593]</p> 	<p>Cic_3454.1_combined_PWM_6.txt (Kafal) No PWM Available</p>
<p>Cic_3454.1_PWM_7.txt (Kafal) No PWM Available</p>	<p>Cic_3454.1_PWM_7.txt (Kafal) [Z-Beta = 11.7312] [Pearson Full 0.616714703, Pearson Zeroed 0.615087]</p> 	<p>Cic_3454.1_combined_PWM_7.txt (Kafal) No PWM Available</p>

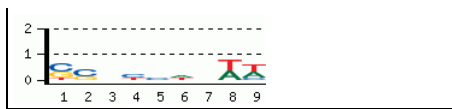
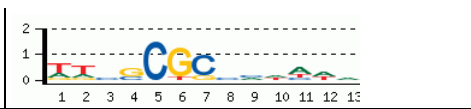
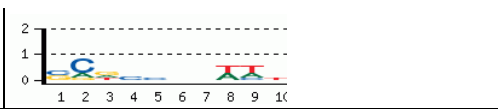

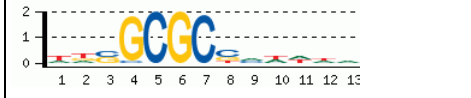
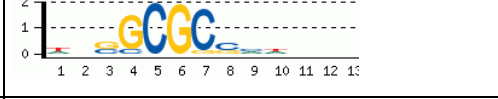
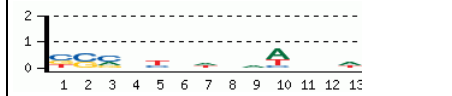
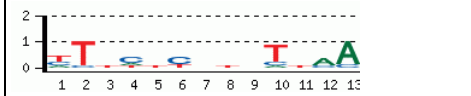
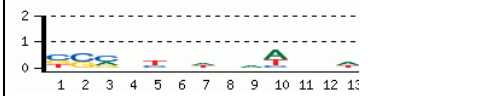

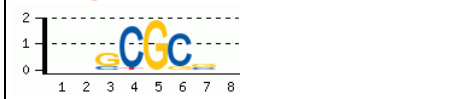
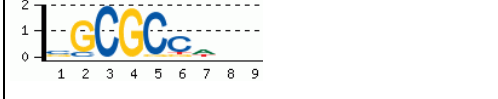
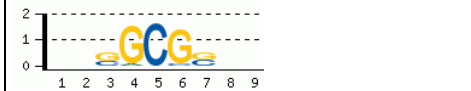

# E2F2\_1022.2

Array Version 1	Array Version 2	Combined
E2F2_1022.2_v1_prim_auc.txt (SW) 	E2F2_1022.2_v2_prim_auc.txt (SW) 	E2F2_1022.2_combined_prim_auc.txt (SW) 
E2F2_1022.2_v1_prim_ic.txt (SW) 	E2F2_1022.2_v2_prim_ic.txt (SW) 	E2F2_1022.2_combined_prim_ic_trimmed.txt (SW) 
E2F2_1022.2_v1_prim_untrimmed.txt (SW) 	E2F2_1022.2_v2_prim_untrimmed.txt (SW) 	E2F2_1022.2_combined_prim_untrimmed.txt (SW) 
E2F2_1022.2_v1_sec_auc.txt (SW) 	E2F2_1022.2_v2_sec_auc.txt (SW) 	E2F2_1022.2_combined_sec_auc.txt (SW) 
E2F2_1022.2_v1_sec_ic.txt (SW) 	E2F2_1022.2_v2_sec_ic.txt (SW) 	E2F2_1022.2_combined_sec_ic_trimmed.txt (SW) 
E2F2_1022.2_v1_sec_untrimmed.txt (SW) 	E2F2_1022.2_v2_sec_untrimmed.txt (SW) [Z-Beta = 229.204] [Pearson Full 0.772937197, Pearson Zeroed 0.749428] 	E2F2_1022.2_combined_sec_untrimmed.txt (SW) 
E2F2_1022.2_v1_prim_trimmed.txt (RM8) 	E2F2_1022.2_v2_prim_trimmed.txt (RM8) 	E2F2_1022.2_RM8_combined_prim_trimmed.txt (RM8) No PWM Available
E2F2_1022.2_v1_sec_trimmed.txt (RM8) 	E2F2_1022.2_v2_sec_trimmed.txt (RM8) [Z-Beta = 190.889] [Pearson Full 0.772937197, Pearson Zeroed 0.765221] 	E2F2_1022.2_RM8_combined_sec_trimmed.txt (RM8) No PWM Available
E2F2_1022.2_v1_prim_untrimmed.txt (RM8) 	E2F2_1022.2_v2_prim_untrimmed.txt (RM8) 	E2F2_1022.2_RM8_combined_prim_untrimmed.txt (RM8) No PWM Available
E2F2_1022.2_v1_sec_untrimmed.txt (RM8) 	E2F2_1022.2_v2_sec_untrimmed.txt (RM8) [Z-Beta = 229.204] [Pearson Full 0.772937197, Pearson Zeroed 0.749428] 	E2F2_1022.2_RM8_combined_sec_untrimmed.txt (RM8) No PWM Available
E2F2_1022.2_v1_RM35_prim_trimmed.txt (RM35) 	E2F2_1022.2_v2_RM35_prim_trimmed.txt (RM35) 	E2F2_1022.2_RM_combined_prim_trimmed.txt (RM35)

		
<b>E2F2_1022.2_v1_RM35_sec_trimmed.txt (RM35)</b> 	<b>E2F2_1022.2_v2_RM35_sec_trimmed.txt (RM35)</b> 	<b>E2F2_1022.2_RM_combined_sec_trimmed.txt (RM35)</b> 
<b>E2F2_1022.2_v1_RM35_prim_untrimmed.txt (RM35)</b> [Z-Beta = 75.5079] [Pearson Full 0.754802216, Pearson Zeroed 0.744448] 	<b>E2F2_1022.2_v2_RM35_prim_untrimmed.txt (RM35)</b> [Z-Beta = 111.016] [Pearson Full 0.772937197, Pearson Zeroed 0.773773] 	<b>E2F2_1022.2_RM_combined_prim_untrimmed.txt (RM35)</b> [Z-Beta = 307.943] [Pearson Full 0.724763929, Pearson Zeroed 0.72192] 
<b>E2F2_1022.2_v1_RM35_sec_untrimmed.txt (RM35)</b> 	<b>E2F2_1022.2_v2_RM35_sec_untrimmed.txt (RM35)</b> 	<b>E2F2_1022.2_RM_combined_sec_untrimmed.txt (RM35)</b> 
<b>E2F2_1022.2_PWM_1.txt (K-afal)</b> [Z-Beta = 799.008] [Pearson Full 0.754802216, Pearson Zeroed 0.570928] 	<b>E2F2_1022.2_PWM_1.txt (K-afal)</b> [Z-Beta = 97.3637] [Pearson Full 0.772937197, Pearson Zeroed 0.753586] 	<b>E2F2_1022.2_combined_PWM_1.txt (K-afal)</b> [Z-Beta = 186.849] [Pearson Full 0.724763929, Pearson Zeroed 0.684598] 
<b>E2F2_1022.2_PWM_2.txt (Kafal)</b> No PWM Available	<b>E2F2_1022.2_PWM_2.txt (Kafal)</b> [Z-Beta = 296.762] [Pearson Full 0.772937197, Pearson Zeroed 0.724601] 	<b>E2F2_1022.2_combined_PWM_2.txt (Kafal)</b> 


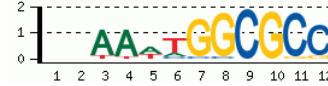
















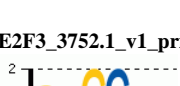
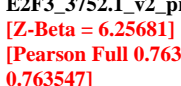
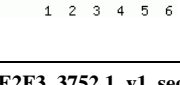



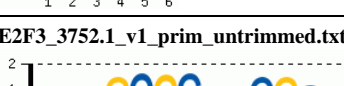
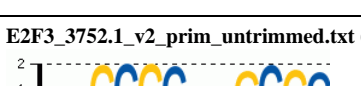
# E2F2\_1022.4

Array Version 1	Array Version 2	Combined
<p>E2F2_1022.4_v1_prim_auc.txt (SW)</p> 	<p>E2F2_1022.4_v2_prim_auc.txt (SW)</p> 	<p>E2F2_1022.4_combined_prim_auc.txt (SW)</p> <p>No PWM Available</p>
<p>E2F2_1022.4_v1_prim_ic.txt (SW)</p> 	<p>E2F2_1022.4_v2_prim_ic.txt (SW)</p> 	<p>E2F2_1022.4_combined_prim_ic_trimmed.txt (SW)</p> 
<p>E2F2_1022.4_v1_prim_untrimmed.txt (SW)</p> 	<p>E2F2_1022.4_v2_prim_untrimmed.txt (SW)</p> 	<p>E2F2_1022.4_combined_prim_untrimmed.txt (SW)</p> 
<p>E2F2_1022.4_v1_sec_auc.txt (SW)</p> 	<p>E2F2_1022.4_v2_sec_auc.txt (SW)</p> 	<p>E2F2_1022.4_combined_sec_auc.txt (SW)</p> 
<p>E2F2_1022.4_v1_sec_ic.txt (SW)</p> 	<p>E2F2_1022.4_v2_sec_ic.txt (SW)</p> 	<p>E2F2_1022.4_combined_sec_ic_trimmed.txt (SW)</p> 
<p>E2F2_1022.4_v1_sec_untrimmed.txt (SW)</p> 	<p>E2F2_1022.4_v2_sec_untrimmed.txt (SW)</p> 	<p>E2F2_1022.4_combined_sec_untrimmed.txt (SW)</p> 
<p>E2F2_1022.4_v1_prim_trimmed.txt (RM8)</p> 	<p>E2F2_1022.4_v2_prim_trimmed.txt (RM8)</p> 	<p>E2F2_1022.4_RM8_combined_prim_trimmed.txt (RM8)</p> <p>No PWM Available</p>
<p>E2F2_1022.4_v1_sec_trimmed.txt (RM8)</p> <p>[Z-Beta = 55.2429] [Pearson Full 0.742884498, Pearson Zeroed 0.741911]</p> 	<p>E2F2_1022.4_v2_sec_trimmed.txt (RM8)</p> <p>[Z-Beta = 36.2363] [Pearson Full 0.76087725, Pearson Zeroed 0.758193]</p> 	<p>E2F2_1022.4_RM8_combined_sec_trimmed.txt (RM8)</p> <p>No PWM Available</p>
<p>E2F2_1022.4_v1_prim_untrimmed.txt (RM8)</p> 	<p>E2F2_1022.4_v2_prim_untrimmed.txt (RM8)</p> 	<p>E2F2_1022.4_RM8_combined_prim_untrimmed.txt (RM8)</p> <p>No PWM Available</p>
<p>E2F2_1022.4_v1_sec_untrimmed.txt (RM8)</p> 	<p>E2F2_1022.4_v2_sec_untrimmed.txt (RM8)</p> 	<p>E2F2_1022.4_RM8_combined_sec_untrimmed.txt (RM8)</p> <p>No PWM Available</p>
<p>E2F2_1022.4_v1_RM35_prim_trimmed.txt (RM35)</p> 	<p>E2F2_1022.4_v2_RM35_prim_trimmed.txt (RM35)</p> 	<p>E2F2_1022.4_RM_combined_prim_trimmed.txt (RM35)</p> 
<p>E2F2_1022.4_v1_RM35_sec_trimmed.txt (RM35)</p>	<p>E2F2_1022.4_v2_RM35_sec_trimmed.txt (RM35)</p> <p>[Z-Beta = 94.4312] [Pearson Full 0.76087725, Pearson Zeroed 0.723334]</p>	<p>E2F2_1022.4_RM_combined_sec_trimmed.txt (RM35)</p>









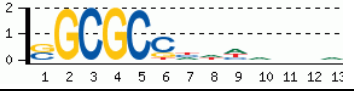

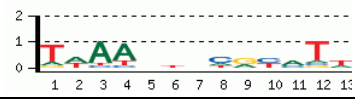
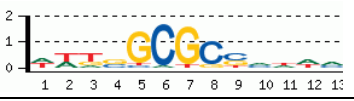
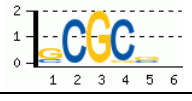
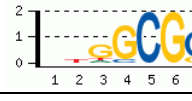


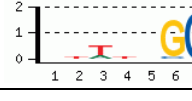


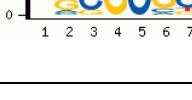
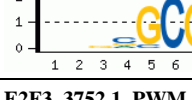



		
<p><b>E2F2_1022.4_v1_RM35_prim_untrimmed.txt (RM35)</b>  [Z-Beta = 507.292]  [Pearson Full 0.742884498, Pearson Zeroed 0.641489]</p> 	<p><b>E2F2_1022.4_v2_RM35_prim_untrimmed.txt (RM35)</b></p> 	<p><b>E2F2_1022.4_RM_combined_prim_untrimmed.txt (RM35)</b>  [Z-Beta = 239.744]  [Pearson Full 0.787909526, Pearson Zeroed 0.764686]</p> 
<p><b>E2F2_1022.4_v1_RM35_sec_untrimmed.txt (RM35)</b></p> 	<p><b>E2F2_1022.4_v2_RM35_sec_untrimmed.txt (RM35)</b></p> 	<p><b>E2F2_1022.4_RM_combined_sec_untrimmed.txt (RM35)</b></p> 
<p><b>E2F2_1022.4_PWM_1.txt (K-afal)</b>  [Z-Beta = 390.448]  [Pearson Full 0.742884498, Pearson Zeroed 0.681654]</p> 	<p><b>E2F2_1022.4_PWM_1.txt (K-afal)</b>  [Z-Beta = 217.245]  [Pearson Full 0.76087725, Pearson Zeroed 0.703149]</p> 	<p><b>E2F2_1022.4_combined_PWM_1.txt (K-afal)</b>  [Z-Beta = 312.776]  [Pearson Full 0.787909526, Pearson Zeroed 0.744893]</p> 
<p><b>E2F2_1022.4_PWM_2.txt (Kafal)</b>  No PWM Available</p>	<p><b>E2F2_1022.4_PWM_2.txt (Kafal)</b>  [Z-Beta = 140.058]  [Pearson Full 0.76087725, Pearson Zeroed 0.725127]</p> 	<p><b>E2F2_1022.4_combined_PWM_2.txt (Kafal)</b>  [Z-Beta = 224.881]  [Pearson Full 0.787909526, Pearson Zeroed 0.710602]</p> 























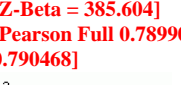
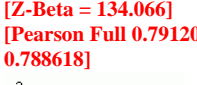
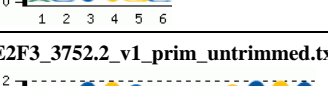
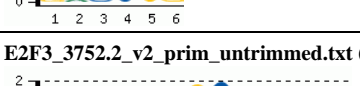
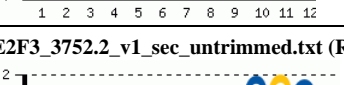
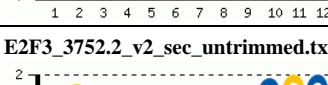
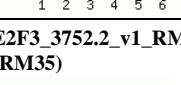
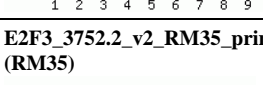
# E2F3\_3752.1

Array Version 1	Array Version 2	Combined
E2F3_3752.1_v1_prim_auc.txt (SW) 	E2F3_3752.1_v2_prim_auc.txt (SW) 	E2F3_3752.1_combined_prim_auc.txt (SW) 
E2F3_3752.1_v1_prim_ic.txt (SW) 	E2F3_3752.1_v2_prim_ic.txt (SW) 	E2F3_3752.1_combined_prim_ic_trimmed.txt (SW) 
E2F3_3752.1_v1_prim_untrimmed.txt (SW) 	E2F3_3752.1_v2_prim_untrimmed.txt (SW) 	E2F3_3752.1_combined_prim_untrimmed.txt (SW) 
E2F3_3752.1_v1_sec_auc.txt (SW) 	E2F3_3752.1_v2_sec_auc.txt (SW) 	E2F3_3752.1_combined_sec_auc.txt (SW) 
E2F3_3752.1_v1_sec_ic.txt (SW) 	E2F3_3752.1_v2_sec_ic.txt (SW) 	E2F3_3752.1_combined_sec_ic_trimmed.txt (SW) 
E2F3_3752.1_v1_sec_untrimmed.txt (SW) 	E2F3_3752.1_v2_sec_untrimmed.txt (SW) 	E2F3_3752.1_combined_sec_untrimmed.txt (SW) 
E2F3_3752.1_v1_prim_trimmed.txt (RM8) 	E2F3_3752.1_v2_prim_trimmed.txt (RM8) [Z-Beta = 6.25681] [Pearson Full 0.763591519, Pearson Zeroed 0.763547] 	E2F3_3752.1_RM8_combined_prim_trimmed.txt (RM8) No PWM Available
E2F3_3752.1_v1_sec_trimmed.txt (RM8) [Z-Beta = 272.479] [Pearson Full 0.808452529, Pearson Zeroed 0.806093] 	E2F3_3752.1_v2_sec_trimmed.txt (RM8) 	E2F3_3752.1_RM8_combined_sec_trimmed.txt (RM8) No PWM Available
E2F3_3752.1_v1_prim_untrimmed.txt (RM8) 	E2F3_3752.1_v2_prim_untrimmed.txt (RM8) 	E2F3_3752.1_RM8_combined_prim_untrimmed.txt (RM8) No PWM Available
E2F3_3752.1_v1_sec_untrimmed.txt (RM8) [Z-Beta = 43.8517] [Pearson Full 0.808452529, Pearson Zeroed 0.804164] 	E2F3_3752.1_v2_sec_untrimmed.txt (RM8) 	E2F3_3752.1_RM8_combined_sec_untrimmed.txt (RM8) No PWM Available
E2F3_3752.1_v1_RM35_prim_trimmed.txt (RM35)	E2F3_3752.1_v2_RM35_prim_trimmed.txt (RM35)	E2F3_3752.1_RM_combined_prim_trimmed.txt (RM35) [Z-Beta = 181.29] [Pearson Full 0.71546945, Pearson Zeroed 0.700574]

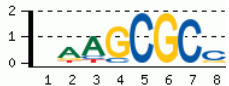









		
<p>E2F3_3752.1_v1_RM35_sec_trimmed.txt (RM35)</p> 	<p>E2F3_3752.1_v2_RM35_sec_trimmed.txt (RM35)  [Z-Beta = 80.2727]  [Pearson Full 0.763591519, Pearson Zeroed 0.738136]</p> 	<p>E2F3_3752.1_RM_combined_sec_trimmed.txt (RM35)  [Z-Beta = 44.691]  [Pearson Full 0.71546945, Pearson Zeroed 0.705606]</p> 
<p>E2F3_3752.1_v1_RM35_prim_untrimmed.txt (RM35)</p> 	<p>E2F3_3752.1_v2_RM35_prim_untrimmed.txt (RM35)</p> 	<p>E2F3_3752.1_RM_combined_prim_untrimmed.txt (RM35)</p> 
<p>E2F3_3752.1_v1_RM35_sec_untrimmed.txt (RM35)</p> 	<p>E2F3_3752.1_v2_RM35_sec_untrimmed.txt (RM35)</p> 	<p>E2F3_3752.1_RM_combined_sec_untrimmed.txt (RM35)</p> 
<p>E2F3_3752.1_PWM_1.txt (K-afal)  [Z-Beta = 316.751]  [Pearson Full 0.808452529, Pearson Zeroed 0.781857]</p> 	<p>E2F3_3752.1_PWM_1.txt (K-afal)  [Z-Beta = 89.6566]  [Pearson Full 0.763591519, Pearson Zeroed 0.710721]</p> 	<p>E2F3_3752.1_combined_PWM_1.txt (K-afal)  [Z-Beta = 28.5243]  [Pearson Full 0.71546945, Pearson Zeroed 0.71402]</p> 
<p>E2F3_3752.1_PWM_2.txt (K-afal)  [Z-Beta = 107.48]  [Pearson Full 0.808452529, Pearson Zeroed 0.790913]</p> 	<p>E2F3_3752.1_PWM_2.txt (K-afal)  [Z-Beta = 82.1592]  [Pearson Full 0.763591519, Pearson Zeroed 0.752326]</p> 	<p>E2F3_3752.1_combined_PWM_2.txt (K-afal)  [Z-Beta = 70.8625]  [Pearson Full 0.71546945, Pearson Zeroed 0.702072]</p> 
<p>E2F3_3752.1_PWM_3.txt (Kafal)  No PWM Available</p>	<p>E2F3_3752.1_PWM_3.txt (Kafal)  [Z-Beta = 8.25031]  [Pearson Full 0.763591519, Pearson Zeroed 0.763375]</p> 	<p>E2F3_3752.1_combined_PWM_3.txt (Kafal)</p> 
<p>E2F3_3752.1_PWM_4.txt (Kafal)  No PWM Available</p>	<p>E2F3_3752.1_PWM_4.txt (Kafal)  [Z-Beta = 219.925]  [Pearson Full 0.763591519, Pearson Zeroed 0.660631]</p> 	<p>E2F3_3752.1_combined_PWM_4.txt (Kafal)</p> 
<p>E2F3_3752.1_PWM_5.txt (Kafal)  No PWM Available</p>	<p>E2F3_3752.1_PWM_5.txt (Kafal)</p> 	<p>E2F3_3752.1_combined_PWM_5.txt (Kafal)</p> 






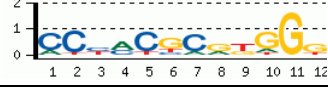

# E2F3\_3752.2

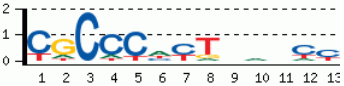







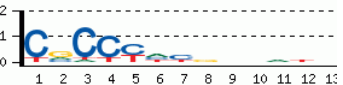

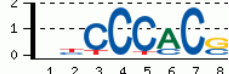
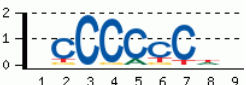



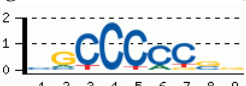

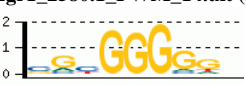




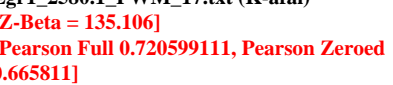
Array Version 1	Array Version 2	Combined
<p>E2F3_3752.2_v1_prim_auc.txt (SW)  <b>[Z-Beta = 48.0732]</b>  <b>[Pearson Full 0.789905228, Pearson Zeroed 0.788013]</b></p> 	<p>E2F3_3752.2_v2_prim_auc.txt (SW)</p> 	<p>E2F3_3752.2_combined_prim_auc.txt (SW)</p> 
<p>E2F3_3752.2_v1_prim_ic.txt (SW)</p> 	<p>E2F3_3752.2_v2_prim_ic.txt (SW)</p> 	<p>E2F3_3752.2_combined_prim_ic_trimmed.txt (SW)</p> 
<p>E2F3_3752.2_v1_prim_untrimmed.txt (SW)</p> 	<p>E2F3_3752.2_v2_prim_untrimmed.txt (SW)</p> 	<p>E2F3_3752.2_combined_prim_untrimmed.txt (SW)</p> 
<p>E2F3_3752.2_v1_sec_auc.txt (SW)</p> 	<p>E2F3_3752.2_v2_sec_auc.txt (SW)</p> 	<p>E2F3_3752.2_combined_sec_auc.txt (SW)</p> 
<p>E2F3_3752.2_v1_sec_ic.txt (SW)</p> 	<p>E2F3_3752.2_v2_sec_ic.txt (SW)</p> 	<p>E2F3_3752.2_combined_sec_ic_trimmed.txt (SW)  <b>[Z-Beta = 10.761]</b>  <b>[Pearson Full 0.702576324, Pearson Zeroed 0.69895]</b></p> 
<p>E2F3_3752.2_v1_sec_untrimmed.txt (SW)</p> 	<p>E2F3_3752.2_v2_sec_untrimmed.txt (SW)</p> 	<p>E2F3_3752.2_combined_sec_untrimmed.txt (SW)</p> 
<p>E2F3_3752.2_v1_prim_trimmed.txt (RM8)</p> 	<p>E2F3_3752.2_v2_prim_trimmed.txt (RM8)</p> 	<p>E2F3_3752.2_RM8_combined_prim_trimmed.txt (RM8)            No PWM Available</p>
<p>E2F3_3752.2_v1_sec_trimmed.txt (RM8)  <b>[Z-Beta = 385.604]</b>  <b>[Pearson Full 0.789905228, Pearson Zeroed 0.790468]</b></p> 	<p>E2F3_3752.2_v2_sec_trimmed.txt (RM8)  <b>[Z-Beta = 134.066]</b>  <b>[Pearson Full 0.791208691, Pearson Zeroed 0.788618]</b></p> 	<p>E2F3_3752.2_RM8_combined_sec_trimmed.txt (RM8)            No PWM Available</p>
<p>E2F3_3752.2_v1_prim_untrimmed.txt (RM8)</p> 	<p>E2F3_3752.2_v2_prim_untrimmed.txt (RM8)</p> 	<p>E2F3_3752.2_RM8_combined_prim_untrimmed.txt (RM8)            No PWM Available</p>
<p>E2F3_3752.2_v1_sec_untrimmed.txt (RM8)</p> 	<p>E2F3_3752.2_v2_sec_untrimmed.txt (RM8)</p> 	<p>E2F3_3752.2_RM8_combined_sec_untrimmed.txt (RM8)            No PWM Available</p>
<p>E2F3_3752.2_v1_RM35_prim_trimmed.txt (RM35)</p> 	<p>E2F3_3752.2_v2_RM35_prim_trimmed.txt (RM35)</p> 	<p>E2F3_3752.2_RM_combined_prim_trimmed.txt (RM35)  <b>[Z-Beta = 127.148]</b>  <b>[Pearson Full 0.702576324, Pearson Zeroed 0.699481]</b></p>

<b>E2F3_3752.2_v1_RM35_sec_trimmed.txt (RM35)</b> [Z-Beta = 24.0708] [Pearson Full 0.789905228, Pearson Zeroed 0.785135]	<b>E2F3_3752.2_v2_RM35_sec_trimmed.txt (RM35)</b> 	<b>E2F3_3752.2_RM_combined_sec_trimmed.txt (RM35)</b> 
<b>E2F3_3752.2_v1_RM35_prim_untrimmed.txt (RM35)</b> 	<b>E2F3_3752.2_v2_RM35_prim_untrimmed.txt (RM35)</b> [Z-Beta = 38.9612] [Pearson Full 0.791208691, Pearson Zeroed 0.786551]	<b>E2F3_3752.2_RM_combined_prim_untrimmed.txt (RM35)</b> 
<b>E2F3_3752.2_v1_RM35_sec_untrimmed.txt (RM35)</b> 	<b>E2F3_3752.2_v2_RM35_sec_untrimmed.txt (RM35)</b> 	<b>E2F3_3752.2_RM_combined_sec_untrimmed.txt (RM35)</b> 
<b>E2F3_3752.2_PWM_10.txt (K-afal)</b> 	<b>E2F3_3752.2_PWM_10.txt (K-afal)</b> No PWM Available	<b>E2F3_3752.2_combined_PWM_10.txt (K-afal)</b> No PWM Available
<b>E2F3_3752.2_PWM_11.txt (K-afal)</b> [Z-Beta = 35.3275] [Pearson Full 0.789905228, Pearson Zeroed 0.78497]	<b>E2F3_3752.2_PWM_11.txt (K-afal)</b> No PWM Available	<b>E2F3_3752.2_combined_PWM_11.txt (K-afal)</b> No PWM Available
<b>E2F3_3752.2_PWM_12.txt (K-afal)</b> [Z-Beta = 43.4166] [Pearson Full 0.789905228, Pearson Zeroed 0.783893]	<b>E2F3_3752.2_PWM_12.txt (K-afal)</b> No PWM Available	<b>E2F3_3752.2_combined_PWM_12.txt (K-afal)</b> No PWM Available
<b>E2F3_3752.2_PWM_1.txt (K-afal)</b> 	<b>E2F3_3752.2_PWM_1.txt (K-afal)</b> 	<b>E2F3_3752.2_combined_PWM_1.txt (K-afal)</b> [Z-Beta = 198.089] [Pearson Full 0.702576324, Pearson Zeroed 0.635788]
<b>E2F3_3752.2_PWM_2.txt (K-afal)</b> [Z-Beta = 23.8708] [Pearson Full 0.789905228, Pearson Zeroed 0.789178]	<b>E2F3_3752.2_PWM_2.txt (K-afal)</b> [Z-Beta = 291.204] [Pearson Full 0.791208691, Pearson Zeroed 0.724976]	<b>E2F3_3752.2_combined_PWM_2.txt (K-afal)</b> 
<b>E2F3_3752.2_PWM_3.txt (K-afal)</b> 	<b>E2F3_3752.2_PWM_3.txt (K-afal)</b> [Z-Beta = 24.5938] [Pearson Full 0.791208691, Pearson Zeroed 0.790823]	<b>E2F3_3752.2_combined_PWM_3.txt (K-afal)</b> 


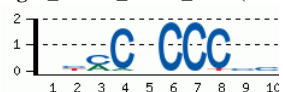
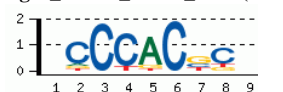
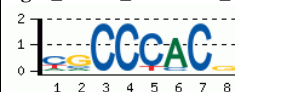




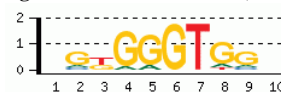


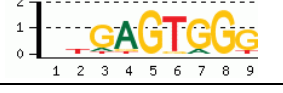












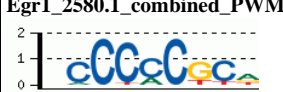



<p>E2F3_3752.2_PWM_4.txt (K-afal)</p> 	<p>E2F3_3752.2_PWM_4.txt (K-afal)  [Z-Beta = 191.032]  [Pearson Full 0.791208691, Pearson Zeroed 0.757248]</p> 	<p>E2F3_3752.2_combined_PWM_4.txt (K-afal)  [Z-Beta = 321.629]  [Pearson Full 0.702576324, Pearson Zeroed 0.636047]</p> 
<p>E2F3_3752.2_PWM_5.txt (K-afal)</p> 	<p>E2F3_3752.2_PWM_5.txt (K-afal)  No PWM Available</p>	<p>E2F3_3752.2_combined_PWM_5.txt (K-afal)  No PWM Available</p>
<p>E2F3_3752.2_PWM_6.txt (K-afal)  [Z-Beta = 19.2637]  [Pearson Full 0.789905228, Pearson Zeroed 0.786745]</p> 	<p>E2F3_3752.2_PWM_6.txt (K-afal)  No PWM Available</p>	<p>E2F3_3752.2_combined_PWM_6.txt (K-afal)  No PWM Available</p>
<p>E2F3_3752.2_PWM_7.txt (K-afal)  [Z-Beta = 139.248]  [Pearson Full 0.789905228, Pearson Zeroed 0.782034]</p> 	<p>E2F3_3752.2_PWM_7.txt (K-afal)  No PWM Available</p>	<p>E2F3_3752.2_combined_PWM_7.txt (K-afal)  No PWM Available</p>
<p>E2F3_3752.2_PWM_8.txt (K-afal)</p> 	<p>E2F3_3752.2_PWM_8.txt (K-afal)  No PWM Available</p>	<p>E2F3_3752.2_combined_PWM_8.txt (K-afal)  No PWM Available</p>
<p>E2F3_3752.2_PWM_9.txt (K-afal)  [Z-Beta = 51.2919]  [Pearson Full 0.789905228, Pearson Zeroed 0.777598]</p> 	<p>E2F3_3752.2_PWM_9.txt (K-afal)  No PWM Available</p>	<p>E2F3_3752.2_combined_PWM_9.txt (K-afal)  No PWM Available</p>






# Egr1\_2580.1

Array Version 1	Array Version 2	Combined
Egr1_2580.1_v1_prim_auc.txt (SW) 	Egr1_2580.1_v2_prim_auc.txt (SW) 	Egr1_2580.1_combined_prim_auc.txt (SW) 
Egr1_2580.1_v1_prim_ic.txt (SW) [Z-Beta = 32.0491] [Pearson Full 0.720599111, Pearson Zeroed 0.712543] 	Egr1_2580.1_v2_prim_ic.txt (SW) 	Egr1_2580.1_combined_prim_ic_trimmed.txt (SW) 
Egr1_2580.1_v1_prim_untrimmed.txt (SW) 	Egr1_2580.1_v2_prim_untrimmed.txt (SW) 	Egr1_2580.1_combined_prim_untrimmed.txt (SW) 
Egr1_2580.1_v1_sec_auc.txt (SW) 	Egr1_2580.1_v2_sec_auc.txt (SW) 	Egr1_2580.1_combined_sec_auc.txt (SW) 
Egr1_2580.1_v1_sec_ic.txt (SW) 	Egr1_2580.1_v2_sec_ic.txt (SW) 	Egr1_2580.1_combined_sec_ic_trimmed.txt (SW) 
Egr1_2580.1_v1_sec_untrimmed.txt (SW) 	Egr1_2580.1_v2_sec_untrimmed.txt (SW) 	Egr1_2580.1_combined_sec_untrimmed.txt (SW) 
Egr1_2580.1_v1_prim_trimmed.txt (RM8) 	Egr1_2580.1_v2_prim_trimmed.txt (RM8) 	Egr1_2580.1_RM8_combined_prim_trimmed.txt (RM8) No PWM Available
Egr1_2580.1_v1_sec_trimmed.txt (RM8) 	Egr1_2580.1_v2_sec_trimmed.txt (RM8) 	Egr1_2580.1_RM8_combined_sec_trimmed.txt (RM8) No PWM Available
Egr1_2580.1_v1_prim_untrimmed.txt (RM8) 	Egr1_2580.1_v2_prim_untrimmed.txt (RM8) 	Egr1_2580.1_RM8_combined_prim_untrimmed.txt (RM8) No PWM Available
Egr1_2580.1_v1_sec_untrimmed.txt (RM8) 	Egr1_2580.1_v2_sec_untrimmed.txt (RM8) 	Egr1_2580.1_RM8_combined_sec_untrimmed.txt (RM8) No PWM Available
Egr1_2580.1_v1_RM35_prim_trimmed.txt (RM35) [Z-Beta = 217.007] [Pearson Full 0.720599111, Pearson Zeroed 0.624471] 	Egr1_2580.1_v2_RM35_prim_trimmed.txt (RM35) [Z-Beta = 206.837] [Pearson Full 0.779833486, Pearson Zeroed 0.769178] 	Egr1_2580.1_RM_combined_prim_trimmed.txt (RM35) [Z-Beta = 177.133] [Pearson Full 0.762651785, Pearson Zeroed 0.764438] 

<p>Egr1_2580.1_v1_RM35_sec_trimmed.txt (RM35)</p> 	<p>Egr1_2580.1_v2_RM35_sec_trimmed.txt (RM35)</p> 	<p>Egr1_2580.1_RM_combined_sec_trimmed.txt (RM35)</p> 
<p>Egr1_2580.1_v1_RM35_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = 24.0704] [Pearson Full 0.720599111, Pearson Zeroed 0.718455]</p> 	<p>Egr1_2580.1_v2_RM35_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = 309.507] [Pearson Full 0.779833486, Pearson Zeroed 0.735417]</p> 	<p>Egr1_2580.1_RM_combined_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = 214.135] [Pearson Full 0.762651785, Pearson Zeroed 0.753229]</p> 
<p>Egr1_2580.1_v1_RM35_sec_untrimmed.txt (RM35)</p> <p>[Z-Beta = 17.0597] [Pearson Full 0.720599111, Pearson Zeroed 0.714856]</p> 	<p>Egr1_2580.1_v2_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Egr1_2580.1_RM_combined_sec_untrimmed.txt (RM35)</p> <p>[Z-Beta = 66.773] [Pearson Full 0.762651785, Pearson Zeroed 0.733991]</p> 
<p>Egr1_2580.1_PWM_10.txt (K-afal)</p> 	<p>Egr1_2580.1_PWM_10.txt (K-afal)</p> 	<p>Egr1_2580.1_combined_PWM_10.txt (K-afal)</p> <p>No PWM Available</p>
<p>Egr1_2580.1_PWM_11.txt (K-afal)</p> 	<p>Egr1_2580.1_PWM_11.txt (K-afal)</p> <p>[Z-Beta = 364.827] [Pearson Full 0.779833486, Pearson Zeroed 0.614849]</p> 	<p>Egr1_2580.1_combined_PWM_11.txt (K-afal)</p> <p>No PWM Available</p>
<p>Egr1_2580.1_PWM_12.txt (K-afal)</p> 	<p>Egr1_2580.1_PWM_12.txt (K-afal)</p> 	<p>Egr1_2580.1_combined_PWM_12.txt (K-afal)</p> <p>No PWM Available</p>
<p>Egr1_2580.1_PWM_13.txt (K-afal)</p> 	<p>Egr1_2580.1_PWM_13.txt (K-afal)</p> 	<p>Egr1_2580.1_combined_PWM_13.txt (K-afal)</p> <p>No PWM Available</p>
<p>Egr1_2580.1_PWM_14.txt (K-afal)</p> 	<p>Egr1_2580.1_PWM_14.txt (K-afal)</p> 	<p>Egr1_2580.1_combined_PWM_14.txt (K-afal)</p> <p>No PWM Available</p>
<p>Egr1_2580.1_PWM_15.txt (K-afal)</p> 	<p>Egr1_2580.1_PWM_15.txt (K-afal)</p> 	<p>Egr1_2580.1_combined_PWM_15.txt (K-afal)</p> <p>No PWM Available</p>
<p>Egr1_2580.1_PWM_16.txt (K-afal)</p> <p>[Z-Beta = 4.0735] [Pearson Full 0.720599111, Pearson Zeroed 0.718386]</p> 	<p>Egr1_2580.1_PWM_16.txt (K-afal)</p> <p>No PWM Available</p>	<p>Egr1_2580.1_combined_PWM_16.txt (K-afal)</p> <p>No PWM Available</p>
<p>Egr1_2580.1_PWM_17.txt (K-afal)</p> <p>[Z-Beta = 135.106] [Pearson Full 0.720599111, Pearson Zeroed 0.665811]</p> 	<p>Egr1_2580.1_PWM_17.txt (K-afal)</p> <p>No PWM Available</p>	<p>Egr1_2580.1_combined_PWM_17.txt (K-afal)</p> <p>No PWM Available</p>










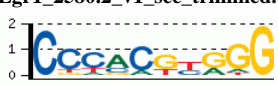
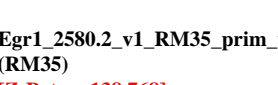
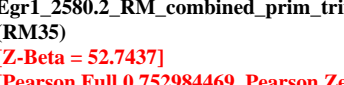


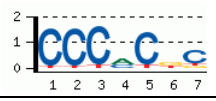


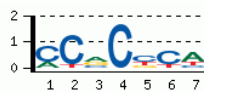

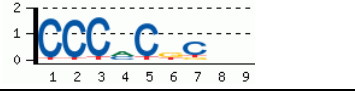
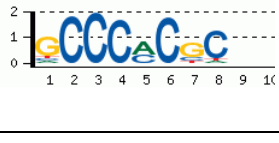


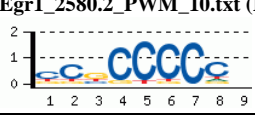


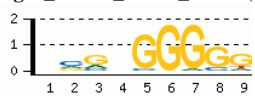
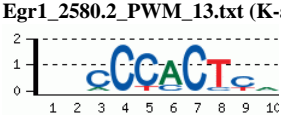







		
Egr1_2580.1_PWM_1.txt (K-afal) 	Egr1_2580.1_PWM_1.txt (K-afal) 	Egr1_2580.1_combined_PWM_1.txt (K-afal) 
Egr1_2580.1_PWM_2.txt (K-afal) 	Egr1_2580.1_PWM_2.txt (K-afal) 	Egr1_2580.1_combined_PWM_2.txt (K-afal) [Z-Beta = 149.928] [Pearson Full 0.762651785, Pearson Zeroed 0.677879] 
Egr1_2580.1_PWM_3.txt (K-afal) 	Egr1_2580.1_PWM_3.txt (K-afal) 	Egr1_2580.1_combined_PWM_3.txt (K-afal) 
Egr1_2580.1_PWM_4.txt (K-afal) 	Egr1_2580.1_PWM_4.txt (K-afal) 	Egr1_2580.1_combined_PWM_4.txt (K-afal) 
Egr1_2580.1_PWM_5.txt (K-afal) [Z-Beta = 10.8394] [Pearson Full 0.720599111, Pearson Zeroed 0.715724] 	Egr1_2580.1_PWM_5.txt (K-afal) 	Egr1_2580.1_combined_PWM_5.txt (K-afal) 
Egr1_2580.1_PWM_6.txt (K-afal) [Z-Beta = 19.1233] [Pearson Full 0.720599111, Pearson Zeroed 0.709932] 	Egr1_2580.1_PWM_6.txt (K-afal) 	Egr1_2580.1_combined_PWM_6.txt (K-afal) 
Egr1_2580.1_PWM_7.txt (K-afal) [Z-Beta = 15.2893] [Pearson Full 0.720599111, Pearson Zeroed 0.713483] 	Egr1_2580.1_PWM_7.txt (K-afal) 	Egr1_2580.1_combined_PWM_7.txt (K-afal) 
Egr1_2580.1_PWM_8.txt (K-afal) 	Egr1_2580.1_PWM_8.txt (K-afal) 	Egr1_2580.1_combined_PWM_8.txt (K-afal) 
Egr1_2580.1_PWM_9.txt (K-afal) 	Egr1_2580.1_PWM_9.txt (K-afal) [Z-Beta = 11.5873] [Pearson Full 0.779833486, Pearson Zeroed 0.778128] 	Egr1_2580.1_combined_PWM_9.txt (K-afal) No PWM Available
Egr1_2580.1_PWM_10.txt (Kafal) No PWM Available	Egr1_2580.1_PWM_10.txt (Kafal) 	Egr1_2580.1_combined_PWM_10.txt (Kafal) No PWM Available

<p>Egr1_2580.1_PWM_11.txt (Kafal) No PWM Available</p>	<p>Egr1_2580.1_PWM_11.txt (Kafal) [Z-Beta = 364.827] [Pearson Full 0.779833486, Pearson Zeroed 0.614849]</p> 	<p>Egr1_2580.1_combined_PWM_11.txt (Kafal) No PWM Available</p>
<p>Egr1_2580.1_PWM_12.txt (Kafal) No PWM Available</p>	<p>Egr1_2580.1_PWM_12.txt (Kafal)</p> 	<p>Egr1_2580.1_combined_PWM_12.txt (Kafal) No PWM Available</p>
<p>Egr1_2580.1_PWM_13.txt (Kafal) No PWM Available</p>	<p>Egr1_2580.1_PWM_13.txt (Kafal)</p> 	<p>Egr1_2580.1_combined_PWM_13.txt (Kafal) No PWM Available</p>
<p>Egr1_2580.1_PWM_14.txt (Kafal) No PWM Available</p>	<p>Egr1_2580.1_PWM_14.txt (Kafal)</p> 	<p>Egr1_2580.1_combined_PWM_14.txt (Kafal) No PWM Available</p>
<p>Egr1_2580.1_PWM_15.txt (Kafal) No PWM Available</p>	<p>Egr1_2580.1_PWM_15.txt (Kafal)</p> 	<p>Egr1_2580.1_combined_PWM_15.txt (Kafal) No PWM Available</p>






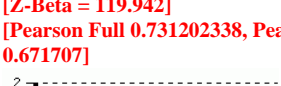
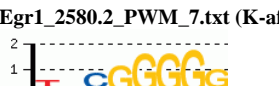
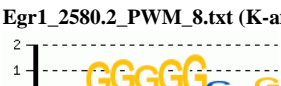
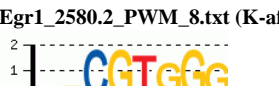
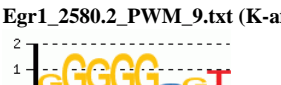
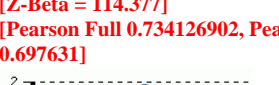
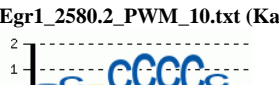

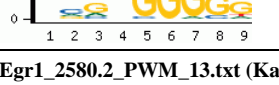

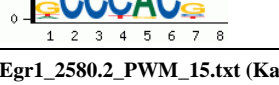



# Egr1\_2580.2

Array Version 1	Array Version 2	Combined
<p>Egr1_2580.2_v1_prim_auc.txt (SW)</p> 	<p>Egr1_2580.2_v2_prim_auc.txt (SW)  [Z-Beta = 94.3383]  [Pearson Full 0.734126902, Pearson Zeroed 0.73612]</p> 	<p>Egr1_2580.2_combined_prim_auc.txt (SW)</p> 
<p>Egr1_2580.2_v1_prim_ic.txt (SW)  [Z-Beta = 134.983]  [Pearson Full 0.731202338, Pearson Zeroed 0.656751]</p> 	<p>Egr1_2580.2_v2_prim_ic.txt (SW)  [Z-Beta = 48.7081]  [Pearson Full 0.734126902, Pearson Zeroed 0.739676]</p> 	<p>Egr1_2580.2_combined_prim_ic_trimmed.txt (SW)  [Z-Beta = 43.6904]  [Pearson Full 0.752984469, Pearson Zeroed 0.74741]</p> 
<p>Egr1_2580.2_v1_prim_untrimmed.txt (SW)</p> 	<p>Egr1_2580.2_v2_prim_untrimmed.txt (SW)</p> 	<p>Egr1_2580.2_combined_prim_untrimmed.txt (SW)</p> 
<p>Egr1_2580.2_v1_sec_auc.txt (SW)</p> 	<p>Egr1_2580.2_v2_sec_auc.txt (SW)</p> 	<p>Egr1_2580.2_combined_sec_auc.txt (SW)</p> 
<p>Egr1_2580.2_v1_sec_ic.txt (SW)</p> 	<p>Egr1_2580.2_v2_sec_ic.txt (SW)</p> 	<p>Egr1_2580.2_combined_sec_ic_trimmed.txt (SW)</p> 
<p>Egr1_2580.2_v1_sec_untrimmed.txt (SW)</p> 	<p>Egr1_2580.2_v2_sec_untrimmed.txt (SW)</p> 	<p>Egr1_2580.2_combined_sec_untrimmed.txt (SW)</p> 
<p>Egr1_2580.2_v1_prim_trimmed.txt (RM8)</p> 	<p>Egr1_2580.2_v2_prim_trimmed.txt (RM8)</p> 	<p>Egr1_2580.2_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Egr1_2580.2_v1_sec_trimmed.txt (RM8)</p> 	<p>Egr1_2580.2_v2_sec_trimmed.txt (RM8)</p> 	<p>Egr1_2580.2_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Egr1_2580.2_v1_prim_untrimmed.txt (RM8)</p> 	<p>Egr1_2580.2_v2_prim_untrimmed.txt (RM8)</p> 	<p>Egr1_2580.2_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Egr1_2580.2_v1_sec_untrimmed.txt (RM8)</p> 	<p>Egr1_2580.2_v2_sec_untrimmed.txt (RM8)</p> 	<p>Egr1_2580.2_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>
<p>Egr1_2580.2_v1_RM35_prim_trimmed.txt (RM35)  [Z-Beta = 138.768]  [Pearson Full 0.731202338, Pearson Zeroed 0.704987]</p> 	<p>Egr1_2580.2_v2_RM35_prim_trimmed.txt (RM35)  [Z-Beta = 67.628]  [Pearson Full 0.734126902, Pearson Zeroed 0.724924]</p> 	<p>Egr1_2580.2_RM_combined_prim_trimmed.txt (RM35)  [Z-Beta = 52.7437]  [Pearson Full 0.752984469, Pearson Zeroed 0.74594]</p> 

		
Egr1_2580.2_v1_RM35_sec_trimmed.txt (RM35)	Egr1_2580.2_v2_RM35_sec_trimmed.txt (RM35)	Egr1_2580.2_RM_combined_sec_trimmed.txt (RM35)
		
Egr1_2580.2_v1_RM35_prim_untrimmed.txt (RM35) [Z-Beta = 78.7925] [Pearson Full 0.731202338, Pearson Zeroed 0.729293]	Egr1_2580.2_v2_RM35_prim_untrimmed.txt (RM35)	Egr1_2580.2_RM_combined_prim_untrimmed.txt (RM35) [Z-Beta = 56.5232] [Pearson Full 0.752984469, Pearson Zeroed 0.746584]
		
Egr1_2580.2_v1_RM35_sec_untrimmed.txt (RM35)	Egr1_2580.2_v2_RM35_sec_untrimmed.txt (RM35) [Z-Beta = 24.6357] [Pearson Full 0.734126902, Pearson Zeroed 0.728183]	Egr1_2580.2_RM_combined_sec_untrimmed.txt (RM35)
		Egr1_2580.2_combined_PWM_10.txt (K-afal) No PWM Available
Egr1_2580.2_PWM_10.txt (K-afal)	Egr1_2580.2_PWM_10.txt (K-afal)	
		Egr1_2580.2_combined_PWM_11.txt (K-afal) No PWM Available
Egr1_2580.2_PWM_11.txt (K-afal)	Egr1_2580.2_PWM_11.txt (K-afal)	
		Egr1_2580.2_combined_PWM_12.txt (K-afal) No PWM Available
Egr1_2580.2_PWM_12.txt (K-afal)	Egr1_2580.2_PWM_12.txt (K-afal)	
		Egr1_2580.2_combined_PWM_13.txt (K-afal) No PWM Available
Egr1_2580.2_PWM_13.txt (K-afal)	Egr1_2580.2_PWM_13.txt (K-afal) [Z-Beta = 50.3148] [Pearson Full 0.734126902, Pearson Zeroed 0.733688]	
		Egr1_2580.2_combined_PWM_14.txt (K-afal) No PWM Available
Egr1_2580.2_PWM_14.txt (K-afal)	Egr1_2580.2_PWM_14.txt (K-afal)	
		Egr1_2580.2_combined_PWM_15.txt (K-afal) No PWM Available
Egr1_2580.2_PWM_15.txt (K-afal)	Egr1_2580.2_PWM_15.txt (K-afal) [Z-Beta = 95.9455] [Pearson Full 0.734126902, Pearson Zeroed 0.705185]	
		Egr1_2580.2_combined_PWM_16.txt (K-afal) No PWM Available
Egr1_2580.2_PWM_16.txt (K-afal)	Egr1_2580.2_PWM_16.txt (K-afal)	

<p>Egr1_2580.2_PWM_17.txt (K-afal)</p> 	<p>Egr1_2580.2_PWM_17.txt (K-afal) No PWM Available</p>	<p>Egr1_2580.2_combined_PWM_17.txt (K-afal) No PWM Available</p>
<p>Egr1_2580.2_PWM_18.txt (K-afal)</p> 	<p>Egr1_2580.2_PWM_18.txt (K-afal) No PWM Available</p>	<p>Egr1_2580.2_combined_PWM_18.txt (K-afal) No PWM Available</p>
<p>Egr1_2580.2_PWM_19.txt (K-afal)</p> 	<p>Egr1_2580.2_PWM_19.txt (K-afal) No PWM Available</p>	<p>Egr1_2580.2_combined_PWM_19.txt (K-afal) No PWM Available</p>
<p>Egr1_2580.2_PWM_1.txt (K-afal)</p> 	<p>Egr1_2580.2_PWM_1.txt (K-afal) [Z-Beta = 26.1703] [Pearson Full 0.734126902, Pearson Zeroed 0.722059]</p> 	<p>Egr1_2580.2_combined_PWM_1.txt (K-afal) [Z-Beta = 180.617] [Pearson Full 0.752984469, Pearson Zeroed 0.689387]</p> 
<p>Egr1_2580.2_PWM_20.txt (K-afal)</p> 	<p>Egr1_2580.2_PWM_20.txt (K-afal) No PWM Available</p>	<p>Egr1_2580.2_combined_PWM_20.txt (K-afal) No PWM Available</p>
<p>Egr1_2580.2_PWM_21.txt (K-afal)</p> 	<p>Egr1_2580.2_PWM_21.txt (K-afal) No PWM Available</p>	<p>Egr1_2580.2_combined_PWM_21.txt (K-afal) No PWM Available</p>
<p>Egr1_2580.2_PWM_22.txt (K-afal)</p> 	<p>Egr1_2580.2_PWM_22.txt (K-afal) No PWM Available</p>	<p>Egr1_2580.2_combined_PWM_22.txt (K-afal) No PWM Available</p>
<p>Egr1_2580.2_PWM_23.txt (K-afal)</p> 	<p>Egr1_2580.2_PWM_23.txt (K-afal) No PWM Available</p>	<p>Egr1_2580.2_combined_PWM_23.txt (K-afal) No PWM Available</p>
<p>Egr1_2580.2_PWM_24.txt (K-afal)</p> 	<p>Egr1_2580.2_PWM_24.txt (K-afal) No PWM Available</p>	<p>Egr1_2580.2_combined_PWM_24.txt (K-afal) No PWM Available</p>
<p>Egr1_2580.2_PWM_2.txt (K-afal)</p> 	<p>Egr1_2580.2_PWM_2.txt (K-afal) [Z-Beta = 16.8183] [Pearson Full 0.734126902, Pearson Zeroed 0.726844]</p> 	<p>Egr1_2580.2_combined_PWM_2.txt (K-afal) [Z-Beta = 17.6269] [Pearson Full 0.752984469, Pearson Zeroed 0.744618]</p> 
<p>Egr1_2580.2_PWM_3.txt (K-afal)</p> 	<p>Egr1_2580.2_PWM_3.txt (K-afal) [Z-Beta = 14.5769] [Pearson Full 0.734126902, Pearson Zeroed 0.731005]</p> 	<p>Egr1_2580.2_combined_PWM_3.txt (K-afal) [Z-Beta = 8.08131] [Pearson Full 0.752984469, Pearson Zeroed 0.750852]</p> 
<p>Egr1_2580.2_PWM_4.txt (K-afal)</p> 	<p>Egr1_2580.2_PWM_4.txt (K-afal)</p> 	<p>Egr1_2580.2_combined_PWM_4.txt (K-afal)</p> 

<p>Egr1_2580.2_PWM_5.txt (K-afal)</p> 	<p>Egr1_2580.2_PWM_5.txt (K-afal)</p> 	<p>Egr1_2580.2_combined_PWM_5.txt (K-afal)</p> 
<p>Egr1_2580.2_PWM_6.txt (K-afal)</p> 	<p>Egr1_2580.2_PWM_6.txt (K-afal)  [Z-Beta = 40.7777]  [Pearson Full 0.734126902, Pearson Zeroed 0.710518]</p> 	<p>Egr1_2580.2_combined_PWM_6.txt (K-afal)  No PWM Available</p>
<p>Egr1_2580.2_PWM_7.txt (K-afal)  [Z-Beta = 119.942]  [Pearson Full 0.731202338, Pearson Zeroed 0.671707]</p> 	<p>Egr1_2580.2_PWM_7.txt (K-afal)</p> 	<p>Egr1_2580.2_combined_PWM_7.txt (K-afal)  No PWM Available</p>
<p>Egr1_2580.2_PWM_8.txt (K-afal)</p> 	<p>Egr1_2580.2_PWM_8.txt (K-afal)</p> 	<p>Egr1_2580.2_combined_PWM_8.txt (K-afal)  No PWM Available</p>
<p>Egr1_2580.2_PWM_9.txt (K-afal)</p> 	<p>Egr1_2580.2_PWM_9.txt (K-afal)  [Z-Beta = 114.377]  [Pearson Full 0.734126902, Pearson Zeroed 0.697631]</p> 	<p>Egr1_2580.2_combined_PWM_9.txt (K-afal)  No PWM Available</p>
<p>Egr1_2580.2_PWM_10.txt (Kafal)  No PWM Available</p>	<p>Egr1_2580.2_PWM_10.txt (Kafal)</p> 	<p>Egr1_2580.2_combined_PWM_10.txt (Kafal)  No PWM Available</p>
<p>Egr1_2580.2_PWM_11.txt (Kafal)  No PWM Available</p>	<p>Egr1_2580.2_PWM_11.txt (Kafal)</p> 	<p>Egr1_2580.2_combined_PWM_11.txt (Kafal)  No PWM Available</p>
<p>Egr1_2580.2_PWM_12.txt (Kafal)  No PWM Available</p>	<p>Egr1_2580.2_PWM_12.txt (Kafal)</p> 	<p>Egr1_2580.2_combined_PWM_12.txt (Kafal)  No PWM Available</p>
<p>Egr1_2580.2_PWM_13.txt (Kafal)  No PWM Available</p>	<p>Egr1_2580.2_PWM_13.txt (Kafal)  [Z-Beta = 50.3148]  [Pearson Full 0.734126902, Pearson Zeroed 0.733688]</p> 	<p>Egr1_2580.2_combined_PWM_13.txt (Kafal)  No PWM Available</p>
<p>Egr1_2580.2_PWM_14.txt (Kafal)  No PWM Available</p>	<p>Egr1_2580.2_PWM_14.txt (Kafal)</p> 	<p>Egr1_2580.2_combined_PWM_14.txt (Kafal)  No PWM Available</p>
<p>Egr1_2580.2_PWM_15.txt (Kafal)  No PWM Available</p>	<p>Egr1_2580.2_PWM_15.txt (Kafal)  [Z-Beta = 95.9455]  [Pearson Full 0.734126902, Pearson Zeroed 0.705185]</p> 	<p>Egr1_2580.2_combined_PWM_15.txt (Kafal)  No PWM Available</p>

Egr1\_2580.2\_PWM\_16.txt (Kafal)  
No PWM Available

Egr1\_2580.2\_PWM\_16.txt (Kafal)



Egr1\_2580.2\_combined\_PWM\_16.txt (Kafal)  
No PWM Available



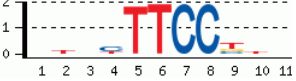
# Ehf\_3056.2

Array Version 1	Array Version 2	Combined
<p>Ehf_3056.2_v1_prim_auc.txt (SW)  <b>[Z-Beta = 2.94759]</b>  <b>[Pearson Full 0.816362204, Pearson Zeroed 0.816209]</b></p>	<p>Ehf_3056.2_v2_prim_auc.txt (SW)</p>	<p>Ehf_3056.2_combined_prim_auc.txt (SW)</p>
<p>Ehf_3056.2_v1_prim_ic.txt (SW)  <b>[Z-Beta = 266.481]</b>  <b>[Pearson Full 0.816362204, Pearson Zeroed 0.773818]</b></p>	<p>Ehf_3056.2_v2_prim_ic.txt (SW)</p>	<p>Ehf_3056.2_combined_prim_ic_trimmed.txt (SW)</p>
<p>Ehf_3056.2_v1_prim_untrimmed.txt (SW)</p>	<p>Ehf_3056.2_v2_prim_untrimmed.txt (SW)</p>	<p>Ehf_3056.2_combined_prim_untrimmed.txt (SW)  <b>[Z-Beta = 27.6689]</b>  <b>[Pearson Full 0.80667412, Pearson Zeroed 0.80534]</b></p>
<p>Ehf_3056.2_v1_sec_auc.txt (SW)  <b>[Z-Beta = 21.3054]</b>  <b>[Pearson Full 0.816362204, Pearson Zeroed 0.813384]</b></p>	<p>Ehf_3056.2_v2_sec_auc.txt (SW)  <b>[Z-Beta = 31.1434]</b>  <b>[Pearson Full 0.621230996, Pearson Zeroed 0.600022]</b></p>	<p>Ehf_3056.2_combined_sec_auc.txt (SW)</p>
<p>Ehf_3056.2_v1_sec_ic.txt (SW)</p>	<p>Ehf_3056.2_v2_sec_ic.txt (SW)</p>	<p>Ehf_3056.2_combined_sec_ic_trimmed.txt (SW)</p>
<p>Ehf_3056.2_v1_sec_untrimmed.txt (SW)</p>	<p>Ehf_3056.2_v2_sec_untrimmed.txt (SW)  <b>[Z-Beta = 41.7321]</b>  <b>[Pearson Full 0.621230996, Pearson Zeroed 0.601505]</b></p>	<p>Ehf_3056.2_combined_sec_untrimmed.txt (SW)</p>
<p>Ehf_3056.2_v1_prim_trimmed.txt (RM8)</p>	<p>Ehf_3056.2_v2_prim_trimmed.txt (RM8)</p>	<p>Ehf_3056.2_RM8_combined_prim_trimmed.txt (RM8)            No PWM Available</p>
<p>Ehf_3056.2_v1_sec_trimmed.txt (RM8)</p>	<p>Ehf_3056.2_v2_sec_trimmed.txt (RM8)</p>	<p>Ehf_3056.2_RM8_combined_sec_trimmed.txt (RM8)            No PWM Available</p>
<p>Ehf_3056.2_v1_prim_untrimmed.txt (RM8)</p>	<p>Ehf_3056.2_v2_prim_untrimmed.txt (RM8)</p>	<p>Ehf_3056.2_RM8_combined_prim_untrimmed.txt (RM8)            No PWM Available</p>
<p>Ehf_3056.2_v1_sec_untrimmed.txt (RM8)</p>	<p>Ehf_3056.2_v2_sec_untrimmed.txt (RM8)  <b>[Z-Beta = 41.7321]</b>  <b>[Pearson Full 0.621230996, Pearson Zeroed 0.601505]</b></p>	<p>Ehf_3056.2_RM8_combined_sec_untrimmed.txt (RM8)            No PWM Available</p>




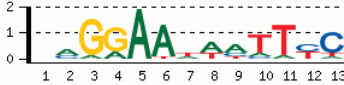

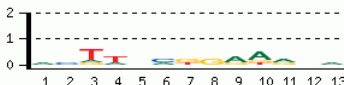

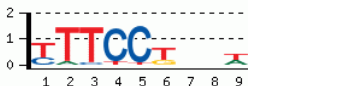
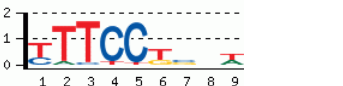
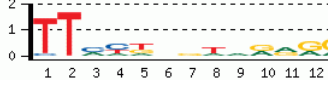
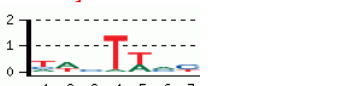
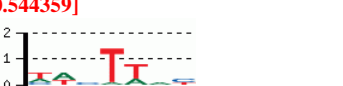

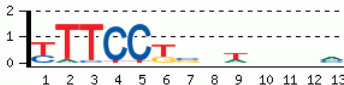
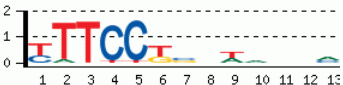

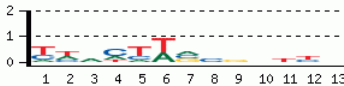
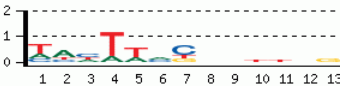






<p>Ehf_3056.2_v1_RM35_prim_trimmed.txt (RM35)</p>	<p>Ehf_3056.2_v2_RM35_prim_trimmed.txt (RM35)</p> <p>[Z-Beta = 8.79519] [Pearson Full 0.621230996, Pearson Zeroed 0.618663]</p>	<p>Ehf_3056.2_RM_combined_prim_trimmed.txt (RM35)</p>
<p>Ehf_3056.2_v1_RM35_sec_trimmed.txt (RM35)</p>	<p>Ehf_3056.2_v2_RM35_sec_trimmed.txt (RM35)</p>	<p>Ehf_3056.2_RM_combined_sec_trimmed.txt (RM35)</p> <p>[Z-Beta = 34.0129] [Pearson Full 0.80667412, Pearson Zeroed 0.803901]</p>
<p>Ehf_3056.2_v1_RM35_prim_untrimmed.txt (RM35)</p>	<p>Ehf_3056.2_v2_RM35_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = 35.5034] [Pearson Full 0.621230996, Pearson Zeroed 0.600418]</p>	<p>Ehf_3056.2_RM_combined_prim_untrimmed.txt (RM35)</p>
<p>Ehf_3056.2_v1_RM35_sec_untrimmed.txt (RM35)</p> <p>[Z-Beta = 100.617] [Pearson Full 0.816362204, Pearson Zeroed 0.808367]</p>	<p>Ehf_3056.2_v2_RM35_sec_untrimmed.txt (RM35)</p> <p>[Z-Beta = 56.3042] [Pearson Full 0.621230996, Pearson Zeroed 0.580996]</p>	<p>Ehf_3056.2_RM_combined_sec_untrimmed.txt (RM35)</p> <p>[Z-Beta = 43.8611] [Pearson Full 0.80667412, Pearson Zeroed 0.802324]</p>
<p>Ehf_3056.2_PWM_1.txt (K-afal)</p> <p>[Z-Beta = 284.711] [Pearson Full 0.816362204, Pearson Zeroed 0.779811]</p>	<p>Ehf_3056.2_PWM_1.txt (K-afal)</p> <p>[Z-Beta = 15.9718] [Pearson Full 0.621230996, Pearson Zeroed 0.618118]</p>	<p>Ehf_3056.2_combined_PWM_1.txt (K-afal)</p> <p>[Z-Beta = 494.201] [Pearson Full 0.80667412, Pearson Zeroed 0.680313]</p>
<p>Ehf_3056.2_PWM_2.txt (K-afal)</p> <p>[Z-Beta = 347.914] [Pearson Full 0.816362204, Pearson Zeroed 0.763808]</p>	<p>Ehf_3056.2_PWM_2.txt (K-afal)</p>	<p>Ehf_3056.2_combined_PWM_2.txt (K-afal)</p> <p>[Z-Beta = 4.60662] [Pearson Full 0.80667412, Pearson Zeroed 0.806278]</p>
<p>Ehf_3056.2_PWM_3.txt (K-afal)</p> <p>[Z-Beta = 54.6735] [Pearson Full 0.816362204, Pearson Zeroed 0.813722]</p>	<p>Ehf_3056.2_PWM_3.txt (K-afal)</p> <p>No PWM Available</p>	<p>Ehf_3056.2_combined_PWM_3.txt (K-afal)</p> <p>[Z-Beta = 39.0234] [Pearson Full 0.80667412, Pearson Zeroed 0.802474]</p>
<p>Ehf_3056.2_PWM_4.txt (K-afal)</p>	<p>Ehf_3056.2_PWM_4.txt (K-afal)</p> <p>No PWM Available</p>	<p>Ehf_3056.2_combined_PWM_4.txt (K-afal)</p>











<p><b>Ehf_3056.2_PWM_5.txt (K-afal)</b></p> 	<p><b>Ehf_3056.2_PWM_5.txt (K-afal)</b> No PWM Available</p>	<p><b>Ehf_3056.2_combined_PWM_5.txt (K-afal)</b></p> 
<p><b>Ehf_3056.2_PWM_6.txt (K-afal)</b> [Z-Beta = 75.103] [Pearson Full 0.816362204, Pearson Zeroed 0.808203]</p> 	<p><b>Ehf_3056.2_PWM_6.txt (K-afal)</b> No PWM Available</p>	<p><b>Ehf_3056.2_combined_PWM_6.txt (K-afal)</b> No PWM Available</p>


















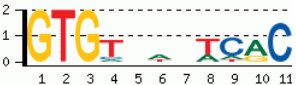
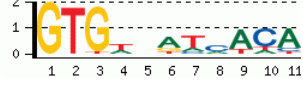

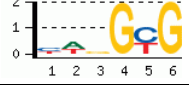
# Elf3\_3876.1

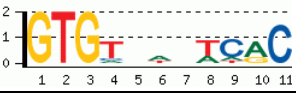

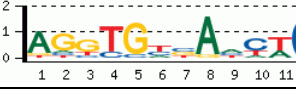

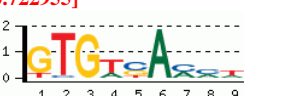


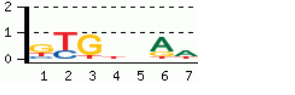


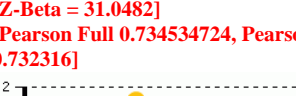


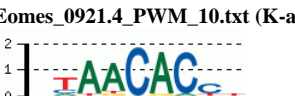


Array Version 1	Array Version 2	Combined
<p>Elf3_3876.1_v1_prim_auc.txt (SW) [Z-Beta = 4.69263] [Pearson Full 0.636404343, Pearson Zeroed 0.634783]</p>	<p>Elf3_3876.1_v2_prim_auc.txt (SW) [Z-Beta = -4.41613] [Pearson Full 0.474643247, Pearson Zeroed 0.474366]</p>	<p>Elf3_3876.1_combined_prim_auc.txt (SW)</p>
<p>Elf3_3876.1_v1_prim_ic.txt (SW)</p>	<p>Elf3_3876.1_v2_prim_ic.txt (SW) [Z-Beta = -10.2513] [Pearson Full 0.474643247, Pearson Zeroed 0.47306]</p>	<p>Elf3_3876.1_combined_prim_ic_trimmed.txt (SW) [Z-Beta = 6.72729] [Pearson Full 0.675728027, Pearson Zeroed 0.675283]</p>
<p>Elf3_3876.1_v1_prim_untrimmed.txt (SW)</p>	<p>Elf3_3876.1_v2_prim_untrimmed.txt (SW) [Z-Beta = 6.56975] [Pearson Full 0.474643247, Pearson Zeroed 0.473905]</p>	<p>Elf3_3876.1_combined_prim_untrimmed.txt (SW) [Z-Beta = 68.6597] [Pearson Full 0.675728027, Pearson Zeroed 0.60696]</p>
<p>Elf3_3876.1_v1_sec_auc.txt (SW) [Z-Beta = 8.58249] [Pearson Full 0.636404343, Pearson Zeroed 0.627328]</p>	<p>Elf3_3876.1_v2_sec_auc.txt (SW) [Z-Beta = 67.1456] [Pearson Full 0.474643247, Pearson Zeroed 0.386178]</p>	<p>Elf3_3876.1_combined_sec_auc.txt (SW)</p>
<p>Elf3_3876.1_v1_sec_ic.txt (SW) [Z-Beta = 53.3327] [Pearson Full 0.636404343, Pearson Zeroed 0.531881]</p>	<p>Elf3_3876.1_v2_sec_ic.txt (SW) [Z-Beta = -46.4858] [Pearson Full 0.474643247, Pearson Zeroed 0.449043]</p>	<p>Elf3_3876.1_combined_sec_ic_trimmed.txt (SW) [Z-Beta = 85.5031] [Pearson Full 0.675728027, Pearson Zeroed 0.543693]</p>
<p>Elf3_3876.1_v1_sec_untrimmed.txt (SW)</p>	<p>Elf3_3876.1_v2_sec_untrimmed.txt (SW) [Z-Beta = 18.7519] [Pearson Full 0.474643247, Pearson Zeroed 0.470628]</p>	<p>Elf3_3876.1_combined_sec_untrimmed.txt (SW) [Z-Beta = -29.5785] [Pearson Full 0.675728027, Pearson Zeroed 0.665198]</p>
<p>Elf3_3876.1_v1_prim_trimmed.txt (RM8)</p>	<p>Elf3_3876.1_v2_prim_trimmed.txt (RM8) [Z-Beta = 7.03859] [Pearson Full 0.474643247, Pearson Zeroed 0.473889]</p>	<p>Elf3_3876.1_RM8_combined_prim_trimmed.txt (RM8) No PWM Available</p>
<p>Elf3_3876.1_v1_sec_trimmed.txt (RM8)</p>	<p>Elf3_3876.1_v2_sec_trimmed.txt (RM8) [Z-Beta = -7.85375] [Pearson Full 0.474643247, Pearson Zeroed 0.473455]</p>	<p>Elf3_3876.1_RM8_combined_sec_trimmed.txt (RM8) No PWM Available</p>






<p>Elf3_3876.1_v1_prim_untrimmed.txt (RM8)</p> 	<p>Elf3_3876.1_v2_prim_untrimmed.txt (RM8)  [Z-Beta = 6.56975]  [Pearson Full 0.474643247, Pearson Zeroed 0.473905]</p> 	<p>Elf3_3876.1_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Elf3_3876.1_v1_sec_untrimmed.txt (RM8)  [Z-Beta = 37.8873]  [Pearson Full 0.636404343, Pearson Zeroed 0.5884]</p> 	<p>Elf3_3876.1_v2_sec_untrimmed.txt (RM8)  [Z-Beta = 18.7519]  [Pearson Full 0.474643247, Pearson Zeroed 0.470628]</p> 	<p>Elf3_3876.1_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>
<p>Elf3_3876.1_v1_RM35_prim_trimmed.txt (RM35)</p> 	<p>Elf3_3876.1_v2_RM35_prim_trimmed.txt (RM35)  [Z-Beta = -9.81264]  [Pearson Full 0.474643247, Pearson Zeroed 0.473498]</p> 	<p>Elf3_3876.1_RM_combined_prim_trimmed.txt (RM35)  [Z-Beta = -16.0465]  [Pearson Full 0.675728027, Pearson Zeroed 0.673182]</p> 
<p>Elf3_3876.1_v1_RM35_sec_trimmed.txt (RM35)</p> 	<p>Elf3_3876.1_v2_RM35_sec_trimmed.txt (RM35)  [Z-Beta = 33.2194]  [Pearson Full 0.474643247, Pearson Zeroed 0.458639]</p> 	<p>Elf3_3876.1_RM_combined_sec_trimmed.txt (RM35)  [Z-Beta = 89.3004]  [Pearson Full 0.675728027, Pearson Zeroed 0.544359]</p> 
<p>Elf3_3876.1_v1_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Elf3_3876.1_v2_RM35_prim_untrimmed.txt (RM35)  [Z-Beta = 34.3035]  [Pearson Full 0.474643247, Pearson Zeroed 0.456813]</p> 	<p>Elf3_3876.1_RM_combined_prim_untrimmed.txt (RM35)  [Z-Beta = 61.1387]  [Pearson Full 0.675728027, Pearson Zeroed 0.622306]</p> 
<p>Elf3_3876.1_v1_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Elf3_3876.1_v2_RM35_sec_untrimmed.txt (RM35)  [Z-Beta = 1.64125]  [Pearson Full 0.474643247, Pearson Zeroed 0.474604]</p> 	<p>Elf3_3876.1_RM_combined_sec_untrimmed.txt (RM35)  [Z-Beta = -13.3863]  [Pearson Full 0.675728027, Pearson Zeroed 0.673878]</p> 
<p>Elf3_3876.1_PWM_1.txt (K-afal)  [Z-Beta = 35.9585]  [Pearson Full 0.636404343, Pearson Zeroed 0.640045]</p> 	<p>Elf3_3876.1_PWM_1.txt (K-afal)  [Z-Beta = 15.7596]  [Pearson Full 0.474643247, Pearson Zeroed 0.471541]</p> 	<p>Elf3_3876.1_combined_PWM_1.txt (K-afal)  [Z-Beta = 5.72362]  [Pearson Full 0.675728027, Pearson Zeroed 0.675331]</p> 
<p>Elf3_3876.1_PWM_2.txt (K-afal)  [Z-Beta = 77.0606]  [Pearson Full 0.636404343, Pearson Zeroed 0.595916]</p> 	<p>Elf3_3876.1_PWM_2.txt (K-afal)  [Z-Beta = 14.9481]  [Pearson Full 0.474643247, Pearson Zeroed 0.470672]</p> 	<p>Elf3_3876.1_combined_PWM_2.txt (K-afal)  [Z-Beta = 50.8017]  [Pearson Full 0.675728027, Pearson Zeroed 0.643764]</p> 

<p>Elf3_3876.1_PWM_3.txt (K-afal)</p> 	<p>Elf3_3876.1_PWM_3.txt (K-afal)  [Z-Beta = 19.5788]  [Pearson Full 0.474643247, Pearson Zeroed 0.467755]</p> 	<p>Elf3_3876.1_combined_PWM_3.txt (K-afal)  [Z-Beta = 25.9224]  [Pearson Full 0.675728027, Pearson Zeroed 0.664229]</p> 
<p>Elf3_3876.1_PWM_4.txt (Kafal)  No PWM Available</p>	<p>Elf3_3876.1_PWM_4.txt (Kafal)  [Z-Beta = 0.652404]  [Pearson Full 0.474643247, Pearson Zeroed 0.474638]</p> 	<p>Elf3_3876.1_combined_PWM_4.txt (Kafal)  [Z-Beta = 7.49043]  [Pearson Full 0.675728027, Pearson Zeroed 0.674959]</p> 
<p>Elf3_3876.1_PWM_5.txt (Kafal)  No PWM Available</p>	<p>Elf3_3876.1_PWM_5.txt (Kafal)  [Z-Beta = 9.98945]  [Pearson Full 0.474643247, Pearson Zeroed 0.473245]</p> 	<p>Elf3_3876.1_combined_PWM_5.txt (Kafal)  No PWM Available</p>
<p>Elf3_3876.1_PWM_6.txt (Kafal)  No PWM Available</p>	<p>Elf3_3876.1_PWM_6.txt (Kafal)  [Z-Beta = 17.7436]  [Pearson Full 0.474643247, Pearson Zeroed 0.469916]</p> 	<p>Elf3_3876.1_combined_PWM_6.txt (Kafal)  No PWM Available</p>
<p>Elf3_3876.1_PWM_7.txt (Kafal)  No PWM Available</p>	<p>Elf3_3876.1_PWM_7.txt (Kafal)  [Z-Beta = 17.3391]  [Pearson Full 0.474643247, Pearson Zeroed 0.469314]</p> 	<p>Elf3_3876.1_combined_PWM_7.txt (Kafal)  No PWM Available</p>











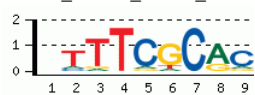




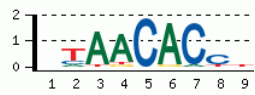

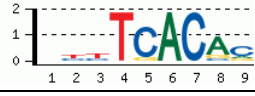





# Eomes\_0921.4

Array Version 1	Array Version 2	Combined
<p>Eomes_0921.4_v1_prim_auc.txt (SW)  [Z-Beta = 677.861]  [Pearson Full 0.734534724, Pearson Zeroed 0.00594444]</p> 	<p>Eomes_0921.4_v2_prim_auc.txt (SW)</p> 	<p>Eomes_0921.4_combined_prim_auc.txt (SW)  No PWM Available</p>
<p>Eomes_0921.4_v1_prim_ic.txt (SW)  [Z-Beta = -755.446]  [Pearson Full 0.734534724, Pearson Zeroed 0.488088]</p> 	<p>Eomes_0921.4_v2_prim_ic.txt (SW)</p> 	<p>Eomes_0921.4_combined_prim_ic_trimmed.txt (SW)</p> 
<p>Eomes_0921.4_v1_prim_untrimmed.txt (SW)  [Z-Beta = 102.65]  [Pearson Full 0.734534724, Pearson Zeroed 0.700102]</p> 	<p>Eomes_0921.4_v2_prim_untrimmed.txt (SW)</p> 	<p>Eomes_0921.4_combined_prim_untrimmed.txt (SW)</p> 
<p>Eomes_0921.4_v1_sec_auc.txt (SW)  [Z-Beta = 67.4858]  [Pearson Full 0.734534724, Pearson Zeroed 0.717634]</p> 	<p>Eomes_0921.4_v2_sec_auc.txt (SW)</p> 	<p>Eomes_0921.4_combined_sec_auc.txt (SW)</p> 
<p>Eomes_0921.4_v1_sec_ic.txt (SW)  [Z-Beta = -3.5487]  [Pearson Full 0.734534724, Pearson Zeroed 0.734487]</p> 	<p>Eomes_0921.4_v2_sec_ic.txt (SW)</p> 	<p>Eomes_0921.4_combined_sec_ic_trimmed.txt (SW)</p> 
<p>Eomes_0921.4_v1_sec_untrimmed.txt (SW)  [Z-Beta = -39.5862]  [Pearson Full 0.734534724, Pearson Zeroed 0.729539]</p> 	<p>Eomes_0921.4_v2_sec_untrimmed.txt (SW)</p> 	<p>Eomes_0921.4_combined_sec_untrimmed.txt (SW)</p> 
<p>Eomes_0921.4_v1_prim_trimmed.txt (RM8)  [Z-Beta = 15.9343]  [Pearson Full 0.734534724, Pearson Zeroed 0.733821]</p> 	<p>Eomes_0921.4_v2_prim_trimmed.txt (RM8)</p> 	<p>Eomes_0921.4_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Eomes_0921.4_v1_sec_trimmed.txt (RM8)</p> 	<p>Eomes_0921.4_v2_sec_trimmed.txt (RM8)</p> 	<p>Eomes_0921.4_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Eomes_0921.4_v1_prim_untrimmed.txt (RM8)</p>	<p>Eomes_0921.4_v2_prim_untrimmed.txt (RM8)</p>	<p>Eomes_0921.4_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>

 <p>Eomes_0921.4_v1_sec_untrimmed.txt (RM8) [Z-Beta = 51.2838] [Pearson Full 0.734534724, Pearson Zeroed 0.728169]</p>	 <p>Eomes_0921.4_v2_sec_untrimmed.txt (RM8)</p>	<p>Eomes_0921.4_RM8_combined_sec_untrimmed.txt (RM8) No PWM Available</p>
 <p>Eomes_0921.4_v1_RM35_prim_trimmed.txt (RM35) [Z-Beta = 61.0693] [Pearson Full 0.734534724, Pearson Zeroed 0.722955]</p>	 <p>Eomes_0921.4_v2_RM35_prim_trimmed.txt (RM35) [Z-Beta = 123.163] [Pearson Full 0.68239163, Pearson Zeroed 0.6733]</p>	<p>Eomes_0921.4_RM_combined_prim_trimmed.txt (RM35) [Z-Beta = 383.054] [Pearson Full 0.69888076, Pearson Zeroed 0.39648]</p>
 <p>Eomes_0921.4_v1_RM35_sec_trimmed.txt (RM35) [Z-Beta = 23.7891] [Pearson Full 0.734534724, Pearson Zeroed 0.733596]</p>	 <p>Eomes_0921.4_v2_RM35_sec_trimmed.txt (RM35)</p>	<p>Eomes_0921.4_RM_combined_sec_trimmed.txt (RM35) [Z-Beta = 2.92137] [Pearson Full 0.69888076, Pearson Zeroed 0.696628]</p>
 <p>Eomes_0921.4_v1_RM35_prim_untrimmed.txt (RM35) [Z-Beta = -62.618] [Pearson Full 0.734534724, Pearson Zeroed 0.725722]</p>	 <p>Eomes_0921.4_v2_RM35_prim_untrimmed.txt (RM35) [Z-Beta = 426.064] [Pearson Full 0.68239163, Pearson Zeroed 0.64884]</p>	<p>Eomes_0921.4_RM_combined_prim_untrimmed.txt (RM35)</p>
 <p>Eomes_0921.4_v1_RM35_sec_untrimmed.txt (RM35) [Z-Beta = 31.0482] [Pearson Full 0.734534724, Pearson Zeroed 0.732316]</p>	 <p>Eomes_0921.4_v2_RM35_sec_untrimmed.txt (RM35)</p>	<p>Eomes_0921.4_RM_combined_sec_untrimmed.txt (RM35)</p>
 <p>Eomes_0921.4_PWM_10.txt (K-afal) [Z-Beta = -99.7245] [Pearson Full 0.734534724, Pearson Zeroed 0.713752]</p>	 <p>Eomes_0921.4_PWM_10.txt (K-afal)</p>	<p>Eomes_0921.4_combined_PWM_10.txt (K-afal)</p>
 <p>Eomes_0921.4_PWM_11.txt (K-afal) [Z-Beta = 67.0689] [Pearson Full 0.734534724, Pearson Zeroed 0.718717]</p>	 <p>Eomes_0921.4_PWM_11.txt (K-afal)</p>	<p>Eomes_0921.4_combined_PWM_11.txt (K-afal)</p>
 <p>Eomes_0921.4_PWM_12.txt (K-afal) [Z-Beta = 3.40406] [Pearson Full 0.734534724, Pearson Zeroed 0.734505]</p>	 <p>Eomes_0921.4_PWM_12.txt (K-afal)</p>	<p>Eomes_0921.4_combined_PWM_12.txt (K-afal) No PWM Available</p>

		
<p><b>Eomes_0921.4_PWM_13.txt (K-afal)</b>  [Z-Beta = 59.0698]  [Pearson Full 0.734534724, Pearson Zeroed 0.726399]</p> 	<p><b>Eomes_0921.4_PWM_13.txt (K-afal)</b></p> 	<p><b>Eomes_0921.4_combined_PWM_13.txt (K-afal)</b>  No PWM Available</p>
<p><b>Eomes_0921.4_PWM_14.txt (K-afal)</b>  [Z-Beta = 88.1166]  [Pearson Full 0.734534724, Pearson Zeroed 0.708823]</p> 	<p><b>Eomes_0921.4_PWM_14.txt (K-afal)</b></p> 	<p><b>Eomes_0921.4_combined_PWM_14.txt (K-afal)</b>  No PWM Available</p>
<p><b>Eomes_0921.4_PWM_15.txt (K-afal)</b>  [Z-Beta = 22.207]  [Pearson Full 0.734534724, Pearson Zeroed 0.733494]</p> 	<p><b>Eomes_0921.4_PWM_15.txt (K-afal)</b></p> 	<p><b>Eomes_0921.4_combined_PWM_15.txt (K-afal)</b>  No PWM Available</p>
<p><b>Eomes_0921.4_PWM_16.txt (K-afal)</b>  [Z-Beta = 43.746]  [Pearson Full 0.734534724, Pearson Zeroed 0.727569]</p> 	<p><b>Eomes_0921.4_PWM_16.txt (K-afal)</b></p> 	<p><b>Eomes_0921.4_combined_PWM_16.txt (K-afal)</b>  No PWM Available</p>
<p><b>Eomes_0921.4_PWM_1.txt (K-afal)</b>  [Z-Beta = 115.327]  [Pearson Full 0.734534724, Pearson Zeroed 0.696219]</p> 	<p><b>Eomes_0921.4_PWM_1.txt (K-afal)</b></p> 	<p><b>Eomes_0921.4_combined_PWM_1.txt (K-afal)</b></p> 
<p><b>Eomes_0921.4_PWM_2.txt (K-afal)</b>  [Z-Beta = 27.7578]  [Pearson Full 0.734534724, Pearson Zeroed 0.732237]</p> 	<p><b>Eomes_0921.4_PWM_2.txt (K-afal)</b></p> 	<p><b>Eomes_0921.4_combined_PWM_2.txt (K-afal)</b></p> 
<p><b>Eomes_0921.4_PWM_3.txt (K-afal)</b>  [Z-Beta = 66.3804]  [Pearson Full 0.734534724, Pearson Zeroed 0.719835]</p> 	<p><b>Eomes_0921.4_PWM_3.txt (K-afal)</b></p> 	<p><b>Eomes_0921.4_combined_PWM_3.txt (K-afal)</b></p> 
<p><b>Eomes_0921.4_PWM_4.txt (K-afal)</b>  [Z-Beta = 14.5416]  [Pearson Full 0.734534724, Pearson Zeroed 0.73399]</p> 	<p><b>Eomes_0921.4_PWM_4.txt (K-afal)</b></p> 	<p><b>Eomes_0921.4_combined_PWM_4.txt (K-afal)</b></p> 



<p>Eomes_0921.4_PWM_5.txt (K-afal)  [Z-Beta = 54.6907]  [Pearson Full 0.734534724, Pearson Zeroed 0.724284]</p> 	<p>Eomes_0921.4_PWM_5.txt (K-afal)</p> 	<p>Eomes_0921.4_combined_PWM_5.txt (K-afal)</p> 
<p>Eomes_0921.4_PWM_6.txt (K-afal)  [Z-Beta = 28.3342]  [Pearson Full 0.734534724, Pearson Zeroed 0.731666]</p> 	<p>Eomes_0921.4_PWM_6.txt (K-afal)</p> 	<p>Eomes_0921.4_combined_PWM_6.txt (K-afal)</p> 
<p>Eomes_0921.4_PWM_7.txt (K-afal)  [Z-Beta = -0.291773]  [Pearson Full 0.734534724, Pearson Zeroed 0.734534]</p> 	<p>Eomes_0921.4_PWM_7.txt (K-afal)</p> 	<p>Eomes_0921.4_combined_PWM_7.txt (K-afal)</p> 
<p>Eomes_0921.4_PWM_8.txt (K-afal)  [Z-Beta = 133.909]  [Pearson Full 0.734534724, Pearson Zeroed 0.669089]</p> 	<p>Eomes_0921.4_PWM_8.txt (K-afal)</p> 	<p>Eomes_0921.4_combined_PWM_8.txt (K-afal)</p> 
<p>Eomes_0921.4_PWM_9.txt (K-afal)  [Z-Beta = 21.5335]  [Pearson Full 0.734534724, Pearson Zeroed 0.733023]</p> 	<p>Eomes_0921.4_PWM_9.txt (K-afal)</p> 	<p>Eomes_0921.4_combined_PWM_9.txt (K-afal)</p> 
<p>Eomes_0921.4_PWM_10.txt (Kafal)  No PWM Available</p>	<p>Eomes_0921.4_PWM_10.txt (Kafal)</p> 	<p>Eomes_0921.4_combined_PWM_10.txt (Kafal)</p> 
<p>Eomes_0921.4_PWM_11.txt (Kafal)  No PWM Available</p>	<p>Eomes_0921.4_PWM_11.txt (Kafal)</p> 	<p>Eomes_0921.4_combined_PWM_11.txt (Kafal)</p> 
<p>Eomes_0921.4_PWM_12.txt (Kafal)  No PWM Available</p>	<p>Eomes_0921.4_PWM_12.txt (Kafal)</p> 	<p>Eomes_0921.4_combined_PWM_12.txt (Kafal)  No PWM Available</p>
<p>Eomes_0921.4_PWM_13.txt (Kafal)  No PWM Available</p>	<p>Eomes_0921.4_PWM_13.txt (Kafal)</p> 	<p>Eomes_0921.4_combined_PWM_13.txt (Kafal)  No PWM Available</p>
<p>Eomes_0921.4_PWM_14.txt (Kafal)  No PWM Available</p>	<p>Eomes_0921.4_PWM_14.txt (Kafal)</p> 	<p>Eomes_0921.4_combined_PWM_14.txt (Kafal)  No PWM Available</p>
<p>Eomes_0921.4_PWM_15.txt (Kafal)  No PWM Available</p>	<p>Eomes_0921.4_PWM_15.txt (Kafal)</p> 	<p>Eomes_0921.4_combined_PWM_15.txt (Kafal)  No PWM Available</p>

Eomes\_0921.4\_PWM\_16.txt (Kafal)  
No PWM Available





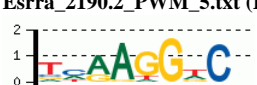
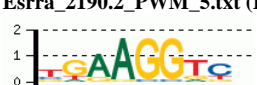
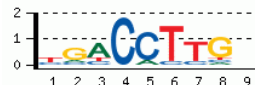
Eomes\_0921.4\_PWM\_16.txt (Kafal)







Eomes\_0921.4\_combined\_PWM\_16.txt (Kafal)  
No PWM Available

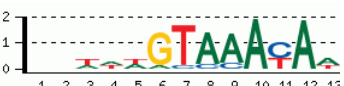



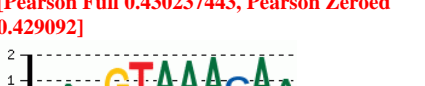






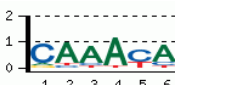
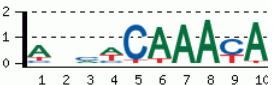
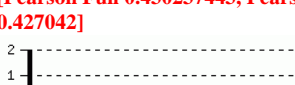




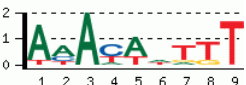



# Esrra\_2190.2


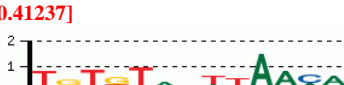
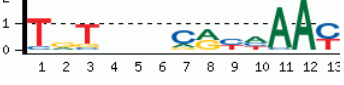



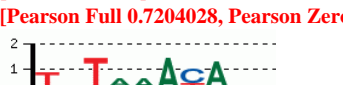

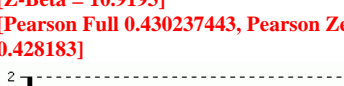
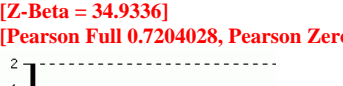
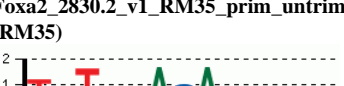
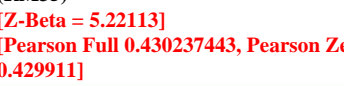

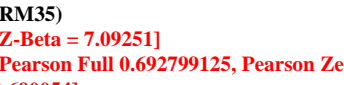
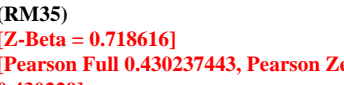
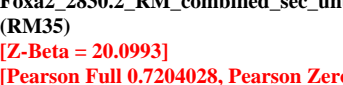
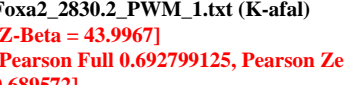
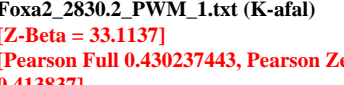

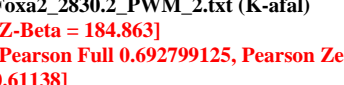
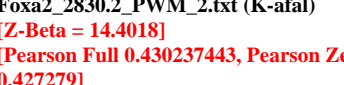

Array Version 1	Array Version 2	Combined
<p>Esrra_2190.2_v1_prim_auc.txt (SW)</p>	<p>Esrra_2190.2_v2_prim_auc.txt (SW)  [Z-Beta = 290.187]  [Pearson Full 0.68146608, Pearson Zeroed 0.569826]</p>	<p>Esrra_2190.2_combined_prim_auc.txt (SW)  No PWM Available</p>
<p>Esrra_2190.2_v1_prim_ic.txt (SW)  [Z-Beta = 28.267]  [Pearson Full 0.717854544, Pearson Zeroed 0.712294]</p>	<p>Esrra_2190.2_v2_prim_ic.txt (SW)</p>	<p>Esrra_2190.2_combined_prim_ic_trimmed.txt (SW)</p>
<p>Esrra_2190.2_v1_prim_untrimmed.txt (SW)</p>	<p>Esrra_2190.2_v2_prim_untrimmed.txt (SW)</p>	<p>Esrra_2190.2_combined_prim_untrimmed.txt (SW)</p>
<p>Esrra_2190.2_v1_sec_auc.txt (SW)</p>	<p>Esrra_2190.2_v2_sec_auc.txt (SW)</p>	<p>Esrra_2190.2_combined_sec_auc.txt (SW)</p>
<p>Esrra_2190.2_v1_sec_ic.txt (SW)</p>	<p>Esrra_2190.2_v2_sec_ic.txt (SW)</p>	<p>Esrra_2190.2_combined_sec_ic_trimmed.txt (SW)</p>
<p>Esrra_2190.2_v1_sec_untrimmed.txt (SW)</p>	<p>Esrra_2190.2_v2_sec_untrimmed.txt (SW)</p>	<p>Esrra_2190.2_combined_sec_untrimmed.txt (SW)</p>
<p>Esrra_2190.2_v1_prim_trimmed.txt (RM8)</p>	<p>Esrra_2190.2_v2_prim_trimmed.txt (RM8)</p>	<p>Esrra_2190.2_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Esrra_2190.2_v1_sec_trimmed.txt (RM8)</p>	<p>Esrra_2190.2_v2_sec_trimmed.txt (RM8)</p>	<p>Esrra_2190.2_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Esrra_2190.2_v1_prim_untrimmed.txt (RM8)</p>	<p>Esrra_2190.2_v2_prim_untrimmed.txt (RM8)</p>	<p>Esrra_2190.2_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Esrra_2190.2_v1_sec_untrimmed.txt (RM8)</p>	<p>Esrra_2190.2_v2_sec_untrimmed.txt (RM8)</p>	<p>Esrra_2190.2_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>
<p>Esrra_2190.2_v1_RM35_prim_trimmed.txt (RM35)  [Z-Beta = 83.0627]  [Pearson Full 0.717854544, Pearson Zeroed 0.707275]</p>	<p>Esrra_2190.2_v2_RM35_prim_trimmed.txt (RM35)</p>	<p>Esrra_2190.2_RM_combined_prim_trimmed.txt (RM35)</p>

		
<b>Esrra_2190.2_v1_RM35_sec_trimmed.txt (RM35)</b> [Z-Beta = 162.495] [Pearson Full 0.717854544, Pearson Zeroed 0.703618]	<b>Esrra_2190.2_v2_RM35_sec_trimmed.txt (RM35)</b> 	<b>Esrra_2190.2_RM_combined_sec_trimmed.txt (RM35)</b> 
<b>Esrra_2190.2_v1_RM35_prim_untrimmed.txt (RM35)</b> 	<b>Esrra_2190.2_v2_RM35_prim_untrimmed.txt (RM35)</b> 	<b>Esrra_2190.2_RM_combined_prim_untrimmed.txt (RM35)</b> 
<b>Esrra_2190.2_v1_RM35_sec_untrimmed.txt (RM35)</b> 	<b>Esrra_2190.2_v2_RM35_sec_untrimmed.txt (RM35)</b> 	<b>Esrra_2190.2_RM_combined_sec_untrimmed.txt (RM35)</b> 
<b>Esrra_2190.2_PWM_10.txt (K-afal)</b> 	<b>Esrra_2190.2_PWM_10.txt (K-afal)</b> No PWM Available	<b>Esrra_2190.2_combined_PWM_10.txt (K-afal)</b> No PWM Available
<b>Esrra_2190.2_PWM_1.txt (K-afal)</b> 	<b>Esrra_2190.2_PWM_1.txt (K-afal)</b> [Z-Beta = 403.374] [Pearson Full 0.68146608, Pearson Zeroed 0.644462]	<b>Esrra_2190.2_combined_PWM_1.txt (K-afal)</b> 
<b>Esrra_2190.2_PWM_2.txt (K-afal)</b> [Z-Beta = 414.374] [Pearson Full 0.717854544, Pearson Zeroed 0.731263]	<b>Esrra_2190.2_PWM_2.txt (K-afal)</b> 	<b>Esrra_2190.2_combined_PWM_2.txt (K-afal)</b> 
<b>Esrra_2190.2_PWM_3.txt (K-afal)</b> 	<b>Esrra_2190.2_PWM_3.txt (K-afal)</b> [Z-Beta = 150.514] [Pearson Full 0.68146608, Pearson Zeroed 0.688201]	<b>Esrra_2190.2_combined_PWM_3.txt (K-afal)</b> 
<b>Esrra_2190.2_PWM_4.txt (K-afal)</b> 	<b>Esrra_2190.2_PWM_4.txt (K-afal)</b> 	<b>Esrra_2190.2_combined_PWM_4.txt (K-afal)</b> [Z-Beta = 798.779] [Pearson Full 0.692804154, Pearson Zeroed NaN]
<b>Esrra_2190.2_PWM_5.txt (K-afal)</b> 	<b>Esrra_2190.2_PWM_5.txt (K-afal)</b> 	<b>Esrra_2190.2_combined_PWM_5.txt (K-afal)</b> No PWM Available
<b>Esrra_2190.2_PWM_6.txt (K-afal)</b> 	<b>Esrra_2190.2_PWM_6.txt (K-afal)</b> 	<b>Esrra_2190.2_combined_PWM_6.txt (K-afal)</b> No PWM Available




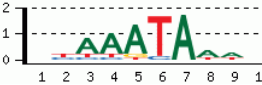
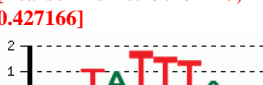
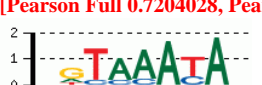




<p>Esrra_2190.2_PWM_7.txt (K-afal)</p> 	<p>Esrra_2190.2_PWM_7.txt (K-afal)</p> 	<p>Esrra_2190.2_combined_PWM_7.txt (K-afal) No PWM Available</p>
<p>Esrra_2190.2_PWM_8.txt (K-afal) [Z-Beta = 109.56] [Pearson Full 0.717854544, Pearson Zeroed 0.707304]</p> 	<p>Esrra_2190.2_PWM_8.txt (K-afal) No PWM Available</p>	<p>Esrra_2190.2_combined_PWM_8.txt (K-afal) No PWM Available</p>
<p>Esrra_2190.2_PWM_9.txt (K-afal)</p> 	<p>Esrra_2190.2_PWM_9.txt (K-afal) No PWM Available</p>	<p>Esrra_2190.2_combined_PWM_9.txt (K-afal) No PWM Available</p>

# Foxa2\_2830.2

Array Version 1	Array Version 2	Combined
<p>Foxa2_2830.2_v1_prim_auc.txt (SW)  [Z-Beta = 55.0737]  [Pearson Full 0.692799125, Pearson Zeroed 0.666342]</p> 	<p>Foxa2_2830.2_v2_prim_auc.txt (SW)  [Z-Beta = 90.1188]  [Pearson Full 0.430237443, Pearson Zeroed 0.285228]</p> 	<p>Foxa2_2830.2_combined_prim_auc.txt (SW)</p> 
<p>Foxa2_2830.2_v1_prim_ic.txt (SW)</p> 	<p>Foxa2_2830.2_v2_prim_ic.txt (SW)  [Z-Beta = -10.0584]  [Pearson Full 0.430237443, Pearson Zeroed 0.429092]</p> 	<p>Foxa2_2830.2_combined_prim_ic_trimmed.txt (SW)</p> 
<p>Foxa2_2830.2_v1_prim_untrimmed.txt (SW)  [Z-Beta = 41.3162]  [Pearson Full 0.692799125, Pearson Zeroed 0.673053]</p> 	<p>Foxa2_2830.2_v2_prim_untrimmed.txt (SW)  [Z-Beta = 37.1582]  [Pearson Full 0.430237443, Pearson Zeroed 0.41237]</p> 	<p>Foxa2_2830.2_combined_prim_untrimmed.txt (SW)</p> 
<p>Foxa2_2830.2_v1_sec_auc.txt (SW)</p> 	<p>Foxa2_2830.2_v2_sec_auc.txt (SW)  [Z-Beta = 24.4745]  [Pearson Full 0.430237443, Pearson Zeroed 0.419739]</p> 	<p>Foxa2_2830.2_combined_sec_auc.txt (SW)</p> 
<p>Foxa2_2830.2_v1_sec_ic.txt (SW)</p> 	<p>Foxa2_2830.2_v2_sec_ic.txt (SW)  [Z-Beta = 13.4007]  [Pearson Full 0.430237443, Pearson Zeroed 0.427042]</p> 	<p>Foxa2_2830.2_combined_sec_ic_trimmed.txt (SW)</p> 
<p>Foxa2_2830.2_v1_sec_untrimmed.txt (SW)</p> 	<p>Foxa2_2830.2_v2_sec_untrimmed.txt (SW)  [Z-Beta = 17.9524]  [Pearson Full 0.430237443, Pearson Zeroed 0.4249]</p> 	<p>Foxa2_2830.2_combined_sec_untrimmed.txt (SW)</p> 
<p>Foxa2_2830.2_v1_prim_trimmed.txt (RM8)</p> 	<p>Foxa2_2830.2_v2_prim_trimmed.txt (RM8)  [Z-Beta = 6.16721]  [Pearson Full 0.430237443, Pearson Zeroed 0.429767]</p> 	<p>Foxa2_2830.2_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Foxa2_2830.2_v1_sec_trimmed.txt (RM8)</p> 	<p>Foxa2_2830.2_v2_sec_trimmed.txt (RM8)  [Z-Beta = -6.958]  [Pearson Full 0.430237443, Pearson Zeroed 0.429527]</p> 	<p>Foxa2_2830.2_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>

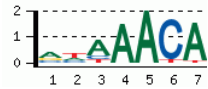
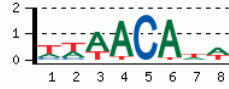
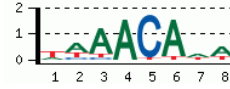

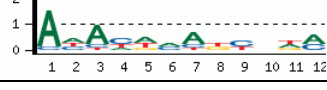
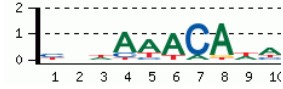

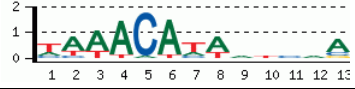





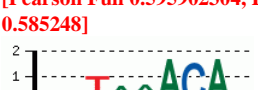





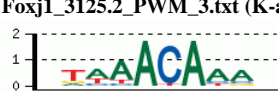

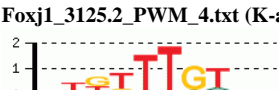
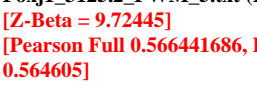

<p>Foxa2_2830.2_v1_prim_untrimmed.txt (RM8)</p> 	<p>Foxa2_2830.2_v2_prim_untrimmed.txt (RM8)</p> <p>[Z-Beta = 37.1582] [Pearson Full 0.430237443, Pearson Zeroed 0.41237]</p> 	<p>Foxa2_2830.2_RM8_combined_prim_untrimmed.txt (RM8)</p> <p>No PWM Available</p>
<p>Foxa2_2830.2_v1_sec_untrimmed.txt (RM8)</p> 	<p>Foxa2_2830.2_v2_sec_untrimmed.txt (RM8)</p> <p>[Z-Beta = 17.9524] [Pearson Full 0.430237443, Pearson Zeroed 0.4249]</p> 	<p>Foxa2_2830.2_RM8_combined_sec_untrimmed.txt (RM8)</p> <p>No PWM Available</p>
<p>Foxa2_2830.2_v1_RM35_prim_trimmed.txt (RM35)</p> 	<p>Foxa2_2830.2_v2_RM35_prim_trimmed.txt (RM35)</p> <p>[Z-Beta = 15.4625] [Pearson Full 0.430237443, Pearson Zeroed 0.427301]</p> 	<p>Foxa2_2830.2_RM_combined_prim_trimmed.txt (RM35)</p> <p>[Z-Beta = 17.52] [Pearson Full 0.7204028, Pearson Zeroed 0.711886]</p> 
<p>Foxa2_2830.2_v1_RM35_sec_trimmed.txt (RM35)</p> 	<p>Foxa2_2830.2_v2_RM35_sec_trimmed.txt (RM35)</p> <p>[Z-Beta = 10.9193] [Pearson Full 0.430237443, Pearson Zeroed 0.428183]</p> 	<p>Foxa2_2830.2_RM_combined_sec_trimmed.txt (RM35)</p> <p>[Z-Beta = 34.9336] [Pearson Full 0.7204028, Pearson Zeroed 0.701419]</p> 
<p>Foxa2_2830.2_v1_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Foxa2_2830.2_v2_RM35_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = 5.22113] [Pearson Full 0.430237443, Pearson Zeroed 0.429911]</p> 	<p>Foxa2_2830.2_RM_combined_prim_untrimmed.txt (RM35)</p> 
<p>Foxa2_2830.2_v1_RM35_sec_untrimmed.txt (RM35)</p> <p>[Z-Beta = 7.09251] [Pearson Full 0.692799125, Pearson Zeroed 0.690054]</p> 	<p>Foxa2_2830.2_v2_RM35_sec_untrimmed.txt (RM35)</p> <p>[Z-Beta = 0.718616] [Pearson Full 0.430237443, Pearson Zeroed 0.430229]</p> 	<p>Foxa2_2830.2_RM_combined_sec_untrimmed.txt (RM35)</p> <p>[Z-Beta = 20.0993] [Pearson Full 0.7204028, Pearson Zeroed 0.709938]</p> 
<p>Foxa2_2830.2_PWM_1.txt (K-afal)</p> <p>[Z-Beta = 43.9967] [Pearson Full 0.692799125, Pearson Zeroed 0.689572]</p> 	<p>Foxa2_2830.2_PWM_1.txt (K-afal)</p> <p>[Z-Beta = 33.1137] [Pearson Full 0.430237443, Pearson Zeroed 0.413837]</p> 	<p>Foxa2_2830.2_combined_PWM_1.txt (K-afal)</p> 
<p>Foxa2_2830.2_PWM_2.txt (K-afal)</p> <p>[Z-Beta = 184.863] [Pearson Full 0.692799125, Pearson Zeroed 0.61138]</p> 	<p>Foxa2_2830.2_PWM_2.txt (K-afal)</p> <p>[Z-Beta = 14.4018] [Pearson Full 0.430237443, Pearson Zeroed 0.427279]</p> 	<p>Foxa2_2830.2_combined_PWM_2.txt (K-afal)</p> 





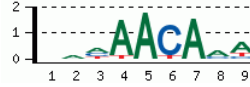
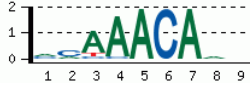


<p>Foxa2_2830.2_PWM_3.txt (K-afal)</p> 	<p>Foxa2_2830.2_PWM_3.txt (K-afal)  [Z-Beta = -23.8184]  [Pearson Full 0.430237443, Pearson Zeroed 0.427315]</p> 	<p>Foxa2_2830.2_combined_PWM_3.txt (K-afal)  [Z-Beta = 36.4907]  [Pearson Full 0.7204028, Pearson Zeroed 0.703947]</p> 
<p>Foxa2_2830.2_PWM_4.txt (K-afal)</p> 	<p>Foxa2_2830.2_PWM_4.txt (K-afal)  [Z-Beta = 15.401]  [Pearson Full 0.430237443, Pearson Zeroed 0.427166]</p> 	<p>Foxa2_2830.2_combined_PWM_4.txt (K-afal)  [Z-Beta = 114.995]  [Pearson Full 0.7204028, Pearson Zeroed 0.588135]</p> 
<p>Foxa2_2830.2_PWM_5.txt (K-afal)</p> 	<p>Foxa2_2830.2_PWM_5.txt (K-afal)  [Z-Beta = 91.4858]  [Pearson Full 0.430237443, Pearson Zeroed 0.295705]</p> 	<p>Foxa2_2830.2_combined_PWM_5.txt (K-afal)  [Z-Beta = 52.4606]  [Pearson Full 0.7204028, Pearson Zeroed 0.685268]</p> 
<p>Foxa2_2830.2_PWM_6.txt (K-afal)</p> 	<p>Foxa2_2830.2_PWM_6.txt (K-afal)  No PWM Available</p>	<p>Foxa2_2830.2_combined_PWM_6.txt (K-afal)  No PWM Available</p>

# Foxj1\_3125.2

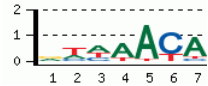
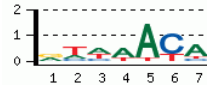
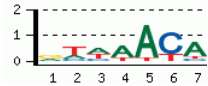

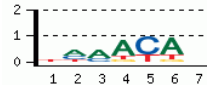
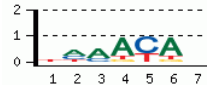
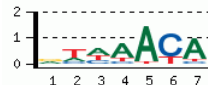
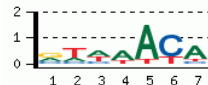
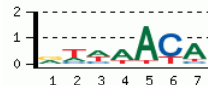
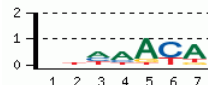
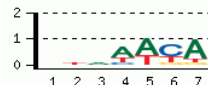
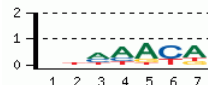

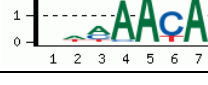
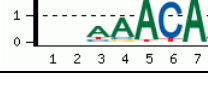
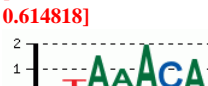







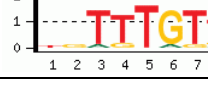
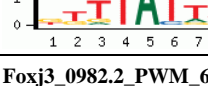
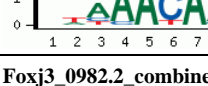
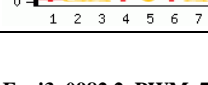
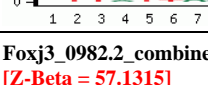


Array Version 1	Array Version 2	Combined
<p>Foxj1_3125.2_v1_prim_auc.txt (SW)</p>	<p>Foxj1_3125.2_v2_prim_auc.txt (SW)</p>	<p>Foxj1_3125.2_combined_prim_auc.txt (SW)</p>
<p>Foxj1_3125.2_v1_prim_ic.txt (SW)</p>	<p>Foxj1_3125.2_v2_prim_ic.txt (SW)</p>	<p>Foxj1_3125.2_combined_prim_ic_trimmed.txt (SW)</p>
<p>Foxj1_3125.2_v1_prim_untrimmed.txt (SW)</p>	<p>Foxj1_3125.2_v2_prim_untrimmed.txt (SW)</p>	<p>Foxj1_3125.2_combined_prim_untrimmed.txt (SW)</p>
<p>Foxj1_3125.2_v1_sec_auc.txt (SW)</p>	<p>Foxj1_3125.2_v2_sec_auc.txt (SW)</p>	<p>Foxj1_3125.2_combined_sec_auc.txt (SW)</p>
<p>Foxj1_3125.2_v1_sec_ic.txt (SW)</p>	<p>Foxj1_3125.2_v2_sec_ic.txt (SW)</p>	<p>Foxj1_3125.2_combined_sec_ic_trimmed.txt (SW)  [Z-Beta = 10.5515]  [Pearson Full 0.577696292, Pearson Zeroed 0.554635]</p>
<p>Foxj1_3125.2_v1_sec_untrimmed.txt (SW)</p>	<p>Foxj1_3125.2_v2_sec_untrimmed.txt (SW)  [Z-Beta = 71.3913]  [Pearson Full 0.595902304, Pearson Zeroed 0.525621]</p>	<p>Foxj1_3125.2_combined_sec_untrimmed.txt (SW)</p>
<p>Foxj1_3125.2_v1_prim_trimmed.txt (RM8)</p>	<p>Foxj1_3125.2_v2_prim_trimmed.txt (RM8)</p>	<p>Foxj1_3125.2_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Foxj1_3125.2_v1_sec_trimmed.txt (RM8)  [Z-Beta = 26.3873]  [Pearson Full 0.566441686, Pearson Zeroed 0.555024]</p>	<p>Foxj1_3125.2_v2_sec_trimmed.txt (RM8)</p>	<p>Foxj1_3125.2_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Foxj1_3125.2_v1_prim_untrimmed.txt (RM8)</p>	<p>Foxj1_3125.2_v2_prim_untrimmed.txt (RM8)</p>	<p>Foxj1_3125.2_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Foxj1_3125.2_v1_sec_untrimmed.txt (RM8)</p>	<p>Foxj1_3125.2_v2_sec_untrimmed.txt (RM8)  [Z-Beta = 71.3913]  [Pearson Full 0.595902304, Pearson Zeroed 0.525621]</p>	<p>Foxj1_3125.2_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>

<p>Foxj1_3125.2_v1_RM35_prim_trimmed.txt (RM35)  [Z-Beta = 47.4158]  [Pearson Full 0.566441686, Pearson Zeroed 0.53478]</p> 	<p>Foxj1_3125.2_v2_RM35_prim_trimmed.txt (RM35)</p> 	<p>Foxj1_3125.2_RM_combined_prim_trimmed.txt (RM35)</p> 
<p>Foxj1_3125.2_v1_RM35_sec_trimmed.txt (RM35)</p> 	<p>Foxj1_3125.2_v2_RM35_sec_trimmed.txt (RM35)  [Z-Beta = 13.8814]  [Pearson Full 0.595902304, Pearson Zeroed 0.58921]</p> 	<p>Foxj1_3125.2_RM_combined_sec_trimmed.txt (RM35)</p> 
<p>Foxj1_3125.2_v1_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Foxj1_3125.2_v2_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Foxj1_3125.2_RM_combined_prim_untrimmed.txt (RM35)</p> 
<p>Foxj1_3125.2_v1_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Foxj1_3125.2_v2_RM35_sec_untrimmed.txt (RM35)  [Z-Beta = 34.5602]  [Pearson Full 0.595902304, Pearson Zeroed 0.567527]</p> 	<p>Foxj1_3125.2_RM_combined_sec_untrimmed.txt (RM35)  [Z-Beta = 88.5866]  [Pearson Full 0.577696292, Pearson Zeroed 0.510963]</p> 
<p>Foxj1_3125.2_PWM_1.txt (K-afal)</p> 	<p>Foxj1_3125.2_PWM_1.txt (K-afal)  [Z-Beta = 6.97171]  [Pearson Full 0.595902304, Pearson Zeroed 0.585248]</p> 	<p>Foxj1_3125.2_combined_PWM_1.txt (K-afal)  [Z-Beta = 38.1884]  [Pearson Full 0.577696292, Pearson Zeroed 0.54991]</p> 
<p>Foxj1_3125.2_PWM_2.txt (K-afal)</p> 	<p>Foxj1_3125.2_PWM_2.txt (K-afal)  [Z-Beta = 14.9567]  [Pearson Full 0.595902304, Pearson Zeroed 0.578676]</p> 	<p>Foxj1_3125.2_combined_PWM_2.txt (K-afal)</p> 
<p>Foxj1_3125.2_PWM_3.txt (K-afal)  [Z-Beta = 43.9252]  [Pearson Full 0.566441686, Pearson Zeroed 0.563195]</p> 	<p>Foxj1_3125.2_PWM_3.txt (K-afal)</p> 	<p>Foxj1_3125.2_combined_PWM_3.txt (K-afal)  No PWM Available</p>
<p>Foxj1_3125.2_PWM_4.txt (K-afal)  [Z-Beta = 8.39121]  [Pearson Full 0.566441686, Pearson Zeroed 0.56056]</p> 	<p>Foxj1_3125.2_PWM_4.txt (K-afal)</p> 	<p>Foxj1_3125.2_combined_PWM_4.txt (K-afal)  No PWM Available</p>
<p>Foxj1_3125.2_PWM_5.txt (K-afal)  [Z-Beta = 9.72445]  [Pearson Full 0.566441686, Pearson Zeroed 0.564605]</p> 	<p>Foxj1_3125.2_PWM_5.txt (K-afal)</p> 	<p>Foxj1_3125.2_combined_PWM_5.txt (K-afal)  No PWM Available</p>

 <p>Sequence logo showing peaks for TGTI. The x-axis is labeled 1-11, and the y-axis is labeled 0-2. The sequence TGTI is highlighted in red and yellow.</p>		
<p>Foxj1_3125.2_PWM_6.txt (K-afal)</p>  <p>Sequence logo showing peaks for TGTG. The x-axis is labeled 1-9, and the y-axis is labeled 0-2. The sequence TGTG is highlighted in red and yellow.</p>	<p>Foxj1_3125.2_PWM_6.txt (K-afal)</p>  <p>Sequence logo showing peaks for AAACA. The x-axis is labeled 1-9, and the y-axis is labeled 0-2. The sequence AAACA is highlighted in green.</p>	<p>Foxj1_3125.2_combined_PWM_6.txt (K-afal) No PWM Available</p>
<p>Foxj1_3125.2_PWM_7.txt (K-afal) [Z-Beta = 66.3128] [Pearson Full 0.566441686, Pearson Zeroed 0.546022]</p>  <p>Sequence logo showing peaks for AAACA. The x-axis is labeled 1-9, and the y-axis is labeled 0-2. The sequence AAACA is highlighted in green.</p>	<p>Foxj1_3125.2_PWM_7.txt (K-afal)</p>  <p>Sequence logo showing peaks for AAACA. The x-axis is labeled 1-10, and the y-axis is labeled 0-2. The sequence AAACA is highlighted in green.</p>	<p>Foxj1_3125.2_combined_PWM_7.txt (K-afal) No PWM Available</p>
<p>Foxj1_3125.2_PWM_8.txt (K-afal)</p>  <p>Sequence logo showing peaks for AAACA. The x-axis is labeled 1-9, and the y-axis is labeled 0-2. The sequence AAACA is highlighted in green.</p>	<p>Foxj1_3125.2_PWM_8.txt (K-afal) No PWM Available</p>	<p>Foxj1_3125.2_combined_PWM_8.txt (K-afal) No PWM Available</p>

# Foxj3\_0982.2

Array Version 1	Array Version 2	Combined
<p>Foxj3_0982.2_v1_prim_auc.txt (SW) [Z-Beta = 82.3596] [Pearson Full 0.626529265, Pearson Zeroed 0.6763]</p>	<p>Foxj3_0982.2_v2_prim_auc.txt (SW) [Z-Beta = 390.003] [Pearson Full 0.661030655, Pearson Zeroed NaN]</p>	<p>Foxj3_0982.2_combined_prim_auc.txt (SW) No PWM Available</p>
<p>Foxj3_0982.2_v1_prim_ic.txt (SW) [Z-Beta = 7.48818] [Pearson Full 0.626529265, Pearson Zeroed 0.618545]</p>	<p>Foxj3_0982.2_v2_prim_ic.txt (SW)</p>	<p>Foxj3_0982.2_combined_prim_ic_trimmed.txt (SW) [Z-Beta = 218.746] [Pearson Full 0.695574601, Pearson Zeroed 0.700614]</p>
<p>Foxj3_0982.2_v1_prim_untrimmed.txt (SW)</p>	<p>Foxj3_0982.2_v2_prim_untrimmed.txt (SW)</p>	<p>Foxj3_0982.2_combined_prim_untrimmed.txt (SW) [Z-Beta = 175.583] [Pearson Full 0.695574601, Pearson Zeroed 0.694164]</p>
<p>Foxj3_0982.2_v1_sec_auc.txt (SW)</p>	<p>Foxj3_0982.2_v2_sec_auc.txt (SW)</p>	<p>Foxj3_0982.2_combined_sec_auc.txt (SW)</p>
<p>Foxj3_0982.2_v1_sec_ic.txt (SW)</p>	<p>Foxj3_0982.2_v2_sec_ic.txt (SW)</p>	<p>Foxj3_0982.2_combined_sec_ic_trimmed.txt (SW)</p>
<p>Foxj3_0982.2_v1_sec_untrimmed.txt (SW)</p>	<p>Foxj3_0982.2_v2_sec_untrimmed.txt (SW)</p>	<p>Foxj3_0982.2_combined_sec_untrimmed.txt (SW)</p>
<p>Foxj3_0982.2_v1_prim_trimmed.txt (RM8)</p>	<p>Foxj3_0982.2_v2_prim_trimmed.txt (RM8)</p>	<p>Foxj3_0982.2_RM8_combined_prim_trimmed.txt (RM8) No PWM Available</p>
<p>Foxj3_0982.2_v1_sec_trimmed.txt (RM8)</p>	<p>Foxj3_0982.2_v2_sec_trimmed.txt (RM8)</p>	<p>Foxj3_0982.2_RM8_combined_sec_trimmed.txt (RM8) No PWM Available</p>
<p>Foxj3_0982.2_v1_prim_untrimmed.txt (RM8)</p>	<p>Foxj3_0982.2_v2_prim_untrimmed.txt (RM8)</p>	<p>Foxj3_0982.2_RM8_combined_prim_untrimmed.txt (RM8) No PWM Available</p>
<p>Foxj3_0982.2_v1_sec_untrimmed.txt (RM8)</p>	<p>Foxj3_0982.2_v2_sec_untrimmed.txt (RM8)</p>	<p>Foxj3_0982.2_RM8_combined_sec_untrimmed.txt (RM8) No PWM Available</p>
<p>Foxj3_0982.2_v1_RM35_prim_trimmed.txt (RM35)</p>	<p>Foxj3_0982.2_v2_RM35_prim_trimmed.txt (RM35)</p>	<p>Foxj3_0982.2_RM_combined_prim_trimmed.txt (RM35)</p>

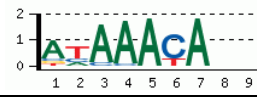
		
Foxj3_0982.2_v1_RM35_sec_trimmed.txt (RM35)	Foxj3_0982.2_v2_RM35_sec_trimmed.txt (RM35)	Foxj3_0982.2_RM_combined_sec_trimmed.txt (RM35)
		
Foxj3_0982.2_v1_RM35_prim_untrimmed.txt (RM35)	Foxj3_0982.2_v2_RM35_prim_untrimmed.txt (RM35)	Foxj3_0982.2_RM_combined_prim_untrimmed.txt (RM35)
		
Foxj3_0982.2_v1_RM35_sec_untrimmed.txt (RM35)	Foxj3_0982.2_v2_RM35_sec_untrimmed.txt (RM35)	Foxj3_0982.2_RM_combined_sec_untrimmed.txt (RM35)
		
Foxj3_0982.2_PWM_1.txt (K-afal)	Foxj3_0982.2_PWM_1.txt (K-afal)	Foxj3_0982.2_combined_PWM_1.txt (K-afal)
 [Z-Beta = 4.78349] [Pearson Full 0.626529265, Pearson Zeroed 0.614818]		
Foxj3_0982.2_PWM_2.txt (K-afal)	Foxj3_0982.2_PWM_2.txt (K-afal)	Foxj3_0982.2_combined_PWM_2.txt (K-afal)
		
Foxj3_0982.2_PWM_3.txt (K-afal)	Foxj3_0982.2_PWM_3.txt (K-afal)	Foxj3_0982.2_combined_PWM_3.txt (K-afal)
		
Foxj3_0982.2_PWM_4.txt (K-afal)	Foxj3_0982.2_PWM_4.txt (K-afal)	Foxj3_0982.2_combined_PWM_4.txt (K-afal)
		
Foxj3_0982.2_PWM_5.txt (K-afal)	Foxj3_0982.2_PWM_5.txt (K-afal)	Foxj3_0982.2_combined_PWM_5.txt (K-afal)
Foxj3_0982.2_PWM_6.txt (Kafal) No PWM Available		
	Foxj3_0982.2_PWM_6.txt (Kafal)	Foxj3_0982.2_combined_PWM_6.txt (Kafal)
Foxj3_0982.2_PWM_7.txt (Kafal) No PWM Available		 [Z-Beta = 57.1315] [Pearson Full 0.695574601, Pearson Zeroed 0.677933]
	Foxj3_0982.2_PWM_7.txt (Kafal)	Foxj3_0982.2_combined_PWM_7.txt (Kafal)
Foxj3_0982.2_PWM_8.txt (Kafal) No PWM Available		
	Foxj3_0982.2_PWM_8.txt (Kafal)	Foxj3_0982.2_combined_PWM_8.txt (Kafal)

Foxj3\_0982.2\_PWM\_9.txt (Kafal)  
No PWM Available

Foxj3\_0982.2\_PWM\_9.txt (Kafal)



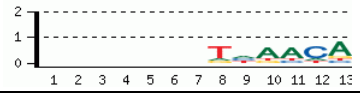
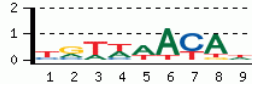

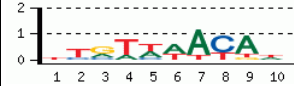
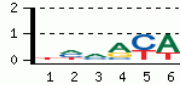
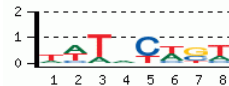
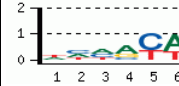
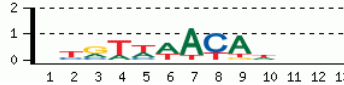
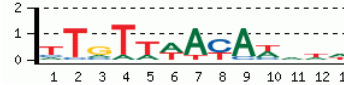
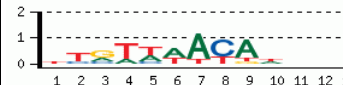

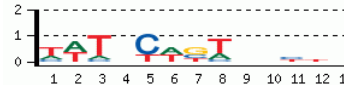
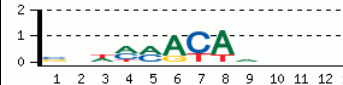
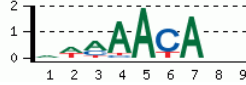
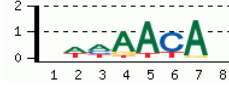


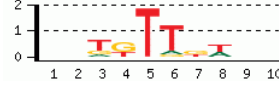
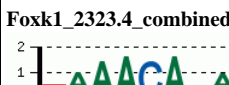




Foxj3\_0982.2\_combined\_PWM\_9.txt (Kafal)





# Foxk1\_2323.4

Array Version 1	Array Version 2	Combined
<p>Foxk1_2323.4_v1_prim_auc.txt (SW)  [Z-Beta = 20.6762]  [Pearson Full 0.607672707, Pearson Zeroed 0.599044]</p>	<p>Foxk1_2323.4_v2_prim_auc.txt (SW)</p>	<p>Foxk1_2323.4_combined_prim_auc.txt (SW)</p>
<p>Foxk1_2323.4_v1_prim_ic.txt (SW)  [Z-Beta = 62.0447]  [Pearson Full 0.607672707, Pearson Zeroed 0.551417]</p>	<p>Foxk1_2323.4_v2_prim_ic.txt (SW)</p>	<p>Foxk1_2323.4_combined_prim_ic_trimmed.txt (SW)</p>
<p>Foxk1_2323.4_v1_prim_untrimmed.txt (SW)</p>	<p>Foxk1_2323.4_v2_prim_untrimmed.txt (SW)</p>	<p>Foxk1_2323.4_combined_prim_untrimmed.txt (SW)</p>
<p>Foxk1_2323.4_v1_sec_auc.txt (SW)</p>	<p>Foxk1_2323.4_v2_sec_auc.txt (SW)</p>	<p>Foxk1_2323.4_combined_sec_auc.txt (SW)</p>
<p>Foxk1_2323.4_v1_sec_ic.txt (SW)</p>	<p>Foxk1_2323.4_v2_sec_ic.txt (SW)</p>	<p>Foxk1_2323.4_combined_sec_ic_trimmed.txt (SW)</p>
<p>Foxk1_2323.4_v1_sec_untrimmed.txt (SW)</p>	<p>Foxk1_2323.4_v2_sec_untrimmed.txt (SW)  [Z-Beta = 42.515]  [Pearson Full 0.592322078, Pearson Zeroed 0.564791]</p>	<p>Foxk1_2323.4_combined_sec_untrimmed.txt (SW)  [Z-Beta = 1.02849]  [Pearson Full 0.693771007, Pearson Zeroed 0.69259]</p>
<p>Foxk1_2323.4_v1_prim_trimmed.txt (RM8)</p>	<p>Foxk1_2323.4_v2_prim_trimmed.txt (RM8)</p>	<p>Foxk1_2323.4_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Foxk1_2323.4_v1_sec_trimmed.txt (RM8)</p>	<p>Foxk1_2323.4_v2_sec_trimmed.txt (RM8)  [Z-Beta = 30.6101]  [Pearson Full 0.592322078, Pearson Zeroed 0.570248]</p>	<p>Foxk1_2323.4_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Foxk1_2323.4_v1_prim_untrimmed.txt (RM8)</p>	<p>Foxk1_2323.4_v2_prim_untrimmed.txt (RM8)</p>	<p>Foxk1_2323.4_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Foxk1_2323.4_v1_sec_untrimmed.txt (RM8)</p>	<p>Foxk1_2323.4_v2_sec_untrimmed.txt (RM8)  [Z-Beta = 42.515]  [Pearson Full 0.592322078, Pearson Zeroed 0.564791]</p>	<p>Foxk1_2323.4_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>

		
Foxk1_2323.4_v1_RM35_prim_trimmed.txt (RM35) 	Foxk1_2323.4_v2_RM35_prim_trimmed.txt (RM35) 	Foxk1_2323.4_RM_combined_prim_trimmed.txt (RM35) 
Foxk1_2323.4_v1_RM35_sec_trimmed.txt (RM35) [Z-Beta = 10.7624] [Pearson Full 0.607672707, Pearson Zeroed 0.598965] 	Foxk1_2323.4_v2_RM35_sec_trimmed.txt (RM35) 	Foxk1_2323.4_RM_combined_sec_trimmed.txt (RM35) [Z-Beta = 61.7203] [Pearson Full 0.693771007, Pearson Zeroed 0.632351] 
Foxk1_2323.4_v1_RM35_prim_untrimmed.txt (RM35) 	Foxk1_2323.4_v2_RM35_prim_untrimmed.txt (RM35) 	Foxk1_2323.4_RM_combined_prim_untrimmed.txt (RM35) [Z-Beta = 4.00905] [Pearson Full 0.693771007, Pearson Zeroed 0.69247] 
Foxk1_2323.4_v1_RM35_sec_untrimmed.txt (RM35) 	Foxk1_2323.4_v2_RM35_sec_untrimmed.txt (RM35) 	Foxk1_2323.4_RM_combined_sec_untrimmed.txt (RM35) 
Foxk1_2323.4_PWM_1.txt (K-afal) [Z-Beta = 132.005] [Pearson Full 0.607672707, Pearson Zeroed 0.506982] 	Foxk1_2323.4_PWM_1.txt (K-afal) [Z-Beta = 65.5209] [Pearson Full 0.592322078, Pearson Zeroed 0.542557] 	Foxk1_2323.4_combined_PWM_1.txt (K-afal) 
Foxk1_2323.4_PWM_2.txt (K-afal) [Z-Beta = 13.3936] [Pearson Full 0.607672707, Pearson Zeroed 0.609108] 	Foxk1_2323.4_PWM_2.txt (K-afal) 	Foxk1_2323.4_combined_PWM_2.txt (K-afal) 
Foxk1_2323.4_PWM_3.txt (K-afal) 	Foxk1_2323.4_PWM_3.txt (K-afal) No PWM Available	Foxk1_2323.4_combined_PWM_3.txt (K-afal) [Z-Beta = 42.966] [Pearson Full 0.693771007, Pearson Zeroed 0.690429] 
Foxk1_2323.4_PWM_4.txt (K-afal) [Z-Beta = 1.52687] [Pearson Full 0.607672707, Pearson Zeroed 0.605743] 	Foxk1_2323.4_PWM_4.txt (K-afal) No PWM Available	Foxk1_2323.4_combined_PWM_4.txt (K-afal) [Z-Beta = 21.451] [Pearson Full 0.693771007, Pearson Zeroed 0.689179] 

# Foxl1\_2809.2

Array Version 1	Array Version 2	Combined
<p>Foxl1_2809.2_v1_prim_auc.txt (SW) [Z-Beta = 98.8748] [Pearson Full 0.653720469, Pearson Zeroed 0.643202]</p>	<p>Foxl1_2809.2_v2_prim_auc.txt (SW) [Z-Beta = 16.9958] [Pearson Full 0.607260156, Pearson Zeroed 0.583592]</p>	<p>Foxl1_2809.2_combined_prim_auc.txt (SW)</p>
<p>Foxl1_2809.2_v1_prim_ic.txt (SW) [Z-Beta = 37.4062] [Pearson Full 0.653720469, Pearson Zeroed 0.659625]</p>	<p>Foxl1_2809.2_v2_prim_ic.txt (SW)</p>	<p>Foxl1_2809.2_combined_prim_ic_trimmed.txt (SW)</p>
<p>Foxl1_2809.2_v1_prim_untrimmed.txt (SW)</p>	<p>Foxl1_2809.2_v2_prim_untrimmed.txt (SW)</p>	<p>Foxl1_2809.2_combined_prim_untrimmed.txt (SW)</p>
<p>Foxl1_2809.2_v1_sec_auc.txt (SW)</p>	<p>Foxl1_2809.2_v2_sec_auc.txt (SW)</p>	<p>Foxl1_2809.2_combined_sec_auc.txt (SW)</p>
<p>Foxl1_2809.2_v1_sec_ic.txt (SW)</p>	<p>Foxl1_2809.2_v2_sec_ic.txt (SW)</p>	<p>Foxl1_2809.2_combined_sec_ic_trimmed.txt (SW)</p>
<p>Foxl1_2809.2_v1_sec_untrimmed.txt (SW)</p>	<p>Foxl1_2809.2_v2_sec_untrimmed.txt (SW)</p>	<p>Foxl1_2809.2_combined_sec_untrimmed.txt (SW)</p>
<p>Foxl1_2809.2_v1_prim_trimmed.txt (RM8)</p>	<p>Foxl1_2809.2_v2_prim_trimmed.txt (RM8)</p>	<p>Foxl1_2809.2_RM8_combined_prim_trimmed.txt (RM8) No PWM Available</p>
<p>Foxl1_2809.2_v1_sec_trimmed.txt (RM8)</p>	<p>Foxl1_2809.2_v2_sec_trimmed.txt (RM8)</p>	<p>Foxl1_2809.2_RM8_combined_sec_trimmed.txt (RM8) No PWM Available</p>
<p>Foxl1_2809.2_v1_prim_untrimmed.txt (RM8)</p>	<p>Foxl1_2809.2_v2_prim_untrimmed.txt (RM8)</p>	<p>Foxl1_2809.2_RM8_combined_prim_untrimmed.txt (RM8) No PWM Available</p>
<p>Foxl1_2809.2_v1_sec_untrimmed.txt (RM8)</p>	<p>Foxl1_2809.2_v2_sec_untrimmed.txt (RM8)</p>	<p>Foxl1_2809.2_RM8_combined_sec_untrimmed.txt (RM8) No PWM Available</p>
<p>Foxl1_2809.2_v1_RM35_prim_trimmed.txt (RM35)</p>	<p>Foxl1_2809.2_v2_RM35_prim_trimmed.txt (RM35)</p>	<p>Foxl1_2809.2_RM_combined_prim_trimmed.txt (RM35)</p>



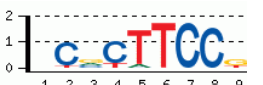




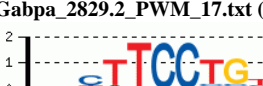
<p>Foxl1_2809.2_v1_RM35_sec_trimmed.txt (RM35)</p>	<p>Foxl1_2809.2_v2_RM35_sec_trimmed.txt (RM35)</p>	<p>Foxl1_2809.2_RM_combined_sec_trimmed.txt (RM35)</p>
<p>Foxl1_2809.2_v1_RM35_prim_untrimmed.txt (RM35)</p>	<p>Foxl1_2809.2_v2_RM35_prim_untrimmed.txt (RM35)</p>	<p>Foxl1_2809.2_RM_combined_prim_untrimmed.txt (RM35)</p>
<p>Foxl1_2809.2_v1_RM35_sec_untrimmed.txt (RM35)</p>	<p>Foxl1_2809.2_v2_RM35_sec_untrimmed.txt (RM35)</p>	<p>Foxl1_2809.2_RM_combined_sec_untrimmed.txt (RM35)</p>
<p>Foxl1_2809.2_PWM_1.txt (K-afal)</p>	<p>Foxl1_2809.2_PWM_1.txt (K-afal)</p>	<p>Foxl1_2809.2_combined_PWM_1.txt (K-afal)</p>
<p>Foxl1_2809.2_PWM_2.txt (K-afal)</p>	<p>Foxl1_2809.2_PWM_2.txt (K-afal)</p>	<p>Foxl1_2809.2_combined_PWM_2.txt (K-afal)</p>
<p>Foxl1_2809.2_PWM_3.txt (K-afal)</p>	<p>Foxl1_2809.2_PWM_3.txt (K-afal)  [Z-Beta = 126.609]  [Pearson Full 0.607260156, Pearson Zeroed 0.511854]</p>	<p>Foxl1_2809.2_combined_PWM_3.txt (K-afal)</p>
<p>Foxl1_2809.2_PWM_4.txt (K-afal)  [Z-Beta = 61.9884]  [Pearson Full 0.653720469, Pearson Zeroed 0.577616]</p>	<p>Foxl1_2809.2_PWM_4.txt (K-afal)  [Z-Beta = 41.2952]  [Pearson Full 0.607260156, Pearson Zeroed 0.529949]</p>	<p>Foxl1_2809.2_combined_PWM_4.txt (K-afal)</p>
<p>Foxl1_2809.2_PWM_5.txt (K-afal)</p>	<p>Foxl1_2809.2_PWM_5.txt (K-afal)</p>	<p>Foxl1_2809.2_combined_PWM_5.txt (K-afal)</p>

# Gabpa\_2829.2

Array Version 1	Array Version 2	Combined
<p>Gabpa_2829.2_v1_prim_auc.txt (SW) [Z-Beta = 70.4663] [Pearson Full 0.706340221, Pearson Zeroed 0.691546]</p>	<p>Gabpa_2829.2_v2_prim_auc.txt (SW) [Z-Beta = 49.9197] [Pearson Full 0.790130826, Pearson Zeroed 0.783544]</p>	<p>Gabpa_2829.2_combined_prim_auc.txt (SW) No PWM Available</p>
<p>Gabpa_2829.2_v1_prim_ic.txt (SW) [Z-Beta = 232.621] [Pearson Full 0.706340221, Pearson Zeroed 0.650443]</p>	<p>Gabpa_2829.2_v2_prim_ic.txt (SW) [Z-Beta = 442.202] [Pearson Full 0.790130826, Pearson Zeroed 0.623891]</p>	<p>Gabpa_2829.2_combined_prim_ic_trimmed.txt (SW) [Z-Beta = 353.787] [Pearson Full 0.819289183, Pearson Zeroed 0.682679]</p>
<p>Gabpa_2829.2_v1_prim_untrimmed.txt (SW)</p>	<p>Gabpa_2829.2_v2_prim_untrimmed.txt (SW)</p>	<p>Gabpa_2829.2_combined_prim_untrimmed.txt (SW)</p>
<p>Gabpa_2829.2_v1_sec_auc.txt (SW)</p>	<p>Gabpa_2829.2_v2_sec_auc.txt (SW)</p>	<p>Gabpa_2829.2_combined_sec_auc.txt (SW)</p>
<p>Gabpa_2829.2_v1_sec_ic.txt (SW)</p>	<p>Gabpa_2829.2_v2_sec_ic.txt (SW)</p>	<p>Gabpa_2829.2_combined_sec_ic_trimmed.txt (SW)</p>
<p>Gabpa_2829.2_v1_sec_untrimmed.txt (SW)</p>	<p>Gabpa_2829.2_v2_sec_untrimmed.txt (SW)</p>	<p>Gabpa_2829.2_combined_sec_untrimmed.txt (SW)</p>
<p>Gabpa_2829.2_v1_prim_trimmed.txt (RM8)</p>	<p>Gabpa_2829.2_v2_prim_trimmed.txt (RM8)</p>	<p>Gabpa_2829.2_RM8_combined_prim_trimmed.txt (RM8) No PWM Available</p>
<p>Gabpa_2829.2_v1_sec_trimmed.txt (RM8)</p>	<p>Gabpa_2829.2_v2_sec_trimmed.txt (RM8)</p>	<p>Gabpa_2829.2_RM8_combined_sec_trimmed.txt (RM8) No PWM Available</p>
<p>Gabpa_2829.2_v1_prim_untrimmed.txt (RM8)</p>	<p>Gabpa_2829.2_v2_prim_untrimmed.txt (RM8)</p>	<p>Gabpa_2829.2_RM8_combined_prim_untrimmed.txt (RM8) No PWM Available</p>
<p>Gabpa_2829.2_v1_sec_untrimmed.txt (RM8)</p>	<p>Gabpa_2829.2_v2_sec_untrimmed.txt (RM8)</p>	<p>Gabpa_2829.2_RM8_combined_sec_untrimmed.txt (RM8) No PWM Available</p>
<p>Gabpa_2829.2_v1_RM35_prim_trimmed.txt (RM35)</p>	<p>Gabpa_2829.2_v2_RM35_prim_trimmed.txt (RM35)</p>	<p>Gabpa_2829.2_RM_combined_prim_trimmed.txt (RM35)</p>











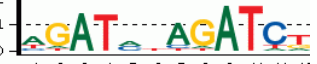

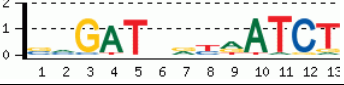
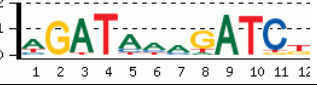
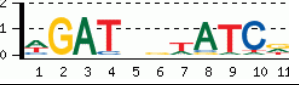
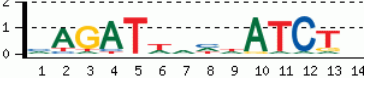
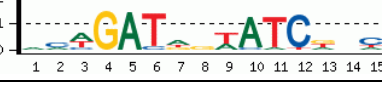




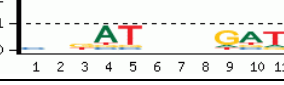



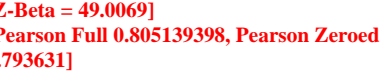
<p>Gabpa_2829.2_v1_RM35_sec_trimmed.txt (RM35)</p>	<p>Gabpa_2829.2_v2_RM35_sec_trimmed.txt (RM35)</p>	<p>Gabpa_2829.2_RM_combined_sec_trimmed.txt (RM35)</p>
<p>Gabpa_2829.2_v1_RM35_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = 33.1668] [Pearson Full 0.706340221, Pearson Zeroed 0.68184]</p>	<p>Gabpa_2829.2_v2_RM35_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = 278.768] [Pearson Full 0.790130826, Pearson Zeroed 0.701922]</p>	<p>Gabpa_2829.2_RM_combined_prim_untrimmed.txt (RM35)</p>
<p>Gabpa_2829.2_v1_RM35_sec_untrimmed.txt (RM35)</p>	<p>Gabpa_2829.2_v2_RM35_sec_untrimmed.txt (RM35)</p>	<p>Gabpa_2829.2_RM_combined_sec_untrimmed.txt (RM35)</p>
<p>Gabpa_2829.2_PWM_1.txt (K-afal)</p>	<p>Gabpa_2829.2_PWM_1.txt (K-afal)</p>	<p>Gabpa_2829.2_combined_PWM_1.txt (K-afal)</p>
<p>Gabpa_2829.2_PWM_2.txt (K-afal)</p>	<p>Gabpa_2829.2_PWM_2.txt (K-afal)</p>	<p>Gabpa_2829.2_combined_PWM_2.txt (K-afal)</p> <p>[Z-Beta = 302.488] [Pearson Full 0.819289183, Pearson Zeroed 0.707238]</p>
<p>Gabpa_2829.2_PWM_3.txt (K-afal)</p>	<p>Gabpa_2829.2_PWM_3.txt (K-afal)</p>	<p>Gabpa_2829.2_combined_PWM_3.txt (K-afal)</p>
<p>Gabpa_2829.2_PWM_4.txt (K-afal)</p>	<p>Gabpa_2829.2_PWM_4.txt (K-afal)</p>	<p>Gabpa_2829.2_combined_PWM_4.txt (K-afal)</p> <p>No PWM Available</p>
<p>Gabpa_2829.2_PWM_5.txt (K-afal)</p>	<p>Gabpa_2829.2_PWM_5.txt (K-afal)</p>	<p>Gabpa_2829.2_combined_PWM_5.txt (K-afal)</p> <p>No PWM Available</p>
<p>Gabpa_2829.2_PWM_6.txt (K-afal)</p>	<p>Gabpa_2829.2_PWM_6.txt (K-afal)</p>	<p>Gabpa_2829.2_combined_PWM_6.txt (K-afal)</p> <p>No PWM Available</p>
<p>Gabpa_2829.2_PWM_7.txt (K-afal)</p>	<p>Gabpa_2829.2_PWM_7.txt (K-afal)</p>	<p>Gabpa_2829.2_combined_PWM_7.txt (K-afal)</p> <p>No PWM Available</p>
<p>Gabpa_2829.2_PWM_8.txt (Kafal)</p> <p>No PWM Available</p>	<p>Gabpa_2829.2_PWM_8.txt (Kafal)</p>	<p>Gabpa_2829.2_combined_PWM_8.txt (Kafal)</p> <p>No PWM Available</p>
<p>Gabpa_2829.2_PWM_9.txt (Kafal)</p> <p>No PWM Available</p>	<p>Gabpa_2829.2_PWM_9.txt (Kafal)</p>	<p>Gabpa_2829.2_combined_PWM_9.txt (Kafal)</p> <p>No PWM Available</p>

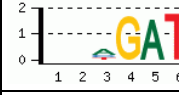
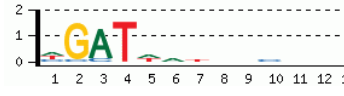










<p>Gabpa_2829.2_PWM_10.txt (Kafal) No PWM Available</p>	<p>Gabpa_2829.2_PWM_10.txt (Kafal)</p> 	<p>Gabpa_2829.2_combined_PWM_10.txt (Kafal) No PWM Available</p>
<p>Gabpa_2829.2_PWM_11.txt (Kafal) No PWM Available</p>	<p>Gabpa_2829.2_PWM_11.txt (Kafal)</p> 	<p>Gabpa_2829.2_combined_PWM_11.txt (Kafal) No PWM Available</p>
<p>Gabpa_2829.2_PWM_12.txt (Kafal) No PWM Available</p>	<p>Gabpa_2829.2_PWM_12.txt (Kafal)</p> 	<p>Gabpa_2829.2_combined_PWM_12.txt (Kafal) No PWM Available</p>
<p>Gabpa_2829.2_PWM_13.txt (Kafal) No PWM Available</p>	<p>Gabpa_2829.2_PWM_13.txt (Kafal)</p> 	<p>Gabpa_2829.2_combined_PWM_13.txt (Kafal) No PWM Available</p>
<p>Gabpa_2829.2_PWM_14.txt (Kafal) No PWM Available</p>	<p>Gabpa_2829.2_PWM_14.txt (Kafal)</p> 	<p>Gabpa_2829.2_combined_PWM_14.txt (Kafal) No PWM Available</p>
<p>Gabpa_2829.2_PWM_15.txt (Kafal) No PWM Available</p>	<p>Gabpa_2829.2_PWM_15.txt (Kafal)</p> 	<p>Gabpa_2829.2_combined_PWM_15.txt (Kafal) No PWM Available</p>
<p>Gabpa_2829.2_PWM_16.txt (Kafal) No PWM Available</p>	<p>Gabpa_2829.2_PWM_16.txt (Kafal)</p> 	<p>Gabpa_2829.2_combined_PWM_16.txt (Kafal) No PWM Available</p>
<p>Gabpa_2829.2_PWM_17.txt (Kafal) No PWM Available</p>	<p>Gabpa_2829.2_PWM_17.txt (Kafal)</p> 	<p>Gabpa_2829.2_combined_PWM_17.txt (Kafal) No PWM Available</p>









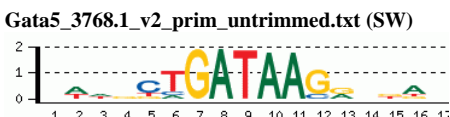

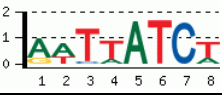



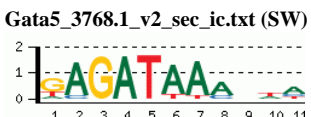


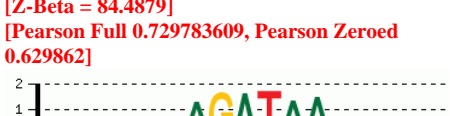


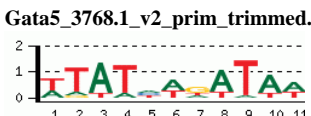

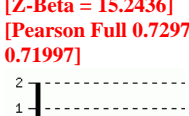
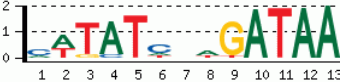
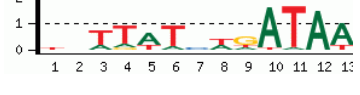
# Gata3\_1024.3

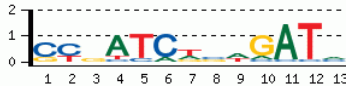




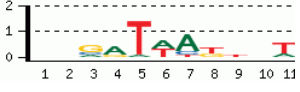
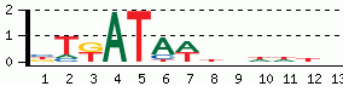
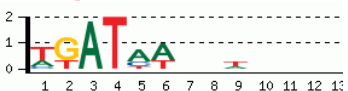
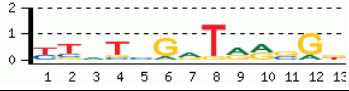
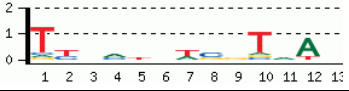





Array Version 1	Array Version 2	Combined
<p>Gata3_1024.3_v1_prim_auc.txt (SW)  [Z-Beta = 17.6178]  [Pearson Full 0.817136392, Pearson Zeroed 0.812878]</p> 	<p>Gata3_1024.3_v2_prim_auc.txt (SW)</p> 	<p>Gata3_1024.3_combined_prim_auc.txt (SW)</p> 
<p>Gata3_1024.3_v1_prim_ic.txt (SW)  [Z-Beta = 32.1544]  [Pearson Full 0.817136392, Pearson Zeroed 0.808469]</p> 	<p>Gata3_1024.3_v2_prim_ic.txt (SW)</p> 	<p>Gata3_1024.3_combined_prim_ic_trimmed.txt (SW)</p> 
<p>Gata3_1024.3_v1_prim_untrimmed.txt (SW)</p> 	<p>Gata3_1024.3_v2_prim_untrimmed.txt (SW)</p> 	<p>Gata3_1024.3_combined_prim_untrimmed.txt (SW)</p> 
<p>Gata3_1024.3_v1_sec_auc.txt (SW)</p> 	<p>Gata3_1024.3_v2_sec_auc.txt (SW)</p> 	<p>Gata3_1024.3_combined_sec_auc.txt (SW)</p> 
<p>Gata3_1024.3_v1_sec_ic.txt (SW)</p> 	<p>Gata3_1024.3_v2_sec_ic.txt (SW)</p> 	<p>Gata3_1024.3_combined_sec_ic_trimmed.txt (SW)</p> 
<p>Gata3_1024.3_v1_sec_untrimmed.txt (SW)</p> 	<p>Gata3_1024.3_v2_sec_untrimmed.txt (SW)  [Z-Beta = 49.0069]  [Pearson Full 0.805139398, Pearson Zeroed 0.793631]</p> 	<p>Gata3_1024.3_combined_sec_untrimmed.txt (SW)</p> 
<p>Gata3_1024.3_v1_prim_trimmed.txt (RM8)</p> 	<p>Gata3_1024.3_v2_prim_trimmed.txt (RM8)</p> 	<p>Gata3_1024.3_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Gata3_1024.3_v1_sec_trimmed.txt (RM8)</p> 	<p>Gata3_1024.3_v2_sec_trimmed.txt (RM8)  [Z-Beta = 12.7026]  [Pearson Full 0.805139398, Pearson Zeroed 0.801753]</p> 	<p>Gata3_1024.3_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Gata3_1024.3_v1_prim_untrimmed.txt (RM8)</p> 	<p>Gata3_1024.3_v2_prim_untrimmed.txt (RM8)</p> 	<p>Gata3_1024.3_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Gata3_1024.3_v1_sec_untrimmed.txt (RM8)</p> 	<p>Gata3_1024.3_v2_sec_untrimmed.txt (RM8)  [Z-Beta = 49.0069]  [Pearson Full 0.805139398, Pearson Zeroed 0.793631]</p> 	<p>Gata3_1024.3_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>

		
Gata3_1024.3_v1_RM35_prim_trimmed.txt (RM35) 	Gata3_1024.3_v2_RM35_prim_trimmed.txt (RM35) [Z-Beta = 26.7354] [Pearson Full 0.805139398, Pearson Zeroed 0.799037] 	Gata3_1024.3_RM_combined_prim_trimmed.txt (RM35) [Z-Beta = 65.7837] [Pearson Full 0.803476324, Pearson Zeroed 0.784513] 
Gata3_1024.3_v1_RM35_sec_trimmed.txt (RM35) [Z-Beta = 215.92] [Pearson Full 0.817136392, Pearson Zeroed 0.786346] 	Gata3_1024.3_v2_RM35_sec_trimmed.txt (RM35) [Z-Beta = 121.025] [Pearson Full 0.805139398, Pearson Zeroed 0.748426] 	Gata3_1024.3_RM_combined_sec_trimmed.txt (RM35) [Z-Beta = 212.785] [Pearson Full 0.803476324, Pearson Zeroed 0.742781] 
Gata3_1024.3_v1_RM35_prim_untrimmed.txt (RM35) 	Gata3_1024.3_v2_RM35_prim_untrimmed.txt (RM35) 	Gata3_1024.3_RM_combined_prim_untrimmed.txt (RM35) 
Gata3_1024.3_v1_RM35_sec_untrimmed.txt (RM35) [Z-Beta = 189.472] [Pearson Full 0.817136392, Pearson Zeroed 0.81334] 	Gata3_1024.3_v2_RM35_sec_untrimmed.txt (RM35) 	Gata3_1024.3_RM_combined_sec_untrimmed.txt (RM35) [Z-Beta = 53.9524] [Pearson Full 0.803476324, Pearson Zeroed 0.798026] 
Gata3_1024.3_PWM_1.txt (K-afal) [Z-Beta = 130.697] [Pearson Full 0.817136392, Pearson Zeroed 0.810743] 	Gata3_1024.3_PWM_1.txt (K-afal) 	Gata3_1024.3_combined_PWM_1.txt (K-afal) [Z-Beta = 131.385] [Pearson Full 0.803476324, Pearson Zeroed 0.792407] 
Gata3_1024.3_PWM_2.txt (K-afal) 	Gata3_1024.3_PWM_2.txt (K-afal) 	Gata3_1024.3_combined_PWM_2.txt (K-afal) [Z-Beta = 152.516] [Pearson Full 0.803476324, Pearson Zeroed 0.783407] 
Gata3_1024.3_PWM_3.txt (K-afal) [Z-Beta = 44.4469] [Pearson Full 0.817136392, Pearson Zeroed 0.806909] 	Gata3_1024.3_PWM_3.txt (K-afal) [Z-Beta = 130.451] [Pearson Full 0.805139398, Pearson Zeroed 0.746225] 	Gata3_1024.3_combined_PWM_3.txt (K-afal) No PWM Available
Gata3_1024.3_PWM_4.txt (K-afal) [Z-Beta = 139.164] [Pearson Full 0.817136392, Pearson Zeroed 0.800471] 	Gata3_1024.3_PWM_4.txt (K-afal) [Z-Beta = 58.7533] [Pearson Full 0.805139398, Pearson Zeroed 0.780071] 	Gata3_1024.3_combined_PWM_4.txt (K-afal) No PWM Available


















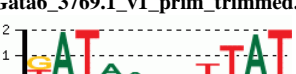


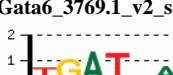
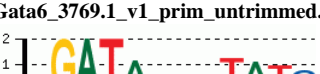
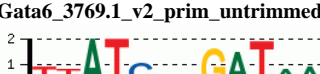
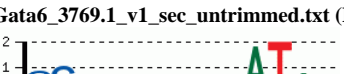
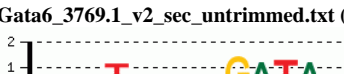
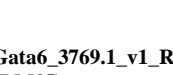
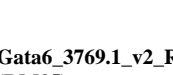
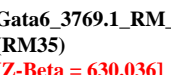
<p><b>Gata3_1024.3_PWM_5.txt (K-afal)</b>  [Z-Beta = 114.244]  [Pearson Full 0.817136392, Pearson Zeroed 0.800588]</p> 	<p><b>Gata3_1024.3_PWM_5.txt (K-afal)</b></p> 	<p><b>Gata3_1024.3_combined_PWM_5.txt (K-afal)</b>  No PWM Available</p>
<p><b>Gata3_1024.3_PWM_6.txt (K-afal)</b></p> 	<p><b>Gata3_1024.3_PWM_6.txt (K-afal)</b>  [Z-Beta = 26.8009]  [Pearson Full 0.805139398, Pearson Zeroed 0.803527]</p> 	<p><b>Gata3_1024.3_combined_PWM_6.txt (K-afal)</b>  No PWM Available</p>
<p><b>Gata3_1024.3_PWM_7.txt (K-afal)</b></p> 	<p><b>Gata3_1024.3_PWM_7.txt (K-afal)</b>  No PWM Available</p>	<p><b>Gata3_1024.3_combined_PWM_7.txt (K-afal)</b>  No PWM Available</p>
<p><b>Gata3_1024.3_PWM_8.txt (K-afal)</b></p> 	<p><b>Gata3_1024.3_PWM_8.txt (K-afal)</b>  No PWM Available</p>	<p><b>Gata3_1024.3_combined_PWM_8.txt (K-afal)</b>  No PWM Available</p>
<p><b>Gata3_1024.3_PWM_9.txt (K-afal)</b></p> 	<p><b>Gata3_1024.3_PWM_9.txt (K-afal)</b>  No PWM Available</p>	<p><b>Gata3_1024.3_combined_PWM_9.txt (K-afal)</b>  No PWM Available</p>




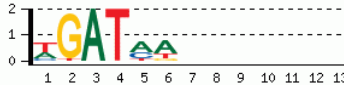
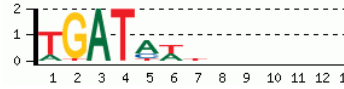
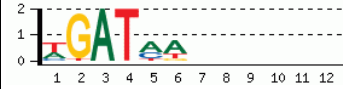

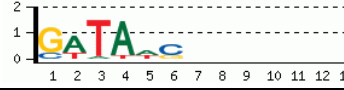


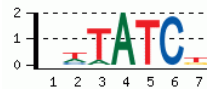




# Gata5\_3768.1

Array Version 1	Array Version 2	Combined
<p>Gata5_3768.1_v1_prim_auc.txt (SW)  [Z-Beta = 78.2144]  [Pearson Full 0.575733748, Pearson Zeroed 0.508635]</p> 	<p>Gata5_3768.1_v2_prim_auc.txt (SW)</p> 	<p>Gata5_3768.1_combined_prim_auc.txt (SW)  No PWM Available</p>
<p>Gata5_3768.1_v1_prim_ic.txt (SW)  [Z-Beta = 35.1129]  [Pearson Full 0.575733748, Pearson Zeroed 0.560439]</p> 	<p>Gata5_3768.1_v2_prim_ic.txt (SW)</p> 	<p>Gata5_3768.1_combined_prim_ic_trimmed.txt (SW)</p> 
<p>Gata5_3768.1_v1_prim_untrimmed.txt (SW)</p> 	<p>Gata5_3768.1_v2_prim_untrimmed.txt (SW)</p> 	<p>Gata5_3768.1_combined_prim_untrimmed.txt (SW)</p> 
<p>Gata5_3768.1_v1_sec_auc.txt (SW)  [Z-Beta = 8.86373]  [Pearson Full 0.575733748, Pearson Zeroed 0.567794]</p> 	<p>Gata5_3768.1_v2_sec_auc.txt (SW)</p> 	<p>Gata5_3768.1_combined_sec_auc.txt (SW)</p> 
<p>Gata5_3768.1_v1_sec_ic.txt (SW)</p> 	<p>Gata5_3768.1_v2_sec_ic.txt (SW)</p> 	<p>Gata5_3768.1_combined_sec_ic_trimmed.txt (SW)</p> 
<p>Gata5_3768.1_v1_sec_untrimmed.txt (SW)</p> 	<p>Gata5_3768.1_v2_sec_untrimmed.txt (SW)  [Z-Beta = 84.4879]  [Pearson Full 0.729783609, Pearson Zeroed 0.629862]</p> 	<p>Gata5_3768.1_combined_sec_untrimmed.txt (SW)</p> 
<p>Gata5_3768.1_v1_prim_trimmed.txt (RM8)</p> 	<p>Gata5_3768.1_v2_prim_trimmed.txt (RM8)</p> 	<p>Gata5_3768.1_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Gata5_3768.1_v1_sec_trimmed.txt (RM8)</p> 	<p>Gata5_3768.1_v2_sec_trimmed.txt (RM8)  [Z-Beta = 15.2436]  [Pearson Full 0.729783609, Pearson Zeroed 0.71997]</p> 	<p>Gata5_3768.1_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Gata5_3768.1_v1_prim_untrimmed.txt (RM8)  [Z-Beta = 4.72187]  [Pearson Full 0.575733748, Pearson Zeroed 0.573251]</p> 	<p>Gata5_3768.1_v2_prim_untrimmed.txt (RM8)</p> 	<p>Gata5_3768.1_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>

<p>Gata5_3768.1_v1_sec_untrimmed.txt (RM8)</p> 	<p>Gata5_3768.1_v2_sec_untrimmed.txt (RM8)  [Z-Beta = 84.4879]  [Pearson Full 0.729783609, Pearson Zeroed 0.629862]</p> 	<p>Gata5_3768.1_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>
<p>Gata5_3768.1_v1_RM35_prim_trimmed.txt (RM35)</p> 	<p>Gata5_3768.1_v2_RM35_prim_trimmed.txt (RM35)</p> 	<p>Gata5_3768.1_RM_combined_prim_trimmed.txt (RM35)  [Z-Beta = 61.8736]  [Pearson Full 0.628223584, Pearson Zeroed 0.574985]</p> 
<p>Gata5_3768.1_v1_RM35_sec_trimmed.txt (RM35)  [Z-Beta = 0.816961]  [Pearson Full 0.575733748, Pearson Zeroed 0.575008]</p> 	<p>Gata5_3768.1_v2_RM35_sec_trimmed.txt (RM35)</p> 	<p>Gata5_3768.1_RM_combined_sec_trimmed.txt (RM35)</p> 
<p>Gata5_3768.1_v1_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Gata5_3768.1_v2_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Gata5_3768.1_RM_combined_prim_untrimmed.txt (RM35)  [Z-Beta = 15.034]  [Pearson Full 0.628223584, Pearson Zeroed 0.625464]</p> 
<p>Gata5_3768.1_v1_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Gata5_3768.1_v2_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Gata5_3768.1_RM_combined_sec_untrimmed.txt (RM35)</p> 
<p>Gata5_3768.1_PWM_1.txt (K-afal)  [Z-Beta = 143.246]  [Pearson Full 0.575733748, Pearson Zeroed 0.37304]</p> 	<p>Gata5_3768.1_PWM_1.txt (K-afal)  [Z-Beta = 54.2436]  [Pearson Full 0.729783609, Pearson Zeroed 0.701305]</p> 	<p>Gata5_3768.1_combined_PWM_1.txt (K-afal)</p> 
<p>Gata5_3768.1_PWM_2.txt (Kafal)  No PWM Available</p>	<p>Gata5_3768.1_PWM_2.txt (Kafal)  [Z-Beta = 44.9328]  [Pearson Full 0.729783609, Pearson Zeroed 0.704297]</p> 	<p>Gata5_3768.1_combined_PWM_2.txt (Kafal)  [Z-Beta = 114.634]  [Pearson Full 0.628223584, Pearson Zeroed 0.532532]</p> 
<p>Gata5_3768.1_PWM_3.txt (Kafal)  No PWM Available</p>	<p>Gata5_3768.1_PWM_3.txt (Kafal)</p> 	<p>Gata5_3768.1_combined_PWM_3.txt (Kafal)  [Z-Beta = 4.28919]  [Pearson Full 0.628223584, Pearson Zeroed 0.626717]</p> 



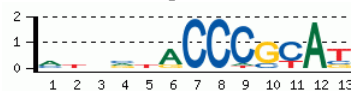


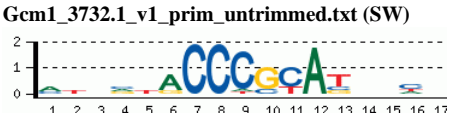
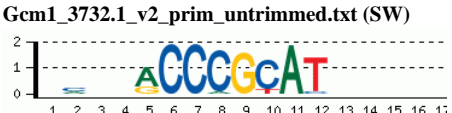
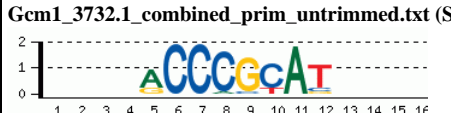
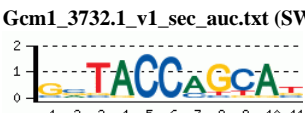
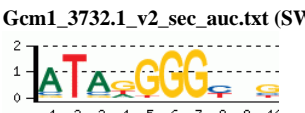
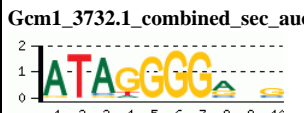
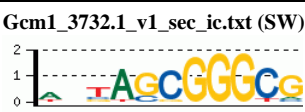
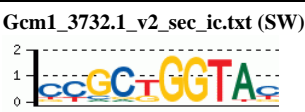
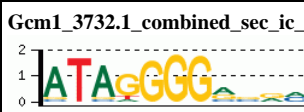
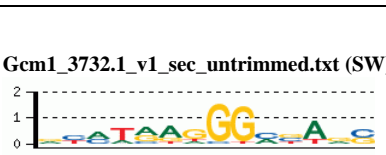
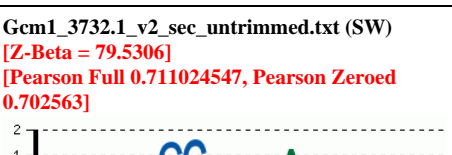
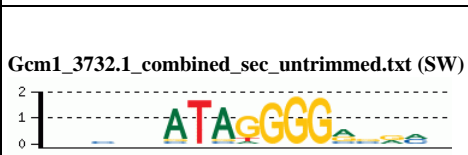
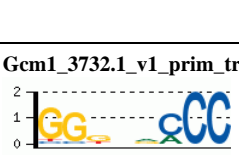
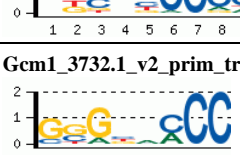
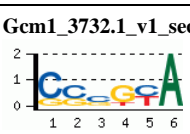
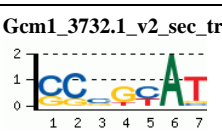
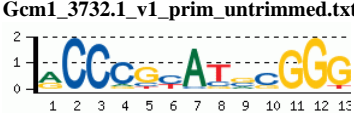
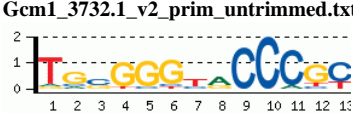
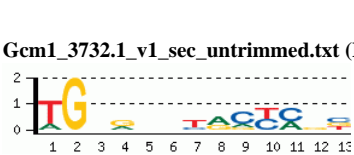
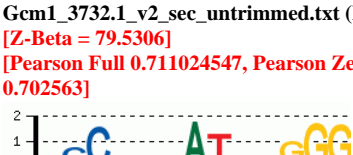
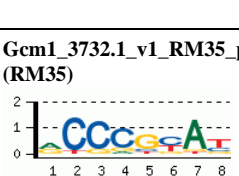
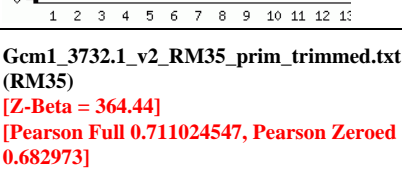
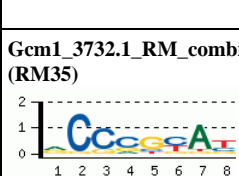
# Gata6\_3769.1

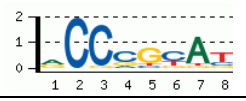
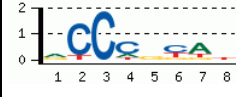
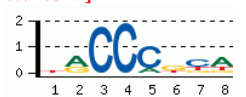
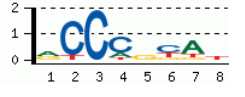




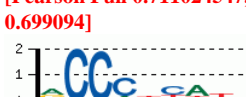









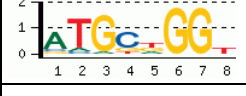









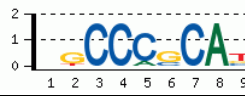
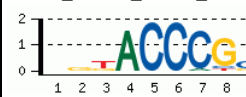
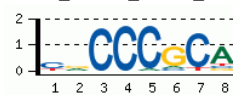

Array Version 1	Array Version 2	Combined
<p>Gata6_3769.1_v1_prim_auc.txt (SW)  [Z-Beta = 59.8776]  [Pearson Full 0.712621405, Pearson Zeroed 0.710556]</p> 	<p>Gata6_3769.1_v2_prim_auc.txt (SW)</p> 	<p>Gata6_3769.1_combined_prim_auc.txt (SW)  No PWM Available</p>
<p>Gata6_3769.1_v1_prim_ic.txt (SW)</p> 	<p>Gata6_3769.1_v2_prim_ic.txt (SW)</p> 	<p>Gata6_3769.1_combined_prim_ic_trimmed.txt (SW)</p> 
<p>Gata6_3769.1_v1_prim_untrimmed.txt (SW)</p> 	<p>Gata6_3769.1_v2_prim_untrimmed.txt (SW)</p> 	<p>Gata6_3769.1_combined_prim_untrimmed.txt (SW)</p> 
<p>Gata6_3769.1_v1_sec_auc.txt (SW)</p> 	<p>Gata6_3769.1_v2_sec_auc.txt (SW)</p> 	<p>Gata6_3769.1_combined_sec_auc.txt (SW)</p> 
<p>Gata6_3769.1_v1_sec_ic.txt (SW)</p> 	<p>Gata6_3769.1_v2_sec_ic.txt (SW)</p> 	<p>Gata6_3769.1_combined_sec_ic_trimmed.txt (SW)</p> 
<p>Gata6_3769.1_v1_sec_untrimmed.txt (SW)</p> 	<p>Gata6_3769.1_v2_sec_untrimmed.txt (SW)</p> 	<p>Gata6_3769.1_combined_sec_untrimmed.txt (SW)</p> 
<p>Gata6_3769.1_v1_prim_trimmed.txt (RM8)</p> 	<p>Gata6_3769.1_v2_prim_trimmed.txt (RM8)</p> 	<p>Gata6_3769.1_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Gata6_3769.1_v1_sec_trimmed.txt (RM8)</p> 	<p>Gata6_3769.1_v2_sec_trimmed.txt (RM8)</p> 	<p>Gata6_3769.1_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Gata6_3769.1_v1_prim_untrimmed.txt (RM8)</p> 	<p>Gata6_3769.1_v2_prim_untrimmed.txt (RM8)</p> 	<p>Gata6_3769.1_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Gata6_3769.1_v1_sec_untrimmed.txt (RM8)</p> 	<p>Gata6_3769.1_v2_sec_untrimmed.txt (RM8)</p> 	<p>Gata6_3769.1_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>
<p>Gata6_3769.1_v1_RM35_prim_trimmed.txt (RM35)</p> 	<p>Gata6_3769.1_v2_RM35_prim_trimmed.txt (RM35)</p> 	<p>Gata6_3769.1_RM_combined_prim_trimmed.txt (RM35)  [Z-Beta = 630.036]  [Pearson Full 0.626863775, Pearson Zeroed 0.514816]</p> 

<p>Gata6_3769.1_v1_RM35_sec_trimmed.txt (RM35)  [Z-Beta = 482.34]  [Pearson Full 0.712621405, Pearson Zeroed 0.682058]</p> 	<p>Gata6_3769.1_v2_RM35_sec_trimmed.txt (RM35)  [Z-Beta = 231.438]  [Pearson Full 0.772302042, Pearson Zeroed 0.73569]</p> 	<p>Gata6_3769.1_RM_combined_sec_trimmed.txt (RM35)  [Z-Beta = 799.274]  [Pearson Full 0.626863775, Pearson Zeroed 0.485263]</p> 
<p>Gata6_3769.1_v1_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Gata6_3769.1_v2_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Gata6_3769.1_RM_combined_prim_untrimmed.txt (RM35)</p> 
<p>Gata6_3769.1_v1_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Gata6_3769.1_v2_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Gata6_3769.1_RM_combined_sec_untrimmed.txt (RM35)</p> 
<p>Gata6_3769.1_PWM_1.txt (K-afal)  [Z-Beta = 2126.86]  [Pearson Full 0.712621405, Pearson Zeroed 0.536198]</p> 	<p>Gata6_3769.1_PWM_1.txt (K-afal)  [Z-Beta = 109.911]  [Pearson Full 0.772302042, Pearson Zeroed 0.76683]</p> 	<p>Gata6_3769.1_combined_PWM_1.txt (K-afal)</p> 
<p>Gata6_3769.1_PWM_2.txt (K-afal)</p> 	<p>Gata6_3769.1_PWM_2.txt (K-afal)  [Z-Beta = 275.637]  [Pearson Full 0.772302042, Pearson Zeroed 0.709742]</p> 	<p>Gata6_3769.1_combined_PWM_2.txt (K-afal)  [Z-Beta = 50.3283]  [Pearson Full 0.626863775, Pearson Zeroed 0.622207]</p> 



# Gcm1\_3732.1


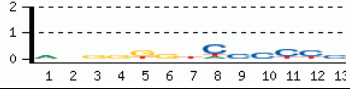
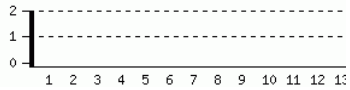
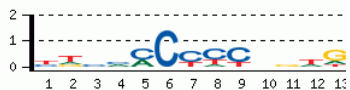

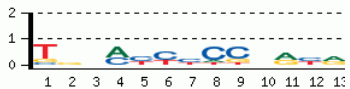
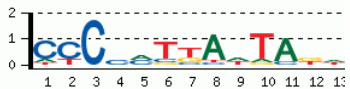



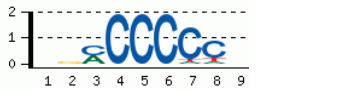
Array Version 1	Array Version 2	Combined
Gcm1_3732.1_v1_prim_auc.txt (SW) 	Gcm1_3732.1_v2_prim_auc.txt (SW) 	Gcm1_3732.1_combined_prim_auc.txt (SW) No PWM Available
Gcm1_3732.1_v1_prim_ic.txt (SW) 	Gcm1_3732.1_v2_prim_ic.txt (SW) 	Gcm1_3732.1_combined_prim_ic_trimmed.txt (SW) 
Gcm1_3732.1_v1_prim_untrimmed.txt (SW) 	Gcm1_3732.1_v2_prim_untrimmed.txt (SW) 	Gcm1_3732.1_combined_prim_untrimmed.txt (SW) 
Gcm1_3732.1_v1_sec_auc.txt (SW) 	Gcm1_3732.1_v2_sec_auc.txt (SW) 	Gcm1_3732.1_combined_sec_auc.txt (SW) 
Gcm1_3732.1_v1_sec_ic.txt (SW) 	Gcm1_3732.1_v2_sec_ic.txt (SW) 	Gcm1_3732.1_combined_sec_ic_trimmed.txt (SW) 
Gcm1_3732.1_v1_sec_untrimmed.txt (SW) 	Gcm1_3732.1_v2_sec_untrimmed.txt (SW) [Z-Beta = 79.5306] [Pearson Full 0.711024547, Pearson Zeroed 0.702563] 	Gcm1_3732.1_combined_sec_untrimmed.txt (SW) 
Gcm1_3732.1_v1_prim_trimmed.txt (RM8) 	Gcm1_3732.1_v2_prim_trimmed.txt (RM8) 	Gcm1_3732.1_RM8_combined_prim_trimmed.txt (RM8) No PWM Available
Gcm1_3732.1_v1_sec_trimmed.txt (RM8) 	Gcm1_3732.1_v2_sec_trimmed.txt (RM8) 	Gcm1_3732.1_RM8_combined_sec_trimmed.txt (RM8) No PWM Available
Gcm1_3732.1_v1_prim_untrimmed.txt (RM8) 	Gcm1_3732.1_v2_prim_untrimmed.txt (RM8) 	Gcm1_3732.1_RM8_combined_prim_untrimmed.txt (RM8) No PWM Available
Gcm1_3732.1_v1_sec_untrimmed.txt (RM8) 	Gcm1_3732.1_v2_sec_untrimmed.txt (RM8) [Z-Beta = 79.5306] [Pearson Full 0.711024547, Pearson Zeroed 0.702563] 	Gcm1_3732.1_RM8_combined_sec_untrimmed.txt (RM8) No PWM Available
Gcm1_3732.1_v1_RM35_prim_trimmed.txt (RM35) 	Gcm1_3732.1_v2_RM35_prim_trimmed.txt (RM35) [Z-Beta = 364.44] [Pearson Full 0.711024547, Pearson Zeroed 0.682973] 	Gcm1_3732.1_RM_combined_prim_trimmed.txt (RM35) 

		
Gcm1_3732.1_v1_RM35_sec_trimmed.txt (RM35) [Z-Beta = 1287.45] [Pearson Full 0.6890893, Pearson Zeroed NaN]	Gcm1_3732.1_v2_RM35_sec_trimmed.txt (RM35) [Z-Beta = 415.621] [Pearson Full 0.711024547, Pearson Zeroed 0.670349]	Gcm1_3732.1_RM_combined_sec_trimmed.txt (RM35) [Z-Beta = 1045.15] [Pearson Full 0.714747218, Pearson Zeroed NaN]
		
Gcm1_3732.1_v1_RM35_prim_untrimmed.txt (RM35)	Gcm1_3732.1_v2_RM35_prim_untrimmed.txt (RM35)	Gcm1_3732.1_RM_combined_prim_untrimmed.txt (RM35)
		
Gcm1_3732.1_v1_RM35_sec_untrimmed.txt (RM35)	Gcm1_3732.1_v2_RM35_sec_untrimmed.txt (RM35) [Z-Beta = 340.581] [Pearson Full 0.711024547, Pearson Zeroed 0.699094]	Gcm1_3732.1_RM_combined_sec_untrimmed.txt (RM35)
		
Gcm1_3732.1_PWM_10.txt (K-afal)	Gcm1_3732.1_PWM_10.txt (K-afal)	Gcm1_3732.1_combined_PWM_10.txt (K-afal)
		
Gcm1_3732.1_PWM_11.txt (K-afal)	Gcm1_3732.1_PWM_11.txt (K-afal)	Gcm1_3732.1_combined_PWM_11.txt (K-afal)
		
Gcm1_3732.1_PWM_1.txt (K-afal)	Gcm1_3732.1_PWM_1.txt (K-afal)	Gcm1_3732.1_combined_PWM_1.txt (K-afal)
		
Gcm1_3732.1_PWM_2.txt (K-afal)	Gcm1_3732.1_PWM_2.txt (K-afal)	Gcm1_3732.1_combined_PWM_2.txt (K-afal)
		
Gcm1_3732.1_PWM_3.txt (K-afal)	Gcm1_3732.1_PWM_3.txt (K-afal)	Gcm1_3732.1_combined_PWM_3.txt (K-afal)
		
Gcm1_3732.1_PWM_4.txt (K-afal)	Gcm1_3732.1_PWM_4.txt (K-afal)	Gcm1_3732.1_combined_PWM_4.txt (K-afal)
		
Gcm1_3732.1_PWM_5.txt (K-afal)	Gcm1_3732.1_PWM_5.txt (K-afal)	Gcm1_3732.1_combined_PWM_5.txt (K-afal)
		
Gcm1_3732.1_PWM_6.txt (K-afal)	Gcm1_3732.1_PWM_6.txt (K-afal)	Gcm1_3732.1_combined_PWM_6.txt (K-afal)
		



<p>Gcm1_3732.1_PWM_7.txt (K-afal)</p>	<p>Gcm1_3732.1_PWM_7.txt (K-afal)</p>	<p>Gcm1_3732.1_combined_PWM_7.txt (K-afal)</p>
<p>Gcm1_3732.1_PWM_8.txt (K-afal)</p>	<p>Gcm1_3732.1_PWM_8.txt (K-afal)</p>	<p>Gcm1_3732.1_combined_PWM_8.txt (K-afal)</p>
<p>Gcm1_3732.1_PWM_9.txt (K-afal)</p>	<p>Gcm1_3732.1_PWM_9.txt (K-afal)  [Z-Beta = 52.5385]  [Pearson Full 0.711024547, Pearson Zeroed 0.700974]</p>	<p>Gcm1_3732.1_combined_PWM_9.txt (K-afal)</p>
<p>Gcm1_3732.1_PWM_10.txt (Kafal)  No PWM Available</p>	<p>Gcm1_3732.1_PWM_10.txt (Kafal)</p>	<p>Gcm1_3732.1_combined_PWM_10.txt (Kafal)</p>
<p>Gcm1_3732.1_PWM_11.txt (Kafal)  No PWM Available</p>	<p>Gcm1_3732.1_PWM_11.txt (Kafal)</p>	<p>Gcm1_3732.1_combined_PWM_11.txt (Kafal)</p>
<p>Gcm1_3732.1_PWM_12.txt (Kafal)  No PWM Available</p>	<p>Gcm1_3732.1_PWM_12.txt (Kafal)</p>	<p>Gcm1_3732.1_combined_PWM_12.txt (Kafal)</p>

# Glis2\_1757.2

Array Version 1	Array Version 2	Combined
<p>Glis2_1757.2_v1_prim_auc.txt (SW)</p>	<p>Glis2_1757.2_v2_prim_auc.txt (SW)  [Z-Beta = 24.855]  [Pearson Full 0.577559623, Pearson Zeroed 0.559616]</p>	<p>Glis2_1757.2_combined_prim_auc.txt (SW)</p>
<p>Glis2_1757.2_v1_prim_ic.txt (SW)</p>	<p>Glis2_1757.2_v2_prim_ic.txt (SW)</p>	<p>Glis2_1757.2_combined_prim_ic_trimmed.txt (SW)  [Z-Beta = -40.4275]  [Pearson Full 0.415528425, Pearson Zeroed 0.388929]</p>
<p>Glis2_1757.2_v1_prim_untrimmed.txt (SW)</p>	<p>Glis2_1757.2_v2_prim_untrimmed.txt (SW)  [Z-Beta = 49.9953]  [Pearson Full 0.577559623, Pearson Zeroed 0.510325]</p>	<p>Glis2_1757.2_combined_prim_untrimmed.txt (SW)</p>
<p>Glis2_1757.2_v1_sec_auc.txt (SW)  [Z-Beta = 17.6551]  [Pearson Full 0.355409122, Pearson Zeroed 0.289761]</p>	<p>Glis2_1757.2_v2_sec_auc.txt (SW)  [Z-Beta = 15.4502]  [Pearson Full 0.577559623, Pearson Zeroed 0.572849]</p>	<p>Glis2_1757.2_combined_sec_auc.txt (SW)</p>
<p>Glis2_1757.2_v1_sec_ic.txt (SW)</p>	<p>Glis2_1757.2_v2_sec_ic.txt (SW)  [Z-Beta = -4.13136]  [Pearson Full 0.577559623, Pearson Zeroed 0.576864]</p>	<p>Glis2_1757.2_combined_sec_ic_trimmed.txt (SW)  [Z-Beta = -440.556]  [Pearson Full 0.415528425, Pearson Zeroed 0.267937]</p>
<p>Glis2_1757.2_v1_sec_untrimmed.txt (SW)</p>	<p>Glis2_1757.2_v2_sec_untrimmed.txt (SW)</p>	<p>Glis2_1757.2_combined_sec_untrimmed.txt (SW)  [Z-Beta = 28.3599]  [Pearson Full 0.415528425, Pearson Zeroed 0.398534]</p>
<p>Glis2_1757.2_v1_prim_trimmed.txt (RM8)</p>	<p>Glis2_1757.2_v2_prim_trimmed.txt (RM8)  [Z-Beta = -8.47378]  [Pearson Full 0.577559623, Pearson Zeroed 0.575146]</p>	<p>Glis2_1757.2_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Glis2_1757.2_v1_sec_trimmed.txt (RM8)</p>	<p>Glis2_1757.2_v2_sec_trimmed.txt (RM8)  [Z-Beta = -97.7495]  [Pearson Full 0.577559623, Pearson Zeroed 0.303054]</p>	<p>Glis2_1757.2_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>

<p>Glis2_1757.2_v1_prim_untrimmed.txt (RM8)</p> 	<p>Glis2_1757.2_v2_prim_untrimmed.txt (RM8)  [Z-Beta = 49.9953]  [Pearson Full 0.577559623, Pearson Zeroed 0.510325]</p> 	<p>Glis2_1757.2_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Glis2_1757.2_v1_sec_untrimmed.txt (RM8)</p> 	<p>Glis2_1757.2_v2_sec_untrimmed.txt (RM8)</p> 	<p>Glis2_1757.2_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>
<p>Glis2_1757.2_v1_RM35_prim_trimmed.txt (RM35)  [Z-Beta = 25.8169]  [Pearson Full 0.355409122, Pearson Zeroed 0.300477]</p> 	<p>Glis2_1757.2_v2_RM35_prim_trimmed.txt (RM35)</p> 	<p>Glis2_1757.2_RM_combined_prim_trimmed.txt (RM35)  [Z-Beta = 14.3566]  [Pearson Full 0.415528425, Pearson Zeroed 0.410922]</p> 
<p>Glis2_1757.2_v1_RM35_sec_trimmed.txt (RM35)</p> 	<p>Glis2_1757.2_v2_RM35_sec_trimmed.txt (RM35)  [Z-Beta = 69.3814]  [Pearson Full 0.577559623, Pearson Zeroed 0.474306]</p> 	<p>Glis2_1757.2_RM_combined_sec_trimmed.txt (RM35)</p> 
<p>Glis2_1757.2_v1_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Glis2_1757.2_v2_RM35_prim_untrimmed.txt (RM35)  [Z-Beta = 25.3802]  [Pearson Full 0.577559623, Pearson Zeroed 0.55819]</p> 	<p>Glis2_1757.2_RM_combined_prim_untrimmed.txt (RM35)</p> 
<p>Glis2_1757.2_v1_RM35_sec_untrimmed.txt (RM35)  [Z-Beta = 30.004]  [Pearson Full 0.355409122, Pearson Zeroed 0.302286]</p> 	<p>Glis2_1757.2_v2_RM35_sec_untrimmed.txt (RM35)  [Z-Beta = -14.829]  [Pearson Full 0.577559623, Pearson Zeroed 0.575355]</p> 	<p>Glis2_1757.2_RM_combined_sec_untrimmed.txt (RM35)  [Z-Beta = 18.342]  [Pearson Full 0.415528425, Pearson Zeroed 0.406159]</p> 
<p>Glis2_1757.2_PWM_1.txt (K-afal)</p> 	<p>Glis2_1757.2_PWM_1.txt (K-afal)  [Z-Beta = 26.0061]  [Pearson Full 0.577559623, Pearson Zeroed 0.557762]</p> 	<p>Glis2_1757.2_combined_PWM_1.txt (K-afal)  [Z-Beta = -7.59132]  [Pearson Full 0.415528425, Pearson Zeroed 0.414381]</p> 


















# Gm397\_1753.4




Array Version 1	Array Version 2	Combined
<p>Gm397_1753.4_v1_prim_auc.txt (SW)  [Z-Beta = 2177.35]  [Pearson Full 0.805930693, Pearson Zeroed 0.0573352]</p> 	<p>Gm397_1753.4_v2_prim_auc.txt (SW)</p> 	<p>Gm397_1753.4_combined_prim_auc.txt (SW)  No PWM Available</p>
<p>Gm397_1753.4_v1_prim_ic.txt (SW)  [Z-Beta = -2036.25]  [Pearson Full 0.805930693, Pearson Zeroed 0.424681]</p> 	<p>Gm397_1753.4_v2_prim_ic.txt (SW)</p> 	<p>Gm397_1753.4_combined_prim_ic_trimmed.txt (SW)</p> 
<p>Gm397_1753.4_v1_prim_untrimmed.txt (SW)  [Z-Beta = 6.4568]  [Pearson Full 0.805930693, Pearson Zeroed 0.805879]</p> 	<p>Gm397_1753.4_v2_prim_untrimmed.txt (SW)  [Z-Beta = 10.7521]  [Pearson Full 0.673416552, Pearson Zeroed 0.669913]</p> 	<p>Gm397_1753.4_combined_prim_untrimmed.txt (SW)</p> 
<p>Gm397_1753.4_v1_sec_auc.txt (SW)</p> 	<p>Gm397_1753.4_v2_sec_auc.txt (SW)</p> 	<p>Gm397_1753.4_combined_sec_auc.txt (SW)</p> 
<p>Gm397_1753.4_v1_sec_ic.txt (SW)  [Z-Beta = 65.229]  [Pearson Full 0.805930693, Pearson Zeroed 0.800271]</p> 	<p>Gm397_1753.4_v2_sec_ic.txt (SW)</p> 	<p>Gm397_1753.4_combined_sec_ic_trimmed.txt (SW)</p> 
<p>Gm397_1753.4_v1_sec_untrimmed.txt (SW)  [Z-Beta = 41.4828]  [Pearson Full 0.805930693, Pearson Zeroed 0.803627]</p> 	<p>Gm397_1753.4_v2_sec_untrimmed.txt (SW)  [Z-Beta = 41.4828]  [Pearson Full 0.673416552, Pearson Zeroed 0.66631]</p> 	<p>Gm397_1753.4_combined_sec_untrimmed.txt (SW)</p> 
<p>Gm397_1753.4_v1_prim_trimmed.txt (RM8)  [Z-Beta = 43.2143]  [Pearson Full 0.805930693, Pearson Zeroed 0.804809]</p> 	<p>Gm397_1753.4_v2_prim_trimmed.txt (RM8)</p> 	<p>Gm397_1753.4_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Gm397_1753.4_v1_sec_trimmed.txt (RM8)  [Z-Beta = -70.7469]  [Pearson Full 0.805930693, Pearson Zeroed 0.800276]</p> 	<p>Gm397_1753.4_v2_sec_trimmed.txt (RM8)</p> 	<p>Gm397_1753.4_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Gm397_1753.4_v1_prim_untrimmed.txt (RM8)  [Z-Beta = 16.2938]  [Pearson Full 0.805930693, Pearson Zeroed 0.805879]</p>	<p>Gm397_1753.4_v2_prim_untrimmed.txt (RM8)  [Z-Beta = 10.7521]  [Pearson Full 0.673416552, Pearson Zeroed 0.669913]</p>	<p>Gm397_1753.4_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>














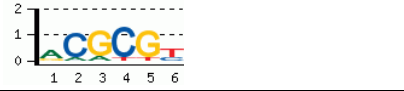
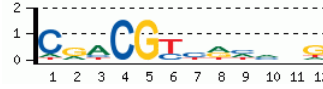









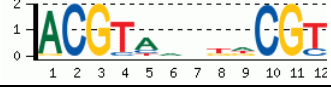
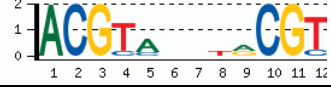
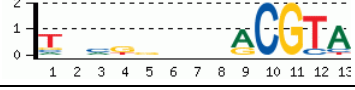




<p>0.805635]</p>	<p>0.669913]</p>	
<p>Gm397_1753.4_v1_sec_untrimmed.txt (RM8) [Z-Beta = 119.24] [Pearson Full 0.805930693, Pearson Zeroed 0.791789]</p>	<p>Gm397_1753.4_v2_sec_untrimmed.txt (RM8) [Z-Beta = 41.4828] [Pearson Full 0.673416552, Pearson Zeroed 0.66631]</p>	<p>Gm397_1753.4_RM8_combined_sec_untrimmed.txt (RM8) No PWM Available</p>
<p>Gm397_1753.4_v1_RM35_prim_trimmed.txt (RM35) [Z-Beta = 210.351] [Pearson Full 0.805930693, Pearson Zeroed 0.751612]</p>	<p>Gm397_1753.4_v2_RM35_prim_trimmed.txt (RM35) [Z-Beta = 210.351] [Pearson Full 0.805930693, Pearson Zeroed 0.751612]</p>	<p>Gm397_1753.4_RM_combined_prim_trimmed.txt (RM35) [Z-Beta = 210.351] [Pearson Full 0.805930693, Pearson Zeroed 0.751612]</p>
<p>Gm397_1753.4_v1_RM35_sec_trimmed.txt (RM35) [Z-Beta = 4.76247] [Pearson Full 0.805930693, Pearson Zeroed 0.805893]</p>	<p>Gm397_1753.4_v2_RM35_sec_trimmed.txt (RM35) [Z-Beta = 4.76247] [Pearson Full 0.805930693, Pearson Zeroed 0.805893]</p>	<p>Gm397_1753.4_RM_combined_sec_trimmed.txt (RM35) [Z-Beta = 4.76247] [Pearson Full 0.805930693, Pearson Zeroed 0.805893]</p>
<p>Gm397_1753.4_v1_RM35_prim_untrimmed.txt (RM35) [Z-Beta = -156.333] [Pearson Full 0.805930693, Pearson Zeroed 0.790979]</p>	<p>Gm397_1753.4_v2_RM35_prim_untrimmed.txt (RM35) [Z-Beta = -156.333] [Pearson Full 0.805930693, Pearson Zeroed 0.790979]</p>	<p>Gm397_1753.4_RM_combined_prim_untrimmed.txt (RM35) [Z-Beta = -156.333] [Pearson Full 0.805930693, Pearson Zeroed 0.790979]</p>
<p>Gm397_1753.4_v1_RM35_sec_untrimmed.txt (RM35) [Z-Beta = 29.6924] [Pearson Full 0.805930693, Pearson Zeroed 0.804444]</p>	<p>Gm397_1753.4_v2_RM35_sec_untrimmed.txt (RM35) [Z-Beta = 29.6924] [Pearson Full 0.805930693, Pearson Zeroed 0.804444]</p>	<p>Gm397_1753.4_RM_combined_sec_untrimmed.txt (RM35) [Z-Beta = 29.6924] [Pearson Full 0.805930693, Pearson Zeroed 0.804444]</p>
<p>Gm397_1753.4_PWM_1.txt (K-afal) [Z-Beta = 42.4139] [Pearson Full 0.805930693, Pearson Zeroed 0.804201]</p>	<p>Gm397_1753.4_PWM_1.txt (K-afal) [Z-Beta = 93.0025] [Pearson Full 0.673416552, Pearson Zeroed 0.631105]</p>	<p>Gm397_1753.4_combined_PWM_1.txt (K-afal) [Z-Beta = 44.4293] [Pearson Full 0.773732964, Pearson Zeroed 0.769737]</p>
<p>Gm397_1753.4_PWM_2.txt (K-afal) [Z-Beta = 81.1568] [Pearson Full 0.805930693, Pearson Zeroed 0.798832]</p>	<p>Gm397_1753.4_PWM_2.txt (K-afal) [Z-Beta = 34.9988] [Pearson Full 0.673416552, Pearson Zeroed 0.651544]</p>	<p>Gm397_1753.4_combined_PWM_2.txt (K-afal) [Z-Beta = 37.438] [Pearson Full 0.773732964, Pearson Zeroed 0.750968]</p>
<p>Gm397_1753.4_PWM_3.txt (K-afal) [Z-Beta = 165.956] [Pearson Full 0.805930693, Pearson Zeroed 0.767639]</p>	<p>Gm397_1753.4_PWM_3.txt (K-afal) [Z-Beta = 232.817] [Pearson Full 0.773732964, Pearson Zeroed 0.735513]</p>	<p>Gm397_1753.4_combined_PWM_3.txt (K-afal) [Z-Beta = 232.817] [Pearson Full 0.773732964, Pearson Zeroed 0.735513]</p>







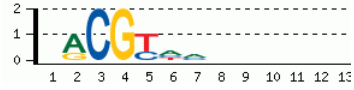
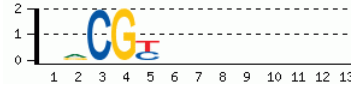
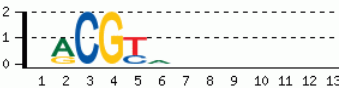



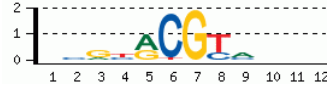











 <p>Gm397_1753.4_PWM_4.txt (K-afal) [Z-Beta = 36.9189] [Pearson Full 0.805930693, Pearson Zeroed 0.804314]</p>		 <p>Gm397_1753.4_combined_PWM_4.txt (K-afal)</p>
 <p>Gm397_1753.4_PWM_5.txt (K-afal) [Z-Beta = 114.521] [Pearson Full 0.805930693, Pearson Zeroed 0.801443]</p>	 <p>Gm397_1753.4_PWM_4.txt (K-afal)</p>	 <p>Gm397_1753.4_combined_PWM_5.txt (K-afal) [Z-Beta = 19.7538] [Pearson Full 0.773732964, Pearson Zeroed 0.761885]</p>
 <p>Gm397_1753.4_PWM_6.txt (K-afal) [Z-Beta = 91.9601] [Pearson Full 0.805930693, Pearson Zeroed 0.801473]</p>	 <p>Gm397_1753.4_PWM_5.txt (K-afal)</p>	
 <p>Gm397_1753.4_PWM_7.txt (K-afal) [Z-Beta = 48.8985] [Pearson Full 0.805930693, Pearson Zeroed 0.80397]</p>	 <p>Gm397_1753.4_PWM_6.txt (K-afal) [Z-Beta = 11.7142] [Pearson Full 0.673416552, Pearson Zeroed 0.67173]</p>	<p>Gm397_1753.4_combined_PWM_6.txt (K-afal) No PWM Available</p>
 <p>Gm397_1753.4_PWM_8.txt (K-afal) [Z-Beta = 48.8985] [Pearson Full 0.805930693, Pearson Zeroed 0.80397]</p>	 <p>Gm397_1753.4_PWM_7.txt (K-afal) [Z-Beta = 18.4685] [Pearson Full 0.673416552, Pearson Zeroed 0.668097]</p>	<p>Gm397_1753.4_combined_PWM_7.txt (K-afal) No PWM Available</p>
 <p>Gm397_1753.4_PWM_8.txt (K-afal) [Z-Beta = -38.9175] [Pearson Full 0.805930693, Pearson Zeroed 0.804996]</p>	 <p>Gm397_1753.4_PWM_8.txt (K-afal)</p>	<p>Gm397_1753.4_combined_PWM_8.txt (K-afal) No PWM Available</p>
<p>Gm397_1753.4_PWM_9.txt (Kafal) No PWM Available</p>	 <p>Gm397_1753.4_PWM_9.txt (Kafal) [Z-Beta = 20.2863] [Pearson Full 0.673416552, Pearson Zeroed 0.667154]</p>	<p>Gm397_1753.4_combined_PWM_9.txt (Kafal) No PWM Available</p>
<p>Gm397_1753.4_PWM_10.txt (Kafal) No PWM Available</p>	 <p>Gm397_1753.4_PWM_10.txt (Kafal)</p>	<p>Gm397_1753.4_combined_PWM_10.txt (Kafal) No PWM Available</p>
<p>Gm397_1753.4_PWM_11.txt (Kafal) No PWM Available</p>	 <p>Gm397_1753.4_PWM_11.txt (Kafal)</p>	<p>Gm397_1753.4_combined_PWM_11.txt (Kafal) No PWM Available</p>
<p>Gm397_1753.4_PWM_12.txt (Kafal) No PWM Available</p>	 <p>Gm397_1753.4_PWM_12.txt (Kafal) [Z-Beta = 35.815] [Pearson Full 0.673416552, Pearson Zeroed 0.658195]</p>	<p>Gm397_1753.4_combined_PWM_12.txt (Kafal) No PWM Available</p>

<p>Gm397_1753.4_PWM_13.txt (Kafal) No PWM Available</p>	<p>Gm397_1753.4_PWM_13.txt (Kafal)</p> 	<p>Gm397_1753.4_combined_PWM_13.txt (Kafal) No PWM Available</p>
<p>Gm397_1753.4_PWM_14.txt (Kafal) No PWM Available</p>	<p>Gm397_1753.4_PWM_14.txt (Kafal)</p> 	<p>Gm397_1753.4_combined_PWM_14.txt (Kafal) No PWM Available</p>
<p>Gm397_1753.4_PWM_15.txt (Kafal) No PWM Available</p>	<p>Gm397_1753.4_PWM_15.txt (Kafal)</p> 	<p>Gm397_1753.4_combined_PWM_15.txt (Kafal) No PWM Available</p>











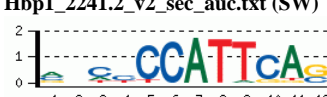

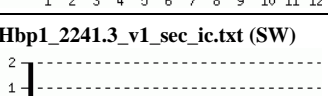
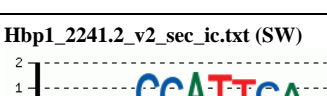
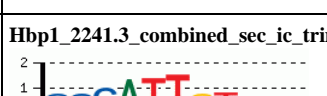
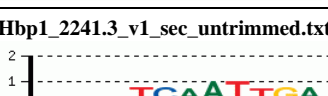
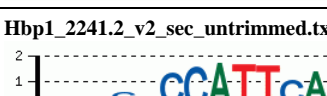
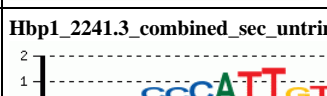
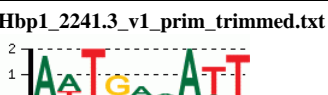
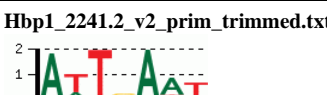
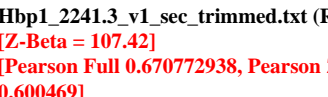
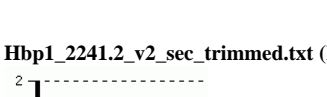
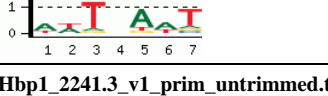
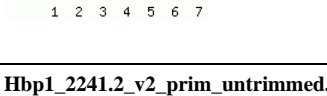
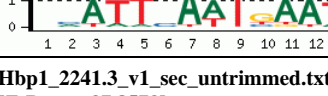


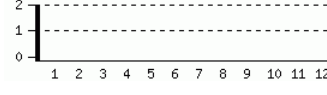
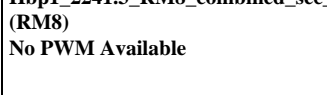
# Gmeb1\_1745.2

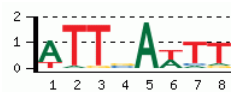
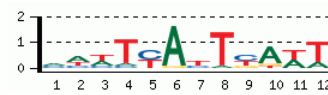

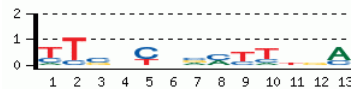

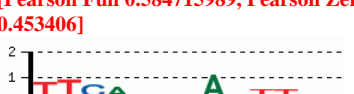

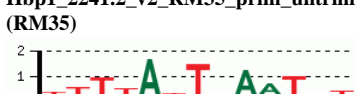
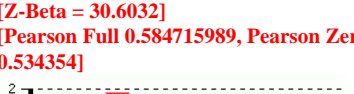
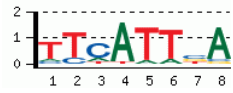
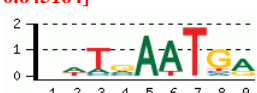







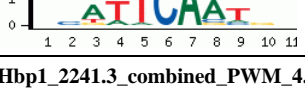
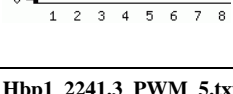
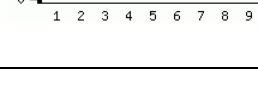



Array Version 1	Array Version 2	Combined
Gmeb1_1745.2_v1_prim_auc.txt (SW) 	Gmeb1_1745.2_v2_prim_auc.txt (SW) 	Gmeb1_1745.2_combined_prim_auc.txt (SW) 
Gmeb1_1745.2_v1_prim_ic.txt (SW) 	Gmeb1_1745.2_v2_prim_ic.txt (SW) 	Gmeb1_1745.2_combined_prim_ic_trimmed.txt (SW) 
Gmeb1_1745.2_v1_prim_untrimmed.txt (SW) 	Gmeb1_1745.2_v2_prim_untrimmed.txt (SW) 	Gmeb1_1745.2_combined_prim_untrimmed.txt (SW) 
Gmeb1_1745.2_v1_sec_auc.txt (SW) 	Gmeb1_1745.2_v2_sec_auc.txt (SW) 	Gmeb1_1745.2_combined_sec_auc.txt (SW) 
Gmeb1_1745.2_v1_sec_ic.txt (SW) 	Gmeb1_1745.2_v2_sec_ic.txt (SW) 	Gmeb1_1745.2_combined_sec_ic_trimmed.txt (SW) 
Gmeb1_1745.2_v1_sec_untrimmed.txt (SW) 	Gmeb1_1745.2_v2_sec_untrimmed.txt (SW) 	Gmeb1_1745.2_combined_sec_untrimmed.txt (SW) 
Gmeb1_1745.2_v1_prim_trimmed.txt (RM8) 	Gmeb1_1745.2_v2_prim_trimmed.txt (RM8) 	Gmeb1_1745.2_RM8_combined_prim_trimmed.txt (RM8) No PWM Available
Gmeb1_1745.2_v1_sec_trimmed.txt (RM8) [Z-Beta = 645.321] [Pearson Full 0.791208173, Pearson Zeroed 0.75058] 	Gmeb1_1745.2_v2_sec_trimmed.txt (RM8) 	Gmeb1_1745.2_RM8_combined_sec_trimmed.txt (RM8) No PWM Available
Gmeb1_1745.2_v1_prim_untrimmed.txt (RM8) 	Gmeb1_1745.2_v2_prim_untrimmed.txt (RM8) 	Gmeb1_1745.2_RM8_combined_prim_untrimmed.txt (RM8) No PWM Available
Gmeb1_1745.2_v1_sec_untrimmed.txt (RM8) 	Gmeb1_1745.2_v2_sec_untrimmed.txt (RM8) 	Gmeb1_1745.2_RM8_combined_sec_untrimmed.txt (RM8) No PWM Available
Gmeb1_1745.2_v1_RM35_prim_trimmed.txt (RM35) 	Gmeb1_1745.2_v2_RM35_prim_trimmed.txt (RM35) [Z-Beta = 1961.84] [Pearson Full 0.766374692, Pearson Zeroed 0.637667] 	Gmeb1_1745.2_RM_combined_prim_trimmed.txt (RM35) 

<p>Gmeb1_1745.2_v1_RM35_sec_trimmed.txt (RM35)  [Z-Beta = 25.5844]  [Pearson Full 0.791208173, Pearson Zeroed 0.791255]</p> 	<p>Gmeb1_1745.2_v2_RM35_sec_trimmed.txt (RM35)</p> 	<p>Gmeb1_1745.2_RM_combined_sec_trimmed.txt (RM35)  [Z-Beta = 1707.22]  [Pearson Full 0.804118325, Pearson Zeroed NaN]</p> 
<p>Gmeb1_1745.2_v1_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Gmeb1_1745.2_v2_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Gmeb1_1745.2_RM_combined_prim_untrimmed.txt (RM35)</p> 
<p>Gmeb1_1745.2_v1_RM35_sec_untrimmed.txt (RM35)  [Z-Beta = 797.254]  [Pearson Full 0.791208173, Pearson Zeroed 0.768005]</p> 	<p>Gmeb1_1745.2_v2_RM35_sec_untrimmed.txt (RM35)  [Z-Beta = 30.8735]  [Pearson Full 0.766374692, Pearson Zeroed 0.765627]</p> 	<p>Gmeb1_1745.2_RM_combined_sec_untrimmed.txt (RM35)</p> 
<p>Gmeb1_1745.2_PWM_1.txt (K-afal)  [Z-Beta = 109.753]  [Pearson Full 0.791208173, Pearson Zeroed 0.788489]</p> 	<p>Gmeb1_1745.2_PWM_1.txt (K-afal)</p> 	<p>Gmeb1_1745.2_combined_PWM_1.txt (K-afal)</p> 
<p>Gmeb1_1745.2_PWM_2.txt (K-afal)</p> 	<p>Gmeb1_1745.2_PWM_2.txt (K-afal)</p> 	<p>Gmeb1_1745.2_combined_PWM_2.txt (K-afal)</p> 
<p>Gmeb1_1745.2_PWM_3.txt (K-afal)  [Z-Beta = 59.3319]  [Pearson Full 0.791208173, Pearson Zeroed 0.789644]</p> 	<p>Gmeb1_1745.2_PWM_3.txt (K-afal)</p> 	<p>Gmeb1_1745.2_combined_PWM_3.txt (K-afal)  No PWM Available</p>
<p>Gmeb1_1745.2_PWM_4.txt (K-afal)  [Z-Beta = 66.6586]  [Pearson Full 0.791208173, Pearson Zeroed 0.785891]</p> 	<p>Gmeb1_1745.2_PWM_4.txt (K-afal)</p> 	<p>Gmeb1_1745.2_combined_PWM_4.txt (K-afal)  No PWM Available</p>
<p>Gmeb1_1745.2_PWM_5.txt (Kafal)  No PWM Available</p>	<p>Gmeb1_1745.2_PWM_5.txt (Kafal)</p> 	<p>Gmeb1_1745.2_combined_PWM_5.txt (Kafal)  No PWM Available</p>
<p>Gmeb1_1745.2_PWM_6.txt (Kafal)  No PWM Available</p>	<p>Gmeb1_1745.2_PWM_6.txt (Kafal)</p> 	<p>Gmeb1_1745.2_combined_PWM_6.txt (Kafal)  No PWM Available</p>
<p>Gmeb1_1745.2_PWM_7.txt (Kafal)  No PWM Available</p>	<p>Gmeb1_1745.2_PWM_7.txt (Kafal)</p> 	<p>Gmeb1_1745.2_combined_PWM_7.txt (Kafal)  No PWM Available</p>






Gmeb1_1745.2_PWM_8.txt (Kafal) No PWM Available	Gmeb1_1745.2_PWM_8.txt (Kafal) 	Gmeb1_1745.2_combined_PWM_8.txt (Kafal) No PWM Available
Gmeb1_1745.2_PWM_9.txt (Kafal) No PWM Available	Gmeb1_1745.2_PWM_9.txt (Kafal) 	Gmeb1_1745.2_combined_PWM_9.txt (Kafal) No PWM Available
Gmeb1_1745.2_PWM_10.txt (Kafal) No PWM Available	Gmeb1_1745.2_PWM_10.txt (Kafal) 	Gmeb1_1745.2_combined_PWM_10.txt (Kafal) No PWM Available
Gmeb1_1745.2_PWM_11.txt (Kafal) No PWM Available	Gmeb1_1745.2_PWM_11.txt (Kafal) 	Gmeb1_1745.2_combined_PWM_11.txt (Kafal) No PWM Available
Gmeb1_1745.2_PWM_12.txt (Kafal) No PWM Available	Gmeb1_1745.2_PWM_12.txt (Kafal) 	Gmeb1_1745.2_combined_PWM_12.txt (Kafal) No PWM Available
Gmeb1_1745.2_PWM_13.txt (Kafal) No PWM Available	Gmeb1_1745.2_PWM_13.txt (Kafal) 	Gmeb1_1745.2_combined_PWM_13.txt (Kafal) No PWM Available
Gmeb1_1745.2_PWM_14.txt (Kafal) No PWM Available	Gmeb1_1745.2_PWM_14.txt (Kafal) 	Gmeb1_1745.2_combined_PWM_14.txt (Kafal) No PWM Available
Gmeb1_1745.2_PWM_15.txt (Kafal) No PWM Available	Gmeb1_1745.2_PWM_15.txt (Kafal) 	Gmeb1_1745.2_combined_PWM_15.txt (Kafal) No PWM Available
Gmeb1_1745.2_PWM_16.txt (Kafal) No PWM Available	Gmeb1_1745.2_PWM_16.txt (Kafal) 	Gmeb1_1745.2_combined_PWM_16.txt (Kafal) No PWM Available
Gmeb1_1745.2_PWM_17.txt (Kafal) No PWM Available	Gmeb1_1745.2_PWM_17.txt (Kafal) 	Gmeb1_1745.2_combined_PWM_17.txt (Kafal) No PWM Available
Gmeb1_1745.2_PWM_18.txt (Kafal) No PWM Available	Gmeb1_1745.2_PWM_18.txt (Kafal) 	Gmeb1_1745.2_combined_PWM_18.txt (Kafal) No PWM Available

# Hbp1\_2241.2

Array Version 1	Array Version 2	Combined
Hbp1_2241.3_v1_prim_auc.txt (SW) 	Hbp1_2241.2_v2_prim_auc.txt (SW) 	Hbp1_2241.2_combined_prim_auc.txt (SW) 
Hbp1_2241.3_v1_prim_ic.txt (SW) 	Hbp1_2241.2_v2_prim_ic.txt (SW) 	Hbp1_2241.2_combined_prim_ic_trimmed.txt (SW) 
Hbp1_2241.3_v1_prim_untrimmed.txt (SW) 	Hbp1_2241.2_v2_prim_untrimmed.txt (SW) 	Hbp1_2241.3_combined_prim_untrimmed.txt (SW) 
Hbp1_2241.3_v1_sec_auc.txt (SW) [Z-Beta = 5.20633] [Pearson Full 0.670772938, Pearson Zeroed 0.663194] 	Hbp1_2241.2_v2_sec_auc.txt (SW) 	Hbp1_2241.3_combined_sec_auc.txt (SW) 
Hbp1_2241.3_v1_sec_ic.txt (SW) 	Hbp1_2241.2_v2_sec_ic.txt (SW) 	Hbp1_2241.3_combined_sec_ic_trimmed.txt (SW) 
Hbp1_2241.3_v1_sec_untrimmed.txt (SW) 	Hbp1_2241.2_v2_sec_untrimmed.txt (SW) 	Hbp1_2241.3_combined_sec_untrimmed.txt (SW) 
Hbp1_2241.3_v1_prim_trimmed.txt (RM8) 	Hbp1_2241.2_v2_prim_trimmed.txt (RM8) 	Hbp1_2241.3_RM8_combined_prim_trimmed.txt (RM8) No PWM Available
Hbp1_2241.3_v1_sec_trimmed.txt (RM8) [Z-Beta = 107.42] [Pearson Full 0.670772938, Pearson Zeroed 0.600469] 	Hbp1_2241.2_v2_sec_trimmed.txt (RM8) 	Hbp1_2241.3_RM8_combined_sec_trimmed.txt (RM8) No PWM Available
Hbp1_2241.3_v1_prim_untrimmed.txt (RM8) 	Hbp1_2241.2_v2_prim_untrimmed.txt (RM8) 	Hbp1_2241.3_RM8_combined_prim_untrimmed.txt (RM8) No PWM Available
Hbp1_2241.3_v1_sec_untrimmed.txt (RM8) [Z-Beta = 37.8573] [Pearson Full 0.670772938, Pearson Zeroed 0.651308] 	Hbp1_2241.2_v2_sec_untrimmed.txt (RM8) 	Hbp1_2241.3_RM8_combined_sec_untrimmed.txt (RM8) No PWM Available
Hbp1_2241.3_v1_RM35_prim_trimmed.txt (RM35) 	Hbp1_2241.2_v2_RM35_prim_trimmed.txt (RM35) 	Hbp1_2241.3_RM_combined_prim_trimmed.txt (RM35) 

 <p>Hbp1_2241.3_v1_RM35_sec_trimmed.txt (RM35)</p>	 <p>Hbp1_2241.2_v2_RM35_sec_trimmed.txt (RM35)</p>	 <p>Hbp1_2241.3_RM_combined_sec_trimmed.txt (RM35)  [Z-Beta = 51.461]  [Pearson Full 0.584715989, Pearson Zeroed 0.453406]</p>
 <p>Hbp1_2241.3_v1_RM35_prim_untrimmed.txt (RM35)</p>	 <p>Hbp1_2241.2_v2_RM35_prim_untrimmed.txt (RM35)</p>	 <p>Hbp1_2241.3_RM_combined_prim_untrimmed.txt (RM35)  [Z-Beta = 30.6032]  [Pearson Full 0.584715989, Pearson Zeroed 0.534354]</p>
 <p>Hbp1_2241.3_v1_RM35_sec_untrimmed.txt (RM35)</p>	 <p>Hbp1_2241.2_v2_RM35_sec_untrimmed.txt (RM35)</p>	 <p>Hbp1_2241.3_RM_combined_sec_untrimmed.txt (RM35)</p>
<p>Hbp1_2241.3_PWM_10.txt (K-afal)  [Z-Beta = 14.9875]  [Pearson Full 0.670772938, Pearson Zeroed 0.660292]</p> 	<p>Hbp1_2241.2_PWM_10.txt (K-afal)  No PWM Available</p>	<p>Hbp1_2241.3_combined_PWM_10.txt (K-afal)  No PWM Available</p>
<p>Hbp1_2241.3_PWM_1.txt (K-afal)  [Z-Beta = 30.801]  [Pearson Full 0.670772938, Pearson Zeroed 0.645164]</p> 	<p>Hbp1_2241.2_PWM_1.txt (K-afal)</p> 	<p>Hbp1_2241.3_combined_PWM_1.txt (K-afal)</p> 
<p>Hbp1_2241.3_PWM_2.txt (K-afal)</p> 	<p>Hbp1_2241.2_PWM_2.txt (K-afal)</p> 	<p>Hbp1_2241.3_combined_PWM_2.txt (K-afal)  [Z-Beta = 1.80134]  [Pearson Full 0.584715989, Pearson Zeroed 0.584165]</p> 
<p>Hbp1_2241.3_PWM_3.txt (K-afal)</p> 	<p>Hbp1_2241.2_PWM_3.txt (K-afal)</p> 	<p>Hbp1_2241.3_combined_PWM_3.txt (K-afal)</p> 
<p>Hbp1_2241.3_PWM_4.txt (K-afal)</p> 	<p>Hbp1_2241.2_PWM_4.txt (K-afal)</p> 	<p>Hbp1_2241.3_combined_PWM_4.txt (K-afal)  [Z-Beta = 47.6867]  [Pearson Full 0.584715989, Pearson Zeroed 0.495734]</p> 
<p>Hbp1_2241.3_PWM_5.txt (K-afal)</p> 	<p>Hbp1_2241.2_PWM_5.txt (K-afal)  No PWM Available</p>	<p>Hbp1_2241.3_combined_PWM_5.txt (K-afal)</p> 



<p><b>Hbp1_2241.3_PWM_6.txt (K-afal)</b></p> 	<p><b>Hbp1_2241.2_PWM_6.txt (K-afal)</b> No PWM Available</p>	<p><b>Hbp1_2241.3_combined_PWM_6.txt (K-afal)</b></p> 
<p><b>Hbp1_2241.3_PWM_7.txt (K-afal)</b></p> 	<p><b>Hbp1_2241.2_PWM_7.txt (K-afal)</b> No PWM Available</p>	<p><b>Hbp1_2241.3_combined_PWM_7.txt (K-afal)</b> No PWM Available</p>
<p><b>Hbp1_2241.3_PWM_8.txt (K-afal)</b></p> 	<p><b>Hbp1_2241.2_PWM_8.txt (K-afal)</b> No PWM Available</p>	<p><b>Hbp1_2241.3_combined_PWM_8.txt (K-afal)</b> No PWM Available</p>
<p><b>Hbp1_2241.3_PWM_9.txt (K-afal)</b></p> 	<p><b>Hbp1_2241.2_PWM_9.txt (K-afal)</b> No PWM Available</p>	<p><b>Hbp1_2241.3_combined_PWM_9.txt (K-afal)</b> No PWM Available</p>














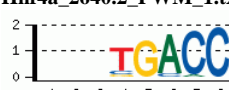

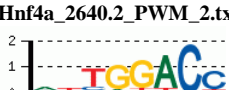


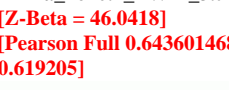
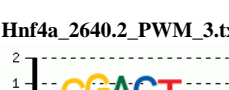
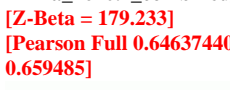
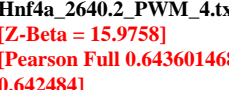
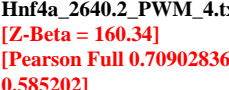
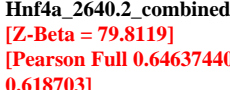
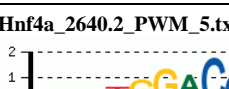
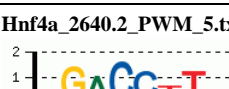

# Hic1\_2816.2

Array Version 1	Array Version 2	Combined
Hic1_2816.2_v1_prim_auc.txt (SW) 	Hic1_2816.2_v2_prim_auc.txt (SW) 	Hic1_2816.2_combined_prim_auc.txt (SW) No PWM Available
Hic1_2816.2_v1_prim_ic.txt (SW) [Z-Beta = 139.191] [Pearson Full 0.673347795, Pearson Zeroed 0.585147] 	Hic1_2816.2_v2_prim_ic.txt (SW) 	Hic1_2816.2_combined_prim_ic_trimmed.txt (SW) 
Hic1_2816.2_v1_prim_untrimmed.txt (SW) 	Hic1_2816.2_v2_prim_untrimmed.txt (SW) 	Hic1_2816.2_combined_prim_untrimmed.txt (SW) 
Hic1_2816.2_v1_sec_auc.txt (SW) 	Hic1_2816.2_v2_sec_auc.txt (SW) 	Hic1_2816.2_combined_sec_auc.txt (SW) 
Hic1_2816.2_v1_sec_ic.txt (SW) 	Hic1_2816.2_v2_sec_ic.txt (SW) 	Hic1_2816.2_combined_sec_ic_trimmed.txt (SW) 
Hic1_2816.2_v1_sec_untrimmed.txt (SW) 	Hic1_2816.2_v2_sec_untrimmed.txt (SW) 	Hic1_2816.2_combined_sec_untrimmed.txt (SW) 
Hic1_2816.2_v1_prim_trimmed.txt (RM8) 	Hic1_2816.2_v2_prim_trimmed.txt (RM8) 	Hic1_2816.2_RM8_combined_prim_trimmed.txt (RM8) No PWM Available
Hic1_2816.2_v1_sec_trimmed.txt (RM8) 	Hic1_2816.2_v2_sec_trimmed.txt (RM8) 	Hic1_2816.2_RM8_combined_sec_trimmed.txt (RM8) No PWM Available
Hic1_2816.2_v1_prim_untrimmed.txt (RM8) 	Hic1_2816.2_v2_prim_untrimmed.txt (RM8) 	Hic1_2816.2_RM8_combined_prim_untrimmed.txt (RM8) No PWM Available
Hic1_2816.2_v1_sec_untrimmed.txt (RM8) 	Hic1_2816.2_v2_sec_untrimmed.txt (RM8) 	Hic1_2816.2_RM8_combined_sec_untrimmed.txt (RM8) No PWM Available
Hic1_2816.2_v1_RM35_prim_trimmed.txt (RM35) 	Hic1_2816.2_v2_RM35_prim_trimmed.txt (RM35) 	Hic1_2816.2_RM_combined_prim_trimmed.txt (RM35) 
Hic1_2816.2_v1_RM35_sec_trimmed.txt (RM35) 	Hic1_2816.2_v2_RM35_sec_trimmed.txt (RM35) 	Hic1_2816.2_RM_combined_sec_trimmed.txt (RM35) 

<b>Hic1_2816.2_v1_RM35_prim_untrimmed.txt (RM35)</b> 	<b>Hic1_2816.2_v2_RM35_prim_untrimmed.txt (RM35)</b> 	<b>Hic1_2816.2_RM_combined_prim_untrimmed.txt (RM35)</b> 	
<b>Hic1_2816.2_v1_RM35_sec_untrimmed.txt (RM35)</b> 	<b>Hic1_2816.2_v2_RM35_sec_untrimmed.txt (RM35)</b> 	<b>Hic1_2816.2_RM_combined_sec_untrimmed.txt (RM35)</b> 	
<b>Hic1_2816.2_PWM_1.txt (K-afal)</b> 	<b>Hic1_2816.2_PWM_1.txt (K-afal)</b> 	<b>Hic1_2816.2_combined_PWM_1.txt (K-afal)</b> 	
<b>Hic1_2816.2_PWM_2.txt (K-afal)</b> 	<b>Hic1_2816.2_PWM_2.txt (K-afal)</b> [Z-Beta = 22.4675] [Pearson Full 0.454823915, Pearson Zeroed NaN] 	<b>Hic1_2816.2_combined_PWM_2.txt (K-afal)</b> 	
<b>Hic1_2816.2_PWM_3.txt (K-afal)</b> [Z-Beta = 344.374] [Pearson Full 0.673347795, Pearson Zeroed 0.522379] 	<b>Hic1_2816.2_PWM_3.txt (K-afal)</b> No PWM Available	<b>Hic1_2816.2_combined_PWM_3.txt (K-afal)</b> [Z-Beta = 315.944] [Pearson Full 0.661508291, Pearson Zeroed NaN] 	
<b>Hic1_2816.2_PWM_4.txt (K-afal)</b> 	<b>Hic1_2816.2_PWM_4.txt (K-afal)</b> No PWM Available	<b>Hic1_2816.2_combined_PWM_4.txt (K-afal)</b> 	

# Hnf4a\_2640.2

Array Version 1	Array Version 2	Combined
<p>Hnf4a_2640.2_v1_prim_auc.txt (SW) [Z-Beta = 13.6273] [Pearson Full 0.643601468, Pearson Zeroed 0.641917]</p>	<p>Hnf4a_2640.2_v2_prim_auc.txt (SW)</p>	<p>Hnf4a_2640.2_combined_prim_auc.txt (SW) No PWM Available</p>
<p>Hnf4a_2640.2_v1_prim_ic.txt (SW) [Z-Beta = 55.1772] [Pearson Full 0.643601468, Pearson Zeroed 0.627379]</p>	<p>Hnf4a_2640.2_v2_prim_ic.txt (SW) [Z-Beta = 23.3759] [Pearson Full 0.709028361, Pearson Zeroed 0.70268]</p>	<p>Hnf4a_2640.2_combined_prim_ic_trimmed.txt (SW)</p>
<p>Hnf4a_2640.2_v1_prim_untrimmed.txt (SW)</p>	<p>Hnf4a_2640.2_v2_prim_untrimmed.txt (SW)</p>	<p>Hnf4a_2640.2_combined_prim_untrimmed.txt (SW)</p>
<p>Hnf4a_2640.2_v1_sec_auc.txt (SW)</p>	<p>Hnf4a_2640.2_v2_sec_auc.txt (SW)</p>	<p>Hnf4a_2640.2_combined_sec_auc.txt (SW)</p>
<p>Hnf4a_2640.2_v1_sec_ic.txt (SW)</p>	<p>Hnf4a_2640.2_v2_sec_ic.txt (SW)</p>	<p>Hnf4a_2640.2_combined_sec_ic_trimmed.txt (SW) [Z-Beta = 23.2327] [Pearson Full 0.646374405, Pearson Zeroed 0.628006]</p>
<p>Hnf4a_2640.2_v1_sec_untrimmed.txt (SW) [Z-Beta = 5.36457] [Pearson Full 0.643601468, Pearson Zeroed 0.641395]</p>	<p>Hnf4a_2640.2_v2_sec_untrimmed.txt (SW)</p>	<p>Hnf4a_2640.2_combined_sec_untrimmed.txt (SW)</p>
<p>Hnf4a_2640.2_v1_prim_trimmed.txt (RM8)</p>	<p>Hnf4a_2640.2_v2_prim_trimmed.txt (RM8)</p>	<p>Hnf4a_2640.2_RM8_combined_prim_trimmed.txt (RM8) No PWM Available</p>
<p>Hnf4a_2640.2_v1_sec_trimmed.txt (RM8)</p>	<p>Hnf4a_2640.2_v2_sec_trimmed.txt (RM8)</p>	<p>Hnf4a_2640.2_RM8_combined_sec_trimmed.txt (RM8) No PWM Available</p>
<p>Hnf4a_2640.2_v1_prim_untrimmed.txt (RM8)</p>	<p>Hnf4a_2640.2_v2_prim_untrimmed.txt (RM8)</p>	<p>Hnf4a_2640.2_RM8_combined_prim_untrimmed.txt (RM8) No PWM Available</p>
<p>Hnf4a_2640.2_v1_sec_untrimmed.txt (RM8)</p>	<p>Hnf4a_2640.2_v2_sec_untrimmed.txt (RM8)</p>	<p>Hnf4a_2640.2_RM8_combined_sec_untrimmed.txt (RM8) No PWM Available</p>


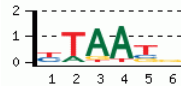
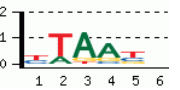

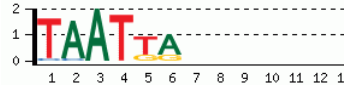
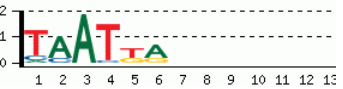
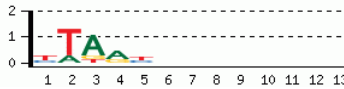
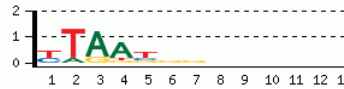
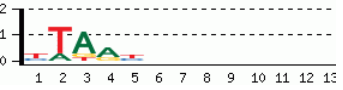
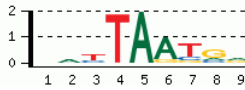
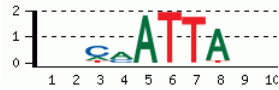







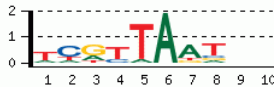


<p>Hnf4a_2640.2_v1_RM35_prim_trimmed.txt (RM35)</p> 	<p>Hnf4a_2640.2_v2_RM35_prim_trimmed.txt (RM35)</p> 	<p>Hnf4a_2640.2_RM_combined_prim_trimmed.txt (RM35)</p> 
<p>Hnf4a_2640.2_v1_RM35_sec_trimmed.txt (RM35)</p> <p>[Z-Beta = 118] [Pearson Full 0.643601468, Pearson Zeroed 0.595744]</p> 	<p>Hnf4a_2640.2_v2_RM35_sec_trimmed.txt (RM35)</p> <p>[Z-Beta = 35.7034] [Pearson Full 0.709028361, Pearson Zeroed 0.696041]</p> 	<p>Hnf4a_2640.2_RM_combined_sec_trimmed.txt (RM35)</p> <p>[Z-Beta = 89.6806] [Pearson Full 0.646374405, Pearson Zeroed 0.603281]</p> 
<p>Hnf4a_2640.2_v1_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Hnf4a_2640.2_v2_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Hnf4a_2640.2_RM_combined_prim_untrimmed.txt (RM35)</p> 
<p>Hnf4a_2640.2_v1_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Hnf4a_2640.2_v2_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Hnf4a_2640.2_RM_combined_sec_untrimmed.txt (RM35)</p> 
<p>Hnf4a_2640.2_PWM_1.txt (K-afal)</p> 	<p>Hnf4a_2640.2_PWM_1.txt (K-afal)</p> 	<p>Hnf4a_2640.2_combined_PWM_1.txt (K-afal)</p> <p>[Z-Beta = 31.4649] [Pearson Full 0.646374405, Pearson Zeroed 0.632195]</p> 
<p>Hnf4a_2640.2_PWM_2.txt (K-afal)</p> 	<p>Hnf4a_2640.2_PWM_2.txt (K-afal)</p> <p>[Z-Beta = 264.998] [Pearson Full 0.709028361, Pearson Zeroed 0.454899]</p> 	<p>Hnf4a_2640.2_combined_PWM_2.txt (K-afal)</p> <p>[Z-Beta = 51.7006] [Pearson Full 0.646374405, Pearson Zeroed 0.632821]</p> 
<p>Hnf4a_2640.2_PWM_3.txt (K-afal)</p> <p>[Z-Beta = 46.0418] [Pearson Full 0.643601468, Pearson Zeroed 0.619205]</p> 	<p>Hnf4a_2640.2_PWM_3.txt (K-afal)</p> 	<p>Hnf4a_2640.2_combined_PWM_3.txt (K-afal)</p> <p>[Z-Beta = 179.233] [Pearson Full 0.646374405, Pearson Zeroed 0.659485]</p> 
<p>Hnf4a_2640.2_PWM_4.txt (K-afal)</p> <p>[Z-Beta = 15.9758] [Pearson Full 0.643601468, Pearson Zeroed 0.642484]</p> 	<p>Hnf4a_2640.2_PWM_4.txt (K-afal)</p> <p>[Z-Beta = 160.34] [Pearson Full 0.709028361, Pearson Zeroed 0.585202]</p> 	<p>Hnf4a_2640.2_combined_PWM_4.txt (K-afal)</p> <p>[Z-Beta = 79.8119] [Pearson Full 0.646374405, Pearson Zeroed 0.618703]</p> 
<p>Hnf4a_2640.2_PWM_5.txt (K-afal)</p> 	<p>Hnf4a_2640.2_PWM_5.txt (K-afal)</p> 	<p>Hnf4a_2640.2_combined_PWM_5.txt (K-afal)</p> <p>No PWM Available</p>
<p>Hnf4a_2640.2_PWM_6.txt (K-afal)</p> 	<p>Hnf4a_2640.2_PWM_6.txt (K-afal)</p> <p>[Z-Beta = 66.5207] [Pearson Full 0.709028361, Pearson Zeroed 0.688633]</p>	<p>Hnf4a_2640.2_combined_PWM_6.txt (K-afal)</p> <p>No PWM Available</p>


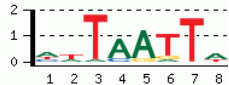





<b>Hnf4a_2640.2_PWM_7.txt (K-afal)</b> 	<b>Hnf4a_2640.2_PWM_7.txt (K-afal)</b> 	<b>Hnf4a_2640.2_combined_PWM_7.txt (K-afal)</b> No PWM Available
<b>Hnf4a_2640.2_PWM_8.txt (K-afal)</b> <b>[Z-Beta = 262.2]</b> <b>[Pearson Full 0.643601468, Pearson Zeroed 0.540228]</b> 	<b>Hnf4a_2640.2_PWM_8.txt (K-afal)</b> <b>[Z-Beta = 15.9704]</b> <b>[Pearson Full 0.709028361, Pearson Zeroed 0.705908]</b> 	<b>Hnf4a_2640.2_combined_PWM_8.txt (K-afal)</b> No PWM Available
<b>Hnf4a_2640.2_PWM_9.txt (Kafal)</b> No PWM Available	<b>Hnf4a_2640.2_PWM_9.txt (Kafal)</b> 	<b>Hnf4a_2640.2_combined_PWM_9.txt (Kafal)</b> No PWM Available
<b>Hnf4a_2640.2_PWM_10.txt (Kafal)</b> No PWM Available	<b>Hnf4a_2640.2_PWM_10.txt (Kafal)</b> 	<b>Hnf4a_2640.2_combined_PWM_10.txt (Kafal)</b> No PWM Available
<b>Hnf4a_2640.2_PWM_11.txt (Kafal)</b> No PWM Available	<b>Hnf4a_2640.2_PWM_11.txt (Kafal)</b> <b>[Z-Beta = 8.21182]</b> <b>[Pearson Full 0.709028361, Pearson Zeroed 0.708129]</b> 	<b>Hnf4a_2640.2_combined_PWM_11.txt (Kafal)</b> No PWM Available
<b>Hnf4a_2640.2_PWM_12.txt (Kafal)</b> No PWM Available	<b>Hnf4a_2640.2_PWM_12.txt (Kafal)</b> 	<b>Hnf4a_2640.2_combined_PWM_12.txt (Kafal)</b> No PWM Available

# Hoxa3\_2783.2

Array Version 1	Array Version 2	Combined
Hoxa3_2783.2_v1_prim_auc.txt (SW) 	Hoxa3_2783.2_v2_prim_auc.txt (SW) 	Hoxa3_2783.2_combined_prim_auc.txt (SW) 
Hoxa3_2783.2_v1_prim_ic.txt (SW) 	Hoxa3_2783.2_v2_prim_ic.txt (SW) 	Hoxa3_2783.2_combined_prim_ic_trimmed.txt (SW) 
Hoxa3_2783.2_v1_prim_untrimmed.txt (SW) 	Hoxa3_2783.2_v2_prim_untrimmed.txt (SW) 	Hoxa3_2783.2_combined_prim_untrimmed.txt (SW) 
Hoxa3_2783.2_v1_sec_auc.txt (SW) 	Hoxa3_2783.2_v2_sec_auc.txt (SW) 	Hoxa3_2783.2_combined_sec_auc.txt (SW) 
Hoxa3_2783.2_v1_sec_ic.txt (SW) 	Hoxa3_2783.2_v2_sec_ic.txt (SW) 	Hoxa3_2783.2_combined_sec_ic_trimmed.txt (SW) [Z-Beta = 9.33216] [Pearson Full 0.838253637, Pearson Zeroed 0.836498] 
Hoxa3_2783.2_v1_sec_untrimmed.txt (SW) 	Hoxa3_2783.2_v2_sec_untrimmed.txt (SW) [Z-Beta = 57.3572] [Pearson Full 0.850148494, Pearson Zeroed 0.846262] 	Hoxa3_2783.2_combined_sec_untrimmed.txt (SW) 
Hoxa3_2783.2_v1_prim_trimmed.txt (RM8) 	Hoxa3_2783.2_v2_prim_trimmed.txt (RM8) [Z-Beta = 48.5686] [Pearson Full 0.850148494, Pearson Zeroed 0.847341] 	Hoxa3_2783.2_RM8_combined_prim_trimmed.txt (RM8) No PWM Available
Hoxa3_2783.2_v1_sec_trimmed.txt (RM8) 	Hoxa3_2783.2_v2_sec_trimmed.txt (RM8) 	Hoxa3_2783.2_RM8_combined_sec_trimmed.txt (RM8) No PWM Available
Hoxa3_2783.2_v1_prim_untrimmed.txt (RM8) 	Hoxa3_2783.2_v2_prim_untrimmed.txt (RM8) 	Hoxa3_2783.2_RM8_combined_prim_untrimmed.txt (RM8) No PWM Available
Hoxa3_2783.2_v1_sec_untrimmed.txt (RM8) [Z-Beta = 39.1322] [Pearson Full 0.79234062, Pearson Zeroed 0.786363] 	Hoxa3_2783.2_v2_sec_untrimmed.txt (RM8) [Z-Beta = 57.3572] [Pearson Full 0.850148494, Pearson Zeroed 0.846262] 	Hoxa3_2783.2_RM8_combined_sec_untrimmed.txt (RM8) No PWM Available




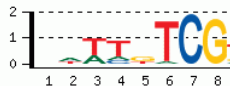




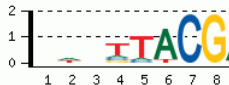
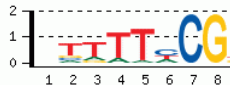

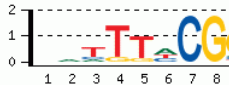


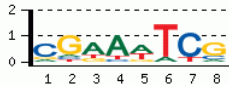


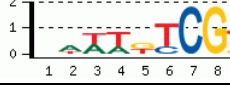

<p>Hoxa3_2783.2_v1_RM35_prim_trimmed.txt (RM35)</p> 	<p>Hoxa3_2783.2_v2_RM35_prim_trimmed.txt (RM35)</p> <p>[Z-Beta = 348.836] [Pearson Full 0.850148494, Pearson Zeroed 0.824213]</p> 	<p>Hoxa3_2783.2_RM_combined_prim_trimmed.txt (RM35)</p> <p>[Z-Beta = 108.714] [Pearson Full 0.838253637, Pearson Zeroed 0.840232]</p> 
<p>Hoxa3_2783.2_v1_RM35_sec_trimmed.txt (RM35)</p> 	<p>Hoxa3_2783.2_v2_RM35_sec_trimmed.txt (RM35)</p> <p>[Z-Beta = 296.095] [Pearson Full 0.850148494, Pearson Zeroed 0.808825]</p> 	<p>Hoxa3_2783.2_RM_combined_sec_trimmed.txt (RM35)</p> <p>[Z-Beta = 43.7357] [Pearson Full 0.838253637, Pearson Zeroed 0.832717]</p> 
<p>Hoxa3_2783.2_v1_RM35_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = 1033.78] [Pearson Full 0.79234062, Pearson Zeroed 0.456788]</p> 	<p>Hoxa3_2783.2_v2_RM35_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = 174.203] [Pearson Full 0.850148494, Pearson Zeroed 0.845001]</p> 	<p>Hoxa3_2783.2_RM_combined_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = 842.149] [Pearson Full 0.838253637, Pearson Zeroed 0.742625]</p> 
<p>Hoxa3_2783.2_v1_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Hoxa3_2783.2_v2_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Hoxa3_2783.2_RM_combined_sec_untrimmed.txt (RM35)</p> <p>[Z-Beta = 36.8357] [Pearson Full 0.838253637, Pearson Zeroed 0.833383]</p> 
<p>Hoxa3_2783.2_PWM_1.txt (K-afal)</p> <p>[Z-Beta = 90.9031] [Pearson Full 0.79234062, Pearson Zeroed 0.77454]</p> 	<p>Hoxa3_2783.2_PWM_1.txt (K-afal)</p> <p>[Z-Beta = 198.756] [Pearson Full 0.850148494, Pearson Zeroed 0.828913]</p> 	<p>Hoxa3_2783.2_combined_PWM_1.txt (K-afal)</p> 
<p>Hoxa3_2783.2_PWM_2.txt (K-afal)</p> 	<p>Hoxa3_2783.2_PWM_2.txt (K-afal)</p> 	<p>Hoxa3_2783.2_combined_PWM_2.txt (K-afal)</p> 
<p>Hoxa3_2783.2_PWM_3.txt (K-afal)</p> 	<p>Hoxa3_2783.2_PWM_3.txt (K-afal)</p> 	<p>Hoxa3_2783.2_combined_PWM_3.txt (K-afal)</p> <p>[Z-Beta = 34.0325] [Pearson Full 0.838253637, Pearson Zeroed 0.834827]</p> 
<p>Hoxa3_2783.2_PWM_4.txt (K-afal)</p> 	<p>Hoxa3_2783.2_PWM_4.txt (K-afal)</p> 	<p>Hoxa3_2783.2_combined_PWM_4.txt (K-afal)</p> <p>[Z-Beta = 155.292] [Pearson Full 0.838253637, Pearson Zeroed 0.821685]</p> 
<p>Hoxa3_2783.2_PWM_5.txt (K-afal)</p> 	<p>Hoxa3_2783.2_PWM_5.txt (K-afal)</p> 	<p>Hoxa3_2783.2_combined_PWM_5.txt (K-afal)</p> 
<p>Hoxa3_2783.2_PWM_6.txt (Kafal)</p> <p>No PWM Available</p>	<p>Hoxa3_2783.2_PWM_6.txt (Kafal)</p> <p>[Z-Beta = 70.708] [Pearson Full 0.850148494, Pearson Zeroed 0.841855]</p>	<p>Hoxa3_2783.2_combined_PWM_6.txt (Kafal)</p> <p>No PWM Available</p>

		
<b>Hoxa3_2783.2_PWM_7.txt (Kafal)</b> No PWM Available	<b>Hoxa3_2783.2_PWM_7.txt (Kafal)</b> 	<b>Hoxa3_2783.2_combined_PWM_7.txt (Kafal)</b> No PWM Available
<b>Hoxa3_2783.2_PWM_8.txt (Kafal)</b> No PWM Available	<b>Hoxa3_2783.2_PWM_8.txt (Kafal)</b> <b>[Z-Beta = 46.1495]</b> <b>[Pearson Full 0.850148494, Pearson Zeroed 0.847655]</b> 	<b>Hoxa3_2783.2_combined_PWM_8.txt (Kafal)</b> No PWM Available
<b>Hoxa3_2783.2_PWM_9.txt (Kafal)</b> No PWM Available	<b>Hoxa3_2783.2_PWM_9.txt (Kafal)</b> <b>[Z-Beta = 21.7481]</b> <b>[Pearson Full 0.850148494, Pearson Zeroed 0.84841]</b> 	<b>Hoxa3_2783.2_combined_PWM_9.txt (Kafal)</b> No PWM Available
<b>Hoxa3_2783.2_PWM_10.txt (Kafal)</b> No PWM Available	<b>Hoxa3_2783.2_PWM_10.txt (Kafal)</b> 	<b>Hoxa3_2783.2_combined_PWM_10.txt (Kafal)</b> No PWM Available
<b>Hoxa3_2783.2_PWM_11.txt (Kafal)</b> No PWM Available	<b>Hoxa3_2783.2_PWM_11.txt (Kafal)</b> 	<b>Hoxa3_2783.2_combined_PWM_11.txt (Kafal)</b> No PWM Available
<b>Hoxa3_2783.2_PWM_12.txt (Kafal)</b> No PWM Available	<b>Hoxa3_2783.2_PWM_12.txt (Kafal)</b> 	<b>Hoxa3_2783.2_combined_PWM_12.txt (Kafal)</b> No PWM Available

# IRC900814\_3520.1


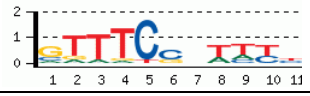
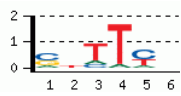

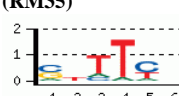
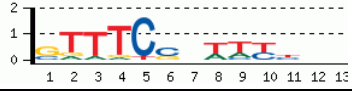
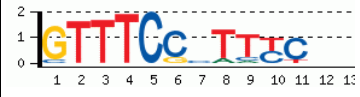
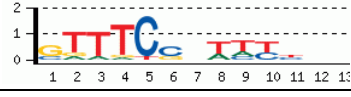
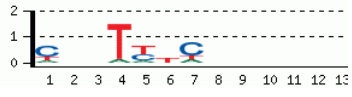
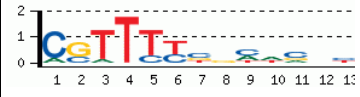
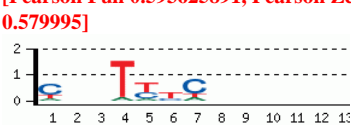




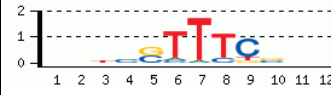

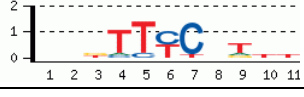
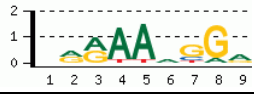

Array Version 1	Array Version 2	Combined
<p>IRC900814_3520.1_v1_prim_auc.txt (SW)</p>	<p>IRC900814_3520.1_v2_prim_auc.txt (SW)</p>	<p>IRC900814_3520.1_combined_prim_auc.txt (SW)</p>
<p>IRC900814_3520.1_v1_prim_ic.txt (SW)</p>	<p>IRC900814_3520.1_v2_prim_ic.txt (SW)</p>	<p>IRC900814_3520.1_combined_prim_ic_trimmed.t</p>
<p>IRC900814_3520.1_v1_prim_untrimmed.txt (SW)</p>	<p>IRC900814_3520.1_v2_prim_untrimmed.txt (SW)  [Z-Beta = 9.66441]  [Pearson Full 0.604943143, Pearson Zeroed 0.600786]</p>	<p>IRC900814_3520.1_combined_prim_untrimmed.t</p>
<p>IRC900814_3520.1_v1_sec_auc.txt (SW)</p>	<p>IRC900814_3520.1_v2_sec_auc.txt (SW)</p>	<p>IRC900814_3520.1_combined_sec_auc.txt (SW)</p>
<p>IRC900814_3520.1_v1_sec_ic.txt (SW)</p>	<p>IRC900814_3520.1_v2_sec_ic.txt (SW)</p>	<p>IRC900814_3520.1_combined_sec_ic_trimmed.txt</p>
<p>IRC900814_3520.1_v1_sec_untrimmed.txt (SW)</p>	<p>IRC900814_3520.1_v2_sec_untrimmed.txt (SW)  [Z-Beta = 22.4719]  [Pearson Full 0.604943143, Pearson Zeroed 0.593099]</p>	<p>IRC900814_3520.1_combined_sec_untrimmed.txt</p>
<p>IRC900814_3520.1_v1_prim_trimmed.txt (RM8)</p>	<p>IRC900814_3520.1_v2_prim_trimmed.txt (RM8)</p>	<p>IRC900814_3520.1_RM8_combined_prim_trimm (RM8)  No PWM Available</p>
<p>IRC900814_3520.1_v1_sec_trimmed.txt (RM8)  [Z-Beta = 56.0515]  [Pearson Full 0.689464204, Pearson Zeroed 0.659944]</p>	<p>IRC900814_3520.1_v2_sec_trimmed.txt (RM8)  [Z-Beta = 12.1857]  [Pearson Full 0.604943143, Pearson Zeroed 0.588659]</p>	<p>IRC900814_3520.1_RM8_combined_sec_trimmed (RM8)  No PWM Available</p>
<p>IRC900814_3520.1_v1_prim_untrimmed.txt (RM8)  [Z-Beta = 8.09798]  [Pearson Full 0.689464204, Pearson Zeroed 0.685904]</p>	<p>IRC900814_3520.1_v2_prim_untrimmed.txt (RM8)  [Z-Beta = 9.66441]  [Pearson Full 0.604943143, Pearson Zeroed 0.600786]</p>	<p>IRC900814_3520.1_RM8_combined_prim_untrin (RM8)  No PWM Available</p>
<p>IRC900814_3520.1_v1_sec_untrimmed.txt (RM8)  [Z-Beta = 123.482]  [Pearson Full 0.689464204, Pearson Zeroed 0.603349]</p>	<p>IRC900814_3520.1_v2_sec_untrimmed.txt (RM8)  [Z-Beta = 22.4719]  [Pearson Full 0.604943143, Pearson Zeroed 0.593099]</p>	<p>IRC900814_3520.1_RM8_combined_sec_untrimm (RM8)  No PWM Available</p>

		
<p>IRC900814_3520.1_v1_RM35_prim_trimmed.txt (RM35)</p> 	<p>IRC900814_3520.1_v2_RM35_prim_trimmed.txt (RM35)</p> 	<p>IRC900814_3520.1_RM_combined_prim_trimmed (RM35)  [Z-Beta = 20.3949]  [Pearson Full 0.456316068, Pearson Zeroed 0.4256]</p> 
<p>IRC900814_3520.1_v1_RM35_sec_trimmed.txt (RM35)  [Z-Beta = 2.64825]  [Pearson Full 0.689464204, Pearson Zeroed 0.688134]</p> 	<p>IRC900814_3520.1_v2_RM35_sec_trimmed.txt (RM35)  [Z-Beta = 51.5219]  [Pearson Full 0.604943143, Pearson Zeroed 0.572005]</p> 	<p>IRC900814_3520.1_RM_combined_sec_trimmed.txt (RM35)  [Z-Beta = 49.2591]  [Pearson Full 0.456316068, Pearson Zeroed 0.3934]</p> 
<p>IRC900814_3520.1_v1_RM35_prim_untrimmed.txt (RM35)</p> 	<p>IRC900814_3520.1_v2_RM35_prim_untrimmed.txt (RM35)</p> 	<p>IRC900814_3520.1_RM_combined_prim_untrimmed (RM35)  [Z-Beta = 1.74863]  [Pearson Full 0.456316068, Pearson Zeroed 0.4552]</p> 
<p>IRC900814_3520.1_v1_RM35_sec_untrimmed.txt (RM35)</p> 	<p>IRC900814_3520.1_v2_RM35_sec_untrimmed.txt (RM35)  [Z-Beta = 13.1353]  [Pearson Full 0.604943143, Pearson Zeroed 0.594645]</p> 	<p>IRC900814_3520.1_RM_combined_sec_untrimmed (RM35)  [Z-Beta = 0.95728]  [Pearson Full 0.456316068, Pearson Zeroed 0.4549]</p> 
<p>IRC900814_3520.1_PWM_10.txt (K-afal)</p> 	<p>IRC900814_3520.1_PWM_10.txt (K-afal)  No PWM Available</p>	<p>IRC900814_3520.1_combined_PWM_10.txt (K-afal)  No PWM Available</p>
<p>IRC900814_3520.1_PWM_11.txt (K-afal)</p> 	<p>IRC900814_3520.1_PWM_11.txt (K-afal)  No PWM Available</p>	<p>IRC900814_3520.1_combined_PWM_11.txt (K-afal)  No PWM Available</p>
<p>IRC900814_3520.1_PWM_12.txt (K-afal)</p> 	<p>IRC900814_3520.1_PWM_12.txt (K-afal)  No PWM Available</p>	<p>IRC900814_3520.1_combined_PWM_12.txt (K-afal)  No PWM Available</p>
<p>IRC900814_3520.1_PWM_1.txt (K-afal)</p> 	<p>IRC900814_3520.1_PWM_1.txt (K-afal)  [Z-Beta = 11.3098]  [Pearson Full 0.604943143, Pearson Zeroed 0.591807]</p> 	<p>IRC900814_3520.1_combined_PWM_1.txt (K-afal)</p> 
<p>IRC900814_3520.1_PWM_2.txt (K-afal)  [Z-Beta = 10.6057]  [Pearson Full 0.689464204, Pearson Zeroed 0.680668]</p> 	<p>IRC900814_3520.1_PWM_2.txt (K-afal)</p> 	<p>IRC900814_3520.1_combined_PWM_2.txt (K-afal)</p> 

<p>IRC900814_3520.1_PWM_3.txt (K-afal)</p> 	<p>IRC900814_3520.1_PWM_3.txt (K-afal)</p> 	<p>IRC900814_3520.1_combined_PWM_3.txt (K-afal)</p> 
<p>IRC900814_3520.1_PWM_4.txt (K-afal)  [Z-Beta = 10.2354]  [Pearson Full 0.689464204, Pearson Zeroed 0.682525]</p> 	<p>IRC900814_3520.1_PWM_4.txt (K-afal)</p> 	<p>IRC900814_3520.1_combined_PWM_4.txt (K-afal)</p> 
<p>IRC900814_3520.1_PWM_5.txt (K-afal)</p> 	<p>IRC900814_3520.1_PWM_5.txt (K-afal)</p> 	<p>IRC900814_3520.1_combined_PWM_5.txt (K-afal)</p> 
<p>IRC900814_3520.1_PWM_6.txt (K-afal)</p> 	<p>IRC900814_3520.1_PWM_6.txt (K-afal)  [Z-Beta = 39.4871]  [Pearson Full 0.604943143, Pearson Zeroed 0.577231]</p> 	<p>IRC900814_3520.1_combined_PWM_6.txt (K-afal)</p> 
<p>IRC900814_3520.1_PWM_7.txt (K-afal)</p> 	<p>IRC900814_3520.1_PWM_7.txt (K-afal)  No PWM Available</p>	<p>IRC900814_3520.1_combined_PWM_7.txt (K-afal)</p> 
<p>IRC900814_3520.1_PWM_8.txt (K-afal)</p> 	<p>IRC900814_3520.1_PWM_8.txt (K-afal)  No PWM Available</p>	<p>IRC900814_3520.1_combined_PWM_8.txt (K-afal)  [Z-Beta = 32.2871]  [Pearson Full 0.456316068, Pearson Zeroed 0.4148]</p> 
<p>IRC900814_3520.1_PWM_9.txt (K-afal)</p> 	<p>IRC900814_3520.1_PWM_9.txt (K-afal)  No PWM Available</p>	<p>IRC900814_3520.1_combined_PWM_9.txt (K-afal)  No PWM Available</p>

# Irf3\_3985.1

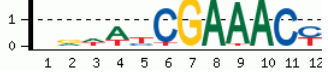
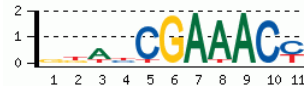


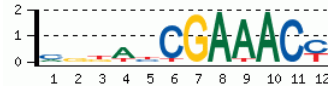







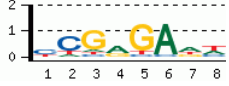







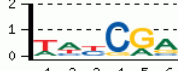

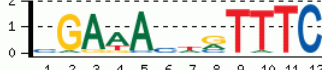
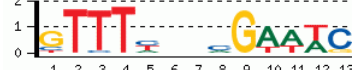
Array Version 1	Array Version 2	Combined
<p>Irf3_3985.1_v1_prim_auc.txt (SW)</p>	<p>Irf3_3985.1_v2_prim_auc.txt (SW)</p>	<p>Irf3_3985.1_combined_prim_auc.txt (SW)</p>
<p>Irf3_3985.1_v1_prim_ic.txt (SW)</p>	<p>Irf3_3985.1_v2_prim_ic.txt (SW)</p>	<p>Irf3_3985.1_combined_prim_ic_trimmed.txt (SW)</p>
<p>Irf3_3985.1_v1_prim_untrimmed.txt (SW)  [Z-Beta = 111.486]  [Pearson Full 0.594299718, Pearson Zeroed 0.566648]</p>	<p>Irf3_3985.1_v2_prim_untrimmed.txt (SW)</p>	<p>Irf3_3985.1_combined_prim_untrimmed.txt (SW)  [Z-Beta = 87.7896]  [Pearson Full 0.593625891, Pearson Zeroed 0.551601]</p>
<p>Irf3_3985.1_v1_sec_auc.txt (SW)</p>	<p>Irf3_3985.1_v2_sec_auc.txt (SW)  [Z-Beta = 6.94265]  [Pearson Full 0.537239378, Pearson Zeroed 0.529266]</p>	<p>Irf3_3985.1_combined_sec_auc.txt (SW)</p>
<p>Irf3_3985.1_v1_sec_ic.txt (SW)</p>	<p>Irf3_3985.1_v2_sec_ic.txt (SW)</p>	<p>Irf3_3985.1_combined_sec_ic_trimmed.txt (SW)</p>
<p>Irf3_3985.1_v1_sec_untrimmed.txt (SW)</p>	<p>Irf3_3985.1_v2_sec_untrimmed.txt (SW)</p>	<p>Irf3_3985.1_combined_sec_untrimmed.txt (SW)</p>
<p>Irf3_3985.1_v1_prim_trimmed.txt (RM8)</p>	<p>Irf3_3985.1_v2_prim_trimmed.txt (RM8)  [Z-Beta = 49.0199]  [Pearson Full 0.537239378, Pearson Zeroed 0.494146]</p>	<p>Irf3_3985.1_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Irf3_3985.1_v1_sec_trimmed.txt (RM8)</p>	<p>Irf3_3985.1_v2_sec_trimmed.txt (RM8)</p>	<p>Irf3_3985.1_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Irf3_3985.1_v1_prim_untrimmed.txt (RM8)</p>	<p>Irf3_3985.1_v2_prim_untrimmed.txt (RM8)</p>	<p>Irf3_3985.1_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Irf3_3985.1_v1_sec_untrimmed.txt (RM8)</p>	<p>Irf3_3985.1_v2_sec_untrimmed.txt (RM8)</p>	<p>Irf3_3985.1_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>
<p>Irf3_3985.1_v1_RM35_prim_trimmed.txt (RM35)</p>	<p>Irf3_3985.1_v2_RM35_prim_trimmed.txt (RM35)</p>	<p>Irf3_3985.1_RM_combined_prim_trimmed.txt (RM35)</p>

		
Irf3_3985.1_v1_RM35_sec_trimmed.txt (RM35) 	Irf3_3985.1_v2_RM35_sec_trimmed.txt (RM35) 	Irf3_3985.1_RM_combined_sec_trimmed.txt (RM35) 
Irf3_3985.1_v1_RM35_prim_untrimmed.txt (RM35) [Z-Beta = 138.703] [Pearson Full 0.594299718, Pearson Zeroed 0.544811] 	Irf3_3985.1_v2_RM35_prim_untrimmed.txt (RM35) 	Irf3_3985.1_RM_combined_prim_untrimmed.txt (RM35) [Z-Beta = 101.976] [Pearson Full 0.593625891, Pearson Zeroed 0.542892] 
Irf3_3985.1_v1_RM35_sec_untrimmed.txt (RM35) 	Irf3_3985.1_v2_RM35_sec_untrimmed.txt (RM35) 	Irf3_3985.1_RM_combined_sec_untrimmed.txt (RM35) [Z-Beta = 12.6335] [Pearson Full 0.593625891, Pearson Zeroed 0.579995] 
Irf3_3985.1_PWM_1.txt (K-afal) 	Irf3_3985.1_PWM_1.txt (K-afal) [Z-Beta = 95.5522] [Pearson Full 0.537239378, Pearson Zeroed 0.483402] 	Irf3_3985.1_combined_PWM_1.txt (K-afal) [Z-Beta = 9.66089] [Pearson Full 0.593625891, Pearson Zeroed 0.583591] 
Irf3_3985.1_PWM_2.txt (K-afal) [Z-Beta = 35.3507] [Pearson Full 0.594299718, Pearson Zeroed 0.560418] 	Irf3_3985.1_PWM_2.txt (K-afal) 	Irf3_3985.1_combined_PWM_2.txt (K-afal) No PWM Available
Irf3_3985.1_PWM_3.txt (K-afal) 	Irf3_3985.1_PWM_3.txt (K-afal) No PWM Available	Irf3_3985.1_combined_PWM_3.txt (K-afal) No PWM Available
Irf3_3985.1_PWM_4.txt (K-afal) 	Irf3_3985.1_PWM_4.txt (K-afal) No PWM Available	Irf3_3985.1_combined_PWM_4.txt (K-afal) No PWM Available
Irf3_3985.1_PWM_5.txt (K-afal) [Z-Beta = 2.1733] [Pearson Full 0.594299718, Pearson Zeroed 0.592588] 	Irf3_3985.1_PWM_5.txt (K-afal) No PWM Available	Irf3_3985.1_combined_PWM_5.txt (K-afal) No PWM Available
Irf3_3985.1_PWM_6.txt (K-afal) 	Irf3_3985.1_PWM_6.txt (K-afal) No PWM Available	Irf3_3985.1_combined_PWM_6.txt (K-afal) No PWM Available
Irf3_3985.1_PWM_7.txt (K-afal)	Irf3_3985.1_PWM_7.txt (K-afal) No PWM Available	Irf3_3985.1_combined_PWM_7.txt (K-afal) No PWM Available





# Irf4\_3476.1

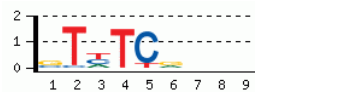
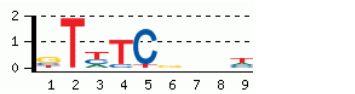
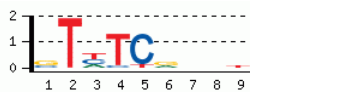



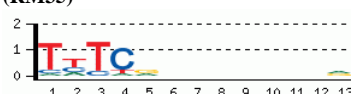
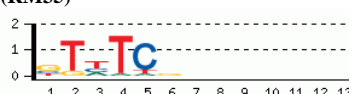
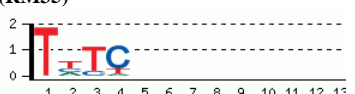



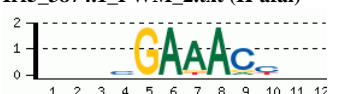
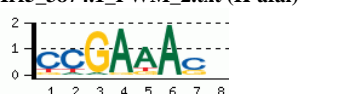


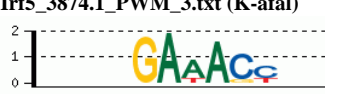


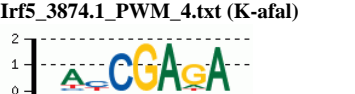

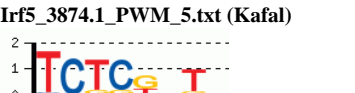



Array Version 1	Array Version 2	Combined
<p>Irf4_3476.1_v1_prim_auc.txt (SW)</p> 	<p>Irf4_3476.1_v2_prim_auc.txt (SW)  [Z-Beta = 6.98484]  [Pearson Full 0.765378219, Pearson Zeroed 0.764659]</p> 	<p>Irf4_3476.1_combined_prim_auc.txt (SW)</p> 
<p>Irf4_3476.1_v1_prim_ic.txt (SW)</p> 	<p>Irf4_3476.1_v2_prim_ic.txt (SW)</p> 	<p>Irf4_3476.1_combined_prim_ic_trimmed.txt (SW)  [Z-Beta = 34.9608]  [Pearson Full 0.824796969, Pearson Zeroed 0.823628]</p> 
<p>Irf4_3476.1_v1_prim_untrimmed.txt (SW)  [Z-Beta = 18.7403]  [Pearson Full 0.746462434, Pearson Zeroed 0.740933]</p> 	<p>Irf4_3476.1_v2_prim_untrimmed.txt (SW)</p> 	<p>Irf4_3476.1_combined_prim_untrimmed.txt (SW)  [Z-Beta = -328.367]  [Pearson Full 0.824796969, Pearson Zeroed 0.786819]</p> 
<p>Irf4_3476.1_v1_sec_auc.txt (SW)</p> 	<p>Irf4_3476.1_v2_sec_auc.txt (SW)</p> 	<p>Irf4_3476.1_combined_sec_auc.txt (SW)</p> 
<p>Irf4_3476.1_v1_sec_ic.txt (SW)</p> 	<p>Irf4_3476.1_v2_sec_ic.txt (SW)</p> 	<p>Irf4_3476.1_combined_sec_ic_trimmed.txt (SW)  [Z-Beta = 49.2958]  [Pearson Full 0.824796969, Pearson Zeroed 0.817834]</p> 
<p>Irf4_3476.1_v1_sec_untrimmed.txt (SW)</p> 	<p>Irf4_3476.1_v2_sec_untrimmed.txt (SW)</p> 	<p>Irf4_3476.1_combined_sec_untrimmed.txt (SW)  [Z-Beta = 3.58614]  [Pearson Full 0.824796969, Pearson Zeroed 0.824761]</p> 
<p>Irf4_3476.1_v1_prim_trimmed.txt (RM8)</p> 	<p>Irf4_3476.1_v2_prim_trimmed.txt (RM8)</p> 	<p>Irf4_3476.1_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Irf4_3476.1_v1_sec_trimmed.txt (RM8)</p> 	<p>Irf4_3476.1_v2_sec_trimmed.txt (RM8)</p> 	<p>Irf4_3476.1_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Irf4_3476.1_v1_prim_untrimmed.txt (RM8)</p> 	<p>Irf4_3476.1_v2_prim_untrimmed.txt (RM8)</p> 	<p>Irf4_3476.1_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Irf4_3476.1_v1_sec_untrimmed.txt (RM8)</p>	<p>Irf4_3476.1_v2_sec_untrimmed.txt (RM8)</p>	<p>Irf4_3476.1_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>

		
<b>Irf4_3476.1_v1_RM35_prim_trimmed.txt (RM35)</b> 	<b>Irf4_3476.1_v2_RM35_prim_trimmed.txt (RM35)</b> 	<b>Irf4_3476.1_RM_combined_prim_trimmed.txt (RM35)</b> <b>[Z-Beta = 19.1761]</b> <b>[Pearson Full 0.824796969, Pearson Zeroed 0.824708]</b> 
<b>Irf4_3476.1_v1_RM35_sec_trimmed.txt (RM35)</b> 	<b>Irf4_3476.1_v2_RM35_sec_trimmed.txt (RM35)</b> <b>[Z-Beta = 33.2699]</b> <b>[Pearson Full 0.765378219, Pearson Zeroed 0.753466]</b> 	<b>Irf4_3476.1_RM_combined_sec_trimmed.txt (RM35)</b> <b>[Z-Beta = -13.5954]</b> <b>[Pearson Full 0.824796969, Pearson Zeroed 0.824512]</b> 
<b>Irf4_3476.1_v1_RM35_prim_untrimmed.txt (RM35)</b> <b>[Z-Beta = 274.775]</b> <b>[Pearson Full 0.746462434, Pearson Zeroed 0.678041]</b> 	<b>Irf4_3476.1_v2_RM35_prim_untrimmed.txt (RM35)</b> <b>[Z-Beta = 180.744]</b> <b>[Pearson Full 0.765378219, Pearson Zeroed 0.757673]</b> 	<b>Irf4_3476.1_RM_combined_prim_untrimmed.txt (RM35)</b> <b>[Z-Beta = 191.519]</b> <b>[Pearson Full 0.824796969, Pearson Zeroed 0.806184]</b> 
<b>Irf4_3476.1_v1_RM35_sec_untrimmed.txt (RM35)</b> 	<b>Irf4_3476.1_v2_RM35_sec_untrimmed.txt (RM35)</b> 	<b>Irf4_3476.1_RM_combined_sec_untrimmed.txt (RM35)</b> <b>[Z-Beta = 78.5277]</b> <b>[Pearson Full 0.824796969, Pearson Zeroed 0.810434]</b> 
<b>Irf4_3476.1_PWM_1.txt (K-afal)</b> <b>[Z-Beta = 3.14845]</b> <b>[Pearson Full 0.746462434, Pearson Zeroed 0.746078]</b> 	<b>Irf4_3476.1_PWM_1.txt (K-afal)</b> 	<b>Irf4_3476.1_combined_PWM_1.txt (K-afal)</b> <b>[Z-Beta = -125.272]</b> <b>[Pearson Full 0.824796969, Pearson Zeroed 0.815929]</b> 
<b>Irf4_3476.1_PWM_2.txt (K-afal)</b> 	<b>Irf4_3476.1_PWM_2.txt (K-afal)</b> <b>[Z-Beta = 101.944]</b> <b>[Pearson Full 0.765378219, Pearson Zeroed 0.757791]</b> 	<b>Irf4_3476.1_combined_PWM_2.txt (K-afal)</b> <b>[Z-Beta = -31.894]</b> <b>[Pearson Full 0.824796969, Pearson Zeroed 0.824151]</b> 
<b>Irf4_3476.1_PWM_3.txt (K-afal)</b> 	<b>Irf4_3476.1_PWM_3.txt (K-afal)</b> 	<b>Irf4_3476.1_combined_PWM_3.txt (K-afal)</b> <b>[Z-Beta = 324.322]</b> <b>[Pearson Full 0.824796969, Pearson Zeroed 0.688015]</b> 
<b>Irf4_3476.1_PWM_4.txt (K-afal)</b> <b>[Z-Beta = 83.9581]</b> <b>[Pearson Full 0.746462434, Pearson Zeroed 0.719385]</b>	<b>Irf4_3476.1_PWM_4.txt (K-afal)</b> No PWM Available	<b>Irf4_3476.1_combined_PWM_4.txt (K-afal)</b> No PWM Available



# Irf5\_3874.1

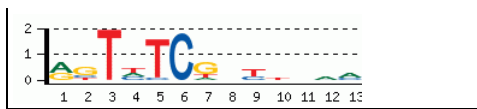
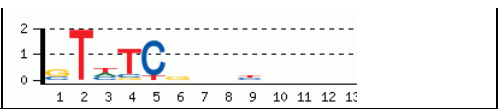
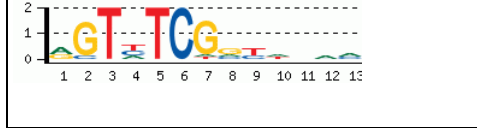
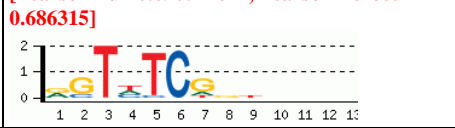
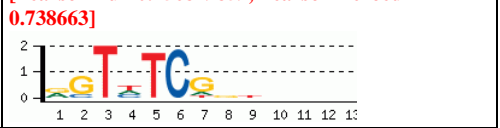
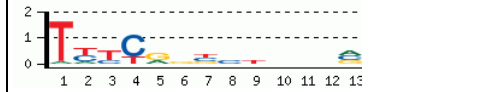
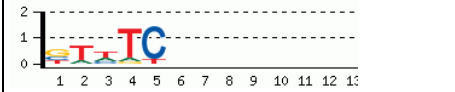
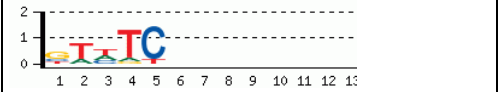



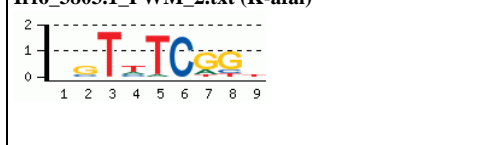


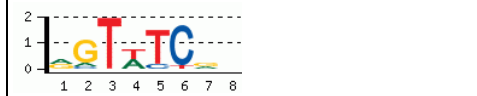
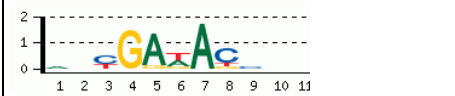


Array Version 1	Array Version 2	Combined
<b>Irf5_3874.1_v1_prim_auc.txt (SW)</b> 	<b>Irf5_3874.1_v2_prim_auc.txt (SW)</b> 	<b>Irf5_3874.1_combined_prim_auc.txt (SW)</b> 
<b>Irf5_3874.1_v1_prim_ic.txt (SW)</b> 	<b>Irf5_3874.1_v2_prim_ic.txt (SW)</b> 	<b>Irf5_3874.1_combined_prim_ic_trimmed.txt (SW)</b> 
<b>Irf5_3874.1_v1_prim_untrimmed.txt (SW)</b> <b>[Z-Beta = 127.106]</b> <b>[Pearson Full 0.711131986, Pearson Zeroed 0.626764]</b> 	<b>Irf5_3874.1_v2_prim_untrimmed.txt (SW)</b> 	<b>Irf5_3874.1_combined_prim_untrimmed.txt (SW)</b> <b>[Z-Beta = 157.822]</b> <b>[Pearson Full 0.792799991, Pearson Zeroed 0.750091]</b> 
<b>Irf5_3874.1_v1_sec_auc.txt (SW)</b> 	<b>Irf5_3874.1_v2_sec_auc.txt (SW)</b> 	<b>Irf5_3874.1_combined_sec_auc.txt (SW)</b> 
<b>Irf5_3874.1_v1_sec_ic.txt (SW)</b> 	<b>Irf5_3874.1_v2_sec_ic.txt (SW)</b> 	<b>Irf5_3874.1_combined_sec_ic_trimmed.txt (SW)</b> 
<b>Irf5_3874.1_v1_sec_untrimmed.txt (SW)</b> 	<b>Irf5_3874.1_v2_sec_untrimmed.txt (SW)</b> 	<b>Irf5_3874.1_combined_sec_untrimmed.txt (SW)</b> 
<b>Irf5_3874.1_v1_prim_trimmed.txt (RM8)</b> 	<b>Irf5_3874.1_v2_prim_trimmed.txt (RM8)</b> 	<b>Irf5_3874.1_RM8_combined_prim_trimmed.txt (RM8)</b> No PWM Available
<b>Irf5_3874.1_v1_sec_trimmed.txt (RM8)</b> 	<b>Irf5_3874.1_v2_sec_trimmed.txt (RM8)</b> 	<b>Irf5_3874.1_RM8_combined_sec_trimmed.txt (RM8)</b> No PWM Available
<b>Irf5_3874.1_v1_prim_untrimmed.txt (RM8)</b> 	<b>Irf5_3874.1_v2_prim_untrimmed.txt (RM8)</b> 	<b>Irf5_3874.1_RM8_combined_prim_untrimmed.txt (RM8)</b> No PWM Available
<b>Irf5_3874.1_v1_sec_untrimmed.txt (RM8)</b> 	<b>Irf5_3874.1_v2_sec_untrimmed.txt (RM8)</b> 	<b>Irf5_3874.1_RM8_combined_sec_untrimmed.txt (RM8)</b> No PWM Available
<b>Irf5_3874.1_v1_RM35_prim_trimmed.txt (RM35)</b> <b>[Z-Beta = 94.8112]</b> <b>[Pearson Full 0.711131986, Pearson Zeroed 0.712084]</b> 	<b>Irf5_3874.1_v2_RM35_prim_trimmed.txt (RM35)</b> <b>[Z-Beta = 103.521]</b> <b>[Pearson Full 0.764403341, Pearson Zeroed 0.76748]</b> 	<b>Irf5_3874.1_RM_combined_prim_trimmed.txt (RM35)</b> <b>[Z-Beta = 209.972]</b> <b>[Pearson Full 0.792799991, Pearson Zeroed 0.762033]</b> 

<p>Irf5_3874.1_v1_RM35_sec_trimmed.txt (RM35)</p> 	<p>Irf5_3874.1_v2_RM35_sec_trimmed.txt (RM35)</p> 	<p>Irf5_3874.1_RM_combined_sec_trimmed.txt (RM35)</p> 
<p>Irf5_3874.1_v1_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Irf5_3874.1_v2_RM35_prim_untrimmed.txt (RM35)  [Z-Beta = 94.0017]  [Pearson Full 0.764403341, Pearson Zeroed 0.767598]</p> 	<p>Irf5_3874.1_RM_combined_prim_untrimmed.txt (RM35)  [Z-Beta = 11.7547]  [Pearson Full 0.792799991, Pearson Zeroed 0.793312]</p> 
<p>Irf5_3874.1_v1_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Irf5_3874.1_v2_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Irf5_3874.1_RM_combined_sec_untrimmed.txt (RM35)</p> 
<p>Irf5_3874.1_PWM_1.txt (K-afal)  [Z-Beta = 68.2265]  [Pearson Full 0.711131986, Pearson Zeroed 0.663207]</p> 	<p>Irf5_3874.1_PWM_1.txt (K-afal)  [Z-Beta = 152.674]  [Pearson Full 0.764403341, Pearson Zeroed 0.725874]</p> 	<p>Irf5_3874.1_combined_PWM_1.txt (K-afal)</p> 
<p>Irf5_3874.1_PWM_2.txt (K-afal)</p> 	<p>Irf5_3874.1_PWM_2.txt (K-afal)</p> 	<p>Irf5_3874.1_combined_PWM_2.txt (K-afal)</p> 
<p>Irf5_3874.1_PWM_3.txt (K-afal)</p> 	<p>Irf5_3874.1_PWM_3.txt (K-afal)</p> 	<p>Irf5_3874.1_combined_PWM_3.txt (K-afal)  [Z-Beta = 71.1766]  [Pearson Full 0.792799991, Pearson Zeroed 0.774373]</p> 
<p>Irf5_3874.1_PWM_4.txt (K-afal)</p> 	<p>Irf5_3874.1_PWM_4.txt (K-afal)</p> 	<p>Irf5_3874.1_combined_PWM_4.txt (K-afal)</p> 
<p>Irf5_3874.1_PWM_5.txt (Kafal)  No PWM Available</p>	<p>Irf5_3874.1_PWM_5.txt (Kafal)</p> 	<p>Irf5_3874.1_combined_PWM_5.txt (Kafal)</p> 
<p>Irf5_3874.1_PWM_6.txt (Kafal)  No PWM Available</p>	<p>Irf5_3874.1_PWM_6.txt (Kafal)</p> 	<p>Irf5_3874.1_combined_PWM_6.txt (Kafal)  No PWM Available</p>
<p>Irf5_3874.1_PWM_7.txt (Kafal)  No PWM Available</p>	<p>Irf5_3874.1_PWM_7.txt (Kafal)</p> 	<p>Irf5_3874.1_combined_PWM_7.txt (Kafal)  No PWM Available</p>

# Irf6\_3803.1

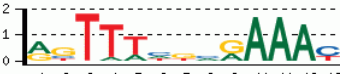
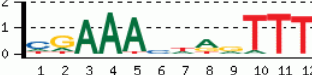

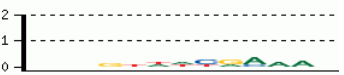


















Array Version 1	Array Version 2	Combined
<b>Irf6_3803.1_v1_prim_auc.txt (SW)</b> 	<b>Irf6_3803.1_v2_prim_auc.txt (SW)</b> 	<b>Irf6_3803.1_combined_prim_auc.txt (SW)</b> 
<b>Irf6_3803.1_v1_prim_ic.txt (SW)</b> <b>[Z-Beta = 38.4024]</b> <b>[Pearson Full 0.749222444, Pearson Zeroed 0.733788]</b> 	<b>Irf6_3803.1_v2_prim_ic.txt (SW)</b> 	<b>Irf6_3803.1_combined_prim_ic_trimmed.txt (SW)</b> 
<b>Irf6_3803.1_v1_prim_untrimmed.txt (SW)</b> 	<b>Irf6_3803.1_v2_prim_untrimmed.txt (SW)</b> 	<b>Irf6_3803.1_combined_prim_untrimmed.txt (SW)</b> 
<b>Irf6_3803.1_v1_sec_auc.txt (SW)</b> 	<b>Irf6_3803.1_v2_sec_auc.txt (SW)</b> 	<b>Irf6_3803.1_combined_sec_auc.txt (SW)</b> 
<b>Irf6_3803.1_v1_sec_ic.txt (SW)</b> 	<b>Irf6_3803.1_v2_sec_ic.txt (SW)</b> 	<b>Irf6_3803.1_combined_sec_ic_trimmed.txt (SW)</b> 
<b>Irf6_3803.1_v1_sec_untrimmed.txt (SW)</b> 	<b>Irf6_3803.1_v2_sec_untrimmed.txt (SW)</b> 	<b>Irf6_3803.1_combined_sec_untrimmed.txt (SW)</b> 
<b>Irf6_3803.1_v1_prim_trimmed.txt (RM8)</b> 	<b>Irf6_3803.1_v2_prim_trimmed.txt (RM8)</b> 	<b>Irf6_3803.1_RM8_combined_prim_trimmed.txt (RM8)</b> No PWM Available
<b>Irf6_3803.1_v1_sec_trimmed.txt (RM8)</b> 	<b>Irf6_3803.1_v2_sec_trimmed.txt (RM8)</b> 	<b>Irf6_3803.1_RM8_combined_sec_trimmed.txt (RM8)</b> No PWM Available
<b>Irf6_3803.1_v1_prim_untrimmed.txt (RM8)</b> 	<b>Irf6_3803.1_v2_prim_untrimmed.txt (RM8)</b> 	<b>Irf6_3803.1_RM8_combined_prim_untrimmed.txt (RM8)</b> No PWM Available
<b>Irf6_3803.1_v1_sec_untrimmed.txt (RM8)</b> 	<b>Irf6_3803.1_v2_sec_untrimmed.txt (RM8)</b> 	<b>Irf6_3803.1_RM8_combined_sec_untrimmed.txt (RM8)</b> No PWM Available
<b>Irf6_3803.1_v1_RM35_prim_trimmed.txt (RM35)</b> 	<b>Irf6_3803.1_v2_RM35_prim_trimmed.txt (RM35)</b> 	<b>Irf6_3803.1_RM_combined_prim_trimmed.txt (RM35)</b> 
<b>Irf6_3803.1_v1_RM35_sec_trimmed.txt (RM35)</b> <b>[Z-Beta = 64.2582]</b> <b>[Pearson Full 0.749222444, Pearson Zeroed 0.731863]</b> 	<b>Irf6_3803.1_v2_RM35_sec_trimmed.txt (RM35)</b> 	<b>Irf6_3803.1_RM_combined_sec_trimmed.txt (RM35)</b> <b>[Z-Beta = 14.4422]</b> <b>[Pearson Full 0.756849859, Pearson Zeroed 0.749921]</b> 






		
<p><b>Irf6_3803.1_v1_RM35_prim_untrimmed.txt (RM35)</b></p> 	<p><b>Irf6_3803.1_v2_RM35_prim_untrimmed.txt (RM35)</b>  [Z-Beta = 22.0997]  [Pearson Full 0.690574612, Pearson Zeroed 0.686315]</p> 	<p><b>Irf6_3803.1_RM_combined_prim_untrimmed.txt (RM35)</b>  [Z-Beta = 72.813]  [Pearson Full 0.756849859, Pearson Zeroed 0.738663]</p> 
<p><b>Irf6_3803.1_v1_RM35_sec_untrimmed.txt (RM35)</b></p> 	<p><b>Irf6_3803.1_v2_RM35_sec_untrimmed.txt (RM35)</b></p> 	<p><b>Irf6_3803.1_RM_combined_sec_untrimmed.txt (RM35)</b></p> 
<p><b>Irf6_3803.1_PWM_1.txt (K-afal)</b>  [Z-Beta = 137.01]  [Pearson Full 0.749222444, Pearson Zeroed 0.729725]</p> 	<p><b>Irf6_3803.1_PWM_1.txt (K-afal)</b></p> 	<p><b>Irf6_3803.1_combined_PWM_1.txt (K-afal)</b></p> 
<p><b>Irf6_3803.1_PWM_2.txt (K-afal)</b></p> 	<p><b>Irf6_3803.1_PWM_2.txt (K-afal)</b>  [Z-Beta = 220.674]  [Pearson Full 0.690574612, Pearson Zeroed 0.665494]</p> 	<p><b>Irf6_3803.1_combined_PWM_2.txt (K-afal)</b></p> 
<p><b>Irf6_3803.1_PWM_3.txt (K-afal)</b>  [Z-Beta = 65.1333]  [Pearson Full 0.749222444, Pearson Zeroed 0.741561]</p> 	<p><b>Irf6_3803.1_PWM_3.txt (K-afal)</b>  [Z-Beta = 22.2707]  [Pearson Full 0.690574612, Pearson Zeroed 0.687514]</p> 	<p><b>Irf6_3803.1_combined_PWM_3.txt (K-afal)</b>  [Z-Beta = 232.13]  [Pearson Full 0.756849859, Pearson Zeroed 0.665399]</p> 
<p><b>Irf6_3803.1_PWM_4.txt (K-afal)</b>  [Z-Beta = 84.1339]  [Pearson Full 0.749222444, Pearson Zeroed 0.740525]</p> 	<p><b>Irf6_3803.1_PWM_4.txt (K-afal)</b>  No PWM Available</p>	<p><b>Irf6_3803.1_combined_PWM_4.txt (K-afal)</b>  No PWM Available</p>

# Isgf3g\_2853.2

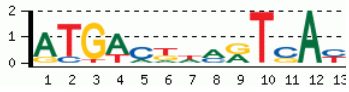
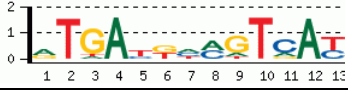
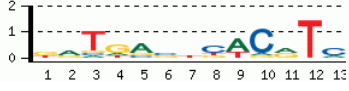
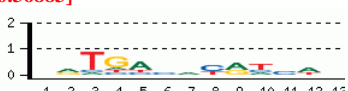
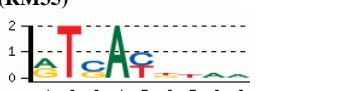





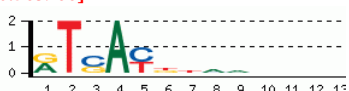





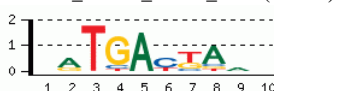




Array Version 1	Array Version 2	Combined
<p>Isgf3g_2853.2_v1_prim_auc.txt (SW)  [Z-Beta = -184.862]  [Pearson Full 0.551866125, Pearson Zeroed 0.439669]</p>	<p>Isgf3g_2853.2_v2_prim_auc.txt (SW)</p>	<p>Isgf3g_2853.2_combined_prim_auc.txt (SW)</p>
<p>Isgf3g_2853.2_v1_prim_ic.txt (SW)  [Z-Beta = -11.4236]  [Pearson Full 0.551866125, Pearson Zeroed 0.550549]</p>	<p>Isgf3g_2853.2_v2_prim_ic.txt (SW)</p>	<p>Isgf3g_2853.2_combined_prim_ic_trimmed.txt (SW)</p>
<p>Isgf3g_2853.2_v1_prim_untrimmed.txt (SW)  [Z-Beta = 204.775]  [Pearson Full 0.551866125, Pearson Zeroed 0.06448]</p>	<p>Isgf3g_2853.2_v2_prim_untrimmed.txt (SW)</p>	<p>Isgf3g_2853.2_combined_prim_untrimmed.txt (SW)</p>
<p>Isgf3g_2853.2_v1_sec_auc.txt (SW)  [Z-Beta = 52.2713]  [Pearson Full 0.551866125, Pearson Zeroed 0.506873]</p>	<p>Isgf3g_2853.2_v2_sec_auc.txt (SW)</p>	<p>Isgf3g_2853.2_combined_sec_auc.txt (SW)</p>
<p>Isgf3g_2853.2_v1_sec_ic.txt (SW)  [Z-Beta = -19.1362]  [Pearson Full 0.551866125, Pearson Zeroed 0.546149]</p>	<p>Isgf3g_2853.2_v2_sec_ic.txt (SW)</p>	<p>Isgf3g_2853.2_combined_sec_ic_trimmed.txt (SW)  [Z-Beta = 31.3372]  [Pearson Full 0.567506439, Pearson Zeroed 0.445826]</p>
<p>Isgf3g_2853.2_v1_sec_untrimmed.txt (SW)  [Z-Beta = -3.50522]  [Pearson Full 0.551866125, Pearson Zeroed 0.551684]</p>	<p>Isgf3g_2853.2_v2_sec_untrimmed.txt (SW)  [Z-Beta = 76.721]  [Pearson Full 0.507221412, Pearson Zeroed 0.403916]</p>	<p>Isgf3g_2853.2_combined_sec_untrimmed.txt (SW)</p>
<p>Isgf3g_2853.2_v1_prim_trimmed.txt (RM8)  [Z-Beta = 19.5159]  [Pearson Full 0.551866125, Pearson Zeroed 0.545875]</p>	<p>Isgf3g_2853.2_v2_prim_trimmed.txt (RM8)</p>	<p>Isgf3g_2853.2_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Isgf3g_2853.2_v1_sec_trimmed.txt (RM8)  [Z-Beta = -24.4633]  [Pearson Full 0.551866125, Pearson Zeroed 0.544769]</p>	<p>Isgf3g_2853.2_v2_sec_trimmed.txt (RM8)</p>	<p>Isgf3g_2853.2_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>




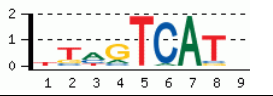

<p>Isgf3g_2853.2_v1_prim_untrimmed.txt (RM8)  [Z-Beta = 30.0198]  [Pearson Full 0.551866125, Pearson Zeroed 0.541252]</p> 	<p>Isgf3g_2853.2_v2_prim_untrimmed.txt (RM8)</p> 	<p>Isgf3g_2853.2_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Isgf3g_2853.2_v1_sec_untrimmed.txt (RM8)  [Z-Beta = 55.4762]  [Pearson Full 0.551866125, Pearson Zeroed 0.513452]</p> 	<p>Isgf3g_2853.2_v2_sec_untrimmed.txt (RM8)  [Z-Beta = 76.721]  [Pearson Full 0.507221412, Pearson Zeroed 0.403916]</p> 	<p>Isgf3g_2853.2_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>
<p>Isgf3g_2853.2_v1_RM35_prim_trimmed.txt (RM35)  [Z-Beta = -30.5598]  [Pearson Full 0.551866125, Pearson Zeroed 0.544292]</p> 	<p>Isgf3g_2853.2_v2_RM35_prim_trimmed.txt (RM35)</p> 	<p>Isgf3g_2853.2_RM_combined_prim_trimmed.txt (RM35)  [Z-Beta = 43.7431]  [Pearson Full 0.567506439, Pearson Zeroed 0.530688]</p> 
<p>Isgf3g_2853.2_v1_RM35_sec_trimmed.txt (RM35)  [Z-Beta = 29.8099]  [Pearson Full 0.551866125, Pearson Zeroed 0.540262]</p> 	<p>Isgf3g_2853.2_v2_RM35_sec_trimmed.txt (RM35)</p> 	<p>Isgf3g_2853.2_RM_combined_sec_trimmed.txt (RM35)</p> 
<p>Isgf3g_2853.2_v1_RM35_prim_untrimmed.txt (RM35)  [Z-Beta = 37.7105]  [Pearson Full 0.551866125, Pearson Zeroed 0.536293]</p> 	<p>Isgf3g_2853.2_v2_RM35_prim_untrimmed.txt (RM35)  [Z-Beta = 8.20043]  [Pearson Full 0.507221412, Pearson Zeroed 0.503118]</p> 	<p>Isgf3g_2853.2_RM_combined_prim_untrimmed.txt (RM35)</p> 
<p>Isgf3g_2853.2_v1_RM35_sec_untrimmed.txt (RM35)  [Z-Beta = -7.20716]  [Pearson Full 0.551866125, Pearson Zeroed 0.551271]</p> 	<p>Isgf3g_2853.2_v2_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Isgf3g_2853.2_RM_combined_sec_untrimmed.txt (RM35)</p> 
<p>Isgf3g_2853.2_PWM_1.txt (K-afal)  [Z-Beta = 1.90673]  [Pearson Full 0.551866125, Pearson Zeroed 0.551836]</p> 	<p>Isgf3g_2853.2_PWM_1.txt (K-afal)</p> 	<p>Isgf3g_2853.2_combined_PWM_1.txt (K-afal)  [Z-Beta = 36.6844]  [Pearson Full 0.567506439, Pearson Zeroed 0.537098]</p> 
<p>Isgf3g_2853.2_PWM_2.txt (K-afal)  [Z-Beta = 67.9213]  [Pearson Full 0.551866125, Pearson Zeroed 0.496315]</p> 	<p>Isgf3g_2853.2_PWM_2.txt (K-afal)  [Z-Beta = 27.232]  [Pearson Full 0.507221412, Pearson Zeroed 0.477674]</p> 	<p>Isgf3g_2853.2_combined_PWM_2.txt (K-afal)</p> 

<p>Isgf3g_2853.2_PWM_3.txt (K-afal)  [Z-Beta = 42.4084]  [Pearson Full 0.551866125, Pearson Zeroed 0.526521]</p> 	<p>Isgf3g_2853.2_PWM_3.txt (K-afal)  [Z-Beta = 5.53219]  [Pearson Full 0.507221412, Pearson Zeroed 0.498932]</p> 	<p>Isgf3g_2853.2_combined_PWM_3.txt (K-afal)  No PWM Available</p>
<p>Isgf3g_2853.2_PWM_4.txt (K-afal)  [Z-Beta = 10.1329]  [Pearson Full 0.551866125, Pearson Zeroed 0.551048]</p> 	<p>Isgf3g_2853.2_PWM_4.txt (K-afal)  No PWM Available</p>	<p>Isgf3g_2853.2_combined_PWM_4.txt (K-afal)  No PWM Available</p>

# Jundm2\_0911.3













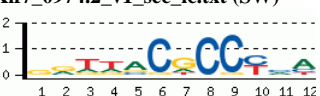
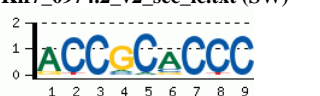

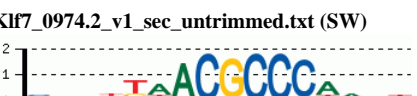
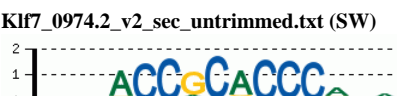







Array Version 1	Array Version 2	Combined
<p>Jundm2_0911.3_v1_prim_auc.txt (SW)</p>	<p>Jundm2_0911.3_v2_prim_auc.txt (SW)  [Z-Beta = 1945.8]  [Pearson Full 0.429073291, Pearson Zeroed 0.0659075]</p>	<p>Jundm2_0911.3_combined_prim_auc.txt (SW)</p>
<p>Jundm2_0911.3_v1_prim_ic.txt (SW)</p>	<p>Jundm2_0911.3_v2_prim_ic.txt (SW)  [Z-Beta = -1951.74]  [Pearson Full 0.429073291, Pearson Zeroed 0.110132]</p>	<p>Jundm2_0911.3_combined_prim_ic_trimmed.txt (SW)  [Z-Beta = 17.2734]  [Pearson Full 0.612672479, Pearson Zeroed 0.565274]</p>
<p>Jundm2_0911.3_v1_prim_untrimmed.txt (SW)  [Z-Beta = 62.3394]  [Pearson Full 0.613898153, Pearson Zeroed 0.568561]</p>	<p>Jundm2_0911.3_v2_prim_untrimmed.txt (SW)  [Z-Beta = 36.8804]  [Pearson Full 0.429073291, Pearson Zeroed 0.409096]</p>	<p>Jundm2_0911.3_combined_prim_untrimmed.txt (SW)</p>
<p>Jundm2_0911.3_v1_sec_auc.txt (SW)</p>	<p>Jundm2_0911.3_v2_sec_auc.txt (SW)  [Z-Beta = 36.8137]  [Pearson Full 0.429073291, Pearson Zeroed 0.400267]</p>	<p>Jundm2_0911.3_combined_sec_auc.txt (SW)</p>
<p>Jundm2_0911.3_v1_sec_ic.txt (SW)  [Z-Beta = 17.276]  [Pearson Full 0.613898153, Pearson Zeroed 0.578725]</p>	<p>Jundm2_0911.3_v2_sec_ic.txt (SW)  [Z-Beta = 5.71834]  [Pearson Full 0.429073291, Pearson Zeroed 0.428364]</p>	<p>Jundm2_0911.3_combined_sec_ic_trimmed.txt (SW)</p>
<p>Jundm2_0911.3_v1_sec_untrimmed.txt (SW)</p>	<p>Jundm2_0911.3_v2_sec_untrimmed.txt (SW)  [Z-Beta = 61.0583]  [Pearson Full 0.429073291, Pearson Zeroed 0.36883]</p>	<p>Jundm2_0911.3_combined_sec_untrimmed.txt (SW)</p>
<p>Jundm2_0911.3_v1_prim_trimmed.txt (RM8)</p>	<p>Jundm2_0911.3_v2_prim_trimmed.txt (RM8)  [Z-Beta = 7.72611]  [Pearson Full 0.429073291, Pearson Zeroed 0.428005]</p>	<p>Jundm2_0911.3_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Jundm2_0911.3_v1_sec_trimmed.txt (RM8)</p>	<p>Jundm2_0911.3_v2_sec_trimmed.txt (RM8)  [Z-Beta = 8.981]  [Pearson Full 0.429073291, Pearson Zeroed 0.427712]</p>	<p>Jundm2_0911.3_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>

<p>Jundm2_0911.3_v1_prim_untrimmed.txt (RM8)</p> 	<p>Jundm2_0911.3_v2_prim_untrimmed.txt (RM8)</p> <p>[Z-Beta = 36.8804] [Pearson Full 0.429073291, Pearson Zeroed 0.409096]</p> 	<p>Jundm2_0911.3_RM8_combined_prim_untrimmed.txt (RM8)</p> <p>No PWM Available</p>
<p>Jundm2_0911.3_v1_sec_untrimmed.txt (RM8)</p> 	<p>Jundm2_0911.3_v2_sec_untrimmed.txt (RM8)</p> <p>[Z-Beta = 61.0583] [Pearson Full 0.429073291, Pearson Zeroed 0.36883]</p> 	<p>Jundm2_0911.3_RM8_combined_sec_untrimmed.txt (RM8)</p> <p>No PWM Available</p>
<p>Jundm2_0911.3_v1_RM35_prim_trimmed.txt (RM35)</p> 	<p>Jundm2_0911.3_v2_RM35_prim_trimmed.txt (RM35)</p> <p>[Z-Beta = 28.4101] [Pearson Full 0.429073291, Pearson Zeroed 0.415416]</p> 	<p>Jundm2_0911.3_RM_combined_prim_trimmed.txt (RM35)</p> 
<p>Jundm2_0911.3_v1_RM35_sec_trimmed.txt (RM35)</p> <p>[Z-Beta = 2.28629] [Pearson Full 0.613898153, Pearson Zeroed 0.61401]</p> 	<p>Jundm2_0911.3_v2_RM35_sec_trimmed.txt (RM35)</p> <p>[Z-Beta = -13.0762] [Pearson Full 0.429073291, Pearson Zeroed 0.425598]</p> 	<p>Jundm2_0911.3_RM_combined_sec_trimmed.txt (RM35)</p> 
<p>Jundm2_0911.3_v1_RM35_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = 34.5955] [Pearson Full 0.613898153, Pearson Zeroed 0.563966]</p> 	<p>Jundm2_0911.3_v2_RM35_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = 0.860792] [Pearson Full 0.429073291, Pearson Zeroed 0.429061]</p> 	<p>Jundm2_0911.3_RM_combined_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = 31.2458] [Pearson Full 0.612672479, Pearson Zeroed 0.563832]</p> 
<p>Jundm2_0911.3_v1_RM35_sec_untrimmed.txt (RM35)</p> <p>[Z-Beta = 88.8324] [Pearson Full 0.613898153, Pearson Zeroed 0.597042]</p> 	<p>Jundm2_0911.3_v2_RM35_sec_untrimmed.txt (RM35)</p> <p>[Z-Beta = 9.77901] [Pearson Full 0.429073291, Pearson Zeroed 0.427008]</p> 	<p>Jundm2_0911.3_RM_combined_sec_untrimmed.txt (RM35)</p> <p>[Z-Beta = 44.8384] [Pearson Full 0.612672479, Pearson Zeroed 0.550498]</p> 
<p>Jundm2_0911.3_PWM_1.txt (K-afal)</p> 	<p>Jundm2_0911.3_PWM_1.txt (K-afal)</p> <p>[Z-Beta = 24.1117] [Pearson Full 0.429073291, Pearson Zeroed 0.420986]</p> 	<p>Jundm2_0911.3_combined_PWM_1.txt (K-afal)</p> 
<p>Jundm2_0911.3_PWM_2.txt (K-afal)</p> 	<p>Jundm2_0911.3_PWM_2.txt (K-afal)</p> <p>No PWM Available</p>	<p>Jundm2_0911.3_combined_PWM_2.txt (K-afal)</p> <p>[Z-Beta = 49.7192] [Pearson Full 0.612672479, Pearson Zeroed 0.576232]</p> 
<p>Jundm2_0911.3_PWM_3.txt (K-afal)</p>	<p>Jundm2_0911.3_PWM_3.txt (K-afal)</p> <p>No PWM Available</p>	<p>Jundm2_0911.3_combined_PWM_3.txt (K-afal)</p> <p>No PWM Available</p>

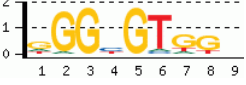





 <p>Sequence logo for PWM 4. The x-axis is labeled 1 to 10. The y-axis ranges from 0 to 2. The sequence is TCGATCA.</p>		
<p><b>Jundm2_0911.3_PWM_4.txt (K-afal)</b>  <b>[Z-Beta = 23.7547]</b>  <b>[Pearson Full 0.613898153, Pearson Zeroed 0.610512]</b></p> 	<p><b>Jundm2_0911.3_PWM_4.txt (K-afal)</b>  No PWM Available</p>	<p><b>Jundm2_0911.3_combined_PWM_4.txt (K-afal)</b>  No PWM Available</p>
<p><b>Jundm2_0911.3_PWM_5.txt (K-afal)</b></p> 	<p><b>Jundm2_0911.3_PWM_5.txt (K-afal)</b>  No PWM Available</p>	<p><b>Jundm2_0911.3_combined_PWM_5.txt (K-afal)</b>  No PWM Available</p>
<p><b>Jundm2_0911.3_PWM_6.txt (K-afal)</b></p> 	<p><b>Jundm2_0911.3_PWM_6.txt (K-afal)</b>  No PWM Available</p>	<p><b>Jundm2_0911.3_combined_PWM_6.txt (K-afal)</b>  No PWM Available</p>
<p><b>Jundm2_0911.3_PWM_7.txt (K-afal)</b>  <b>[Z-Beta = 4.92582]</b>  <b>[Pearson Full 0.613898153, Pearson Zeroed 0.608487]</b></p> 	<p><b>Jundm2_0911.3_PWM_7.txt (K-afal)</b>  No PWM Available</p>	<p><b>Jundm2_0911.3_combined_PWM_7.txt (K-afal)</b>  No PWM Available</p>



# Klf7\_0974.2

Array Version 1	Array Version 2	Combined
<p>Klf7_0974.2_v1_prim_auc.txt (SW)  [Z-Beta = 44.1204]  [Pearson Full 0.654470594, Pearson Zeroed 0.624813]</p> 	<p>Klf7_0974.2_v2_prim_auc.txt (SW)  [Z-Beta = 6.92364]  [Pearson Full 0.408970294, Pearson Zeroed 0.408257]</p> 	<p>Klf7_0974.2_combined_prim_auc.txt (SW)</p> 
<p>Klf7_0974.2_v1_prim_ic.txt (SW)</p> 	<p>Klf7_0974.2_v2_prim_ic.txt (SW)</p> 	<p>Klf7_0974.2_combined_prim_ic_trimmed.txt (SW)  [Z-Beta = -21.0583]  [Pearson Full 0.672487069, Pearson Zeroed 0.670865]</p> 
<p>Klf7_0974.2_v1_prim_untrimmed.txt (SW)</p> 	<p>Klf7_0974.2_v2_prim_untrimmed.txt (SW)</p> 	<p>Klf7_0974.2_combined_prim_untrimmed.txt (SW)  [Z-Beta = 95.4868]  [Pearson Full 0.672487069, Pearson Zeroed 0.619077]</p> 
<p>Klf7_0974.2_v1_sec_auc.txt (SW)</p> 	<p>Klf7_0974.2_v2_sec_auc.txt (SW)</p> 	<p>Klf7_0974.2_combined_sec_auc.txt (SW)</p> 
<p>Klf7_0974.2_v1_sec_ic.txt (SW)</p> 	<p>Klf7_0974.2_v2_sec_ic.txt (SW)</p> 	<p>Klf7_0974.2_combined_sec_ic_trimmed.txt (SW)  [Z-Beta = 1667.98]  [Pearson Full 0.672487069, Pearson Zeroed 0.0917198]</p> 
<p>Klf7_0974.2_v1_sec_untrimmed.txt (SW)</p> 	<p>Klf7_0974.2_v2_sec_untrimmed.txt (SW)</p> 	<p>Klf7_0974.2_combined_sec_untrimmed.txt (SW)  [Z-Beta = 42.0157]  [Pearson Full 0.672487069, Pearson Zeroed 0.661928]</p> 
<p>Klf7_0974.2_v1_prim_trimmed.txt (RM8)</p> 	<p>Klf7_0974.2_v2_prim_trimmed.txt (RM8)  [Z-Beta = -0.136757]  [Pearson Full 0.408970294, Pearson Zeroed 0.408703]</p> 	<p>Klf7_0974.2_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Klf7_0974.2_v1_sec_trimmed.txt (RM8)</p> 	<p>Klf7_0974.2_v2_sec_trimmed.txt (RM8)</p> 	<p>Klf7_0974.2_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Klf7_0974.2_v1_prim_untrimmed.txt (RM8)</p> 	<p>Klf7_0974.2_v2_prim_untrimmed.txt (RM8)</p> 	<p>Klf7_0974.2_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Klf7_0974.2_v1_sec_untrimmed.txt (RM8)</p>	<p>Klf7_0974.2_v2_sec_untrimmed.txt (RM8)</p>	<p>Klf7_0974.2_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>

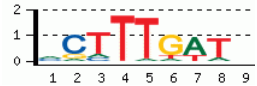






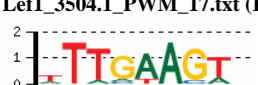

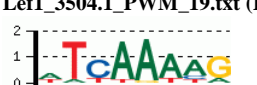
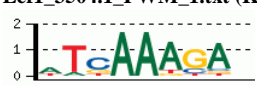





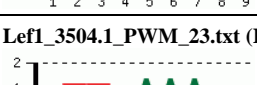
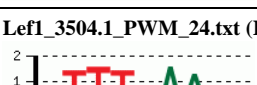
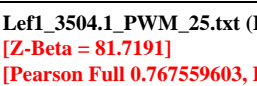
<b>Klf7_0974.2_v1_RM35_prim_trimmed.txt (RM35)</b> [Z-Beta = 96.8226] [Pearson Full 0.654470594, Pearson Zeroed 0.636956]	<b>Klf7_0974.2_v2_RM35_prim_trimmed.txt (RM35)</b> [Z-Beta = 32.1717] [Pearson Full 0.408970294, Pearson Zeroed 0.409118]	<b>Klf7_0974.2_RM_combined_prim_trimmed.txt (RM35)</b> [Z-Beta = -88.6118] [Pearson Full 0.672487069, Pearson Zeroed 0.664037]
<b>Klf7_0974.2_v1_RM35_sec_trimmed.txt (RM35)</b>	<b>Klf7_0974.2_v2_RM35_sec_trimmed.txt (RM35)</b> [Z-Beta = 28.4958] [Pearson Full 0.408970294, Pearson Zeroed 0.363682]	<b>Klf7_0974.2_RM_combined_sec_trimmed.txt (RM35)</b> [Z-Beta = -149.89] [Pearson Full 0.672487069, Pearson Zeroed 0.600886]
<b>Klf7_0974.2_v1_RM35_prim_untrimmed.txt (RM35)</b>	<b>Klf7_0974.2_v2_RM35_prim_untrimmed.txt (RM35)</b>	<b>Klf7_0974.2_RM_combined_prim_untrimmed.txt (RM35)</b> [Z-Beta = 139.935] [Pearson Full 0.672487069, Pearson Zeroed 0.587775]
<b>Klf7_0974.2_v1_RM35_sec_untrimmed.txt (RM35)</b>	<b>Klf7_0974.2_v2_RM35_sec_untrimmed.txt (RM35)</b>	<b>Klf7_0974.2_RM_combined_sec_untrimmed.txt (RM35)</b> [Z-Beta = 223.655] [Pearson Full 0.672487069, Pearson Zeroed 0.327127]
<b>Klf7_0974.2_PWM_1.txt (K-afal)</b>	<b>Klf7_0974.2_PWM_1.txt (K-afal)</b> [Z-Beta = 45.4705] [Pearson Full 0.408970294, Pearson Zeroed 0.395699]	<b>Klf7_0974.2_combined_PWM_1.txt (K-afal)</b> [Z-Beta = 19.4948] [Pearson Full 0.672487069, Pearson Zeroed 0.67165]
<b>Klf7_0974.2_PWM_2.txt (K-afal)</b>	<b>Klf7_0974.2_PWM_2.txt (K-afal)</b>	<b>Klf7_0974.2_combined_PWM_2.txt (K-afal)</b> [Z-Beta = -35.3057] [Pearson Full 0.672487069, Pearson Zeroed 0.669215]
<b>Klf7_0974.2_PWM_3.txt (K-afal)</b> [Z-Beta = 77.1414] [Pearson Full 0.654470594, Pearson Zeroed 0.620607]	<b>Klf7_0974.2_PWM_3.txt (K-afal)</b> [Z-Beta = 10.441] [Pearson Full 0.408970294, Pearson Zeroed 0.408128]	<b>Klf7_0974.2_combined_PWM_3.txt (K-afal)</b> [Z-Beta = 162.321] [Pearson Full 0.672487069, Pearson Zeroed 0.576815]
		<p><b>Klf7_0974.2_combined_PWM_4.txt (K-afal)</b>  No PWM Available</p>
<b>Klf7_0974.2_PWM_4.txt (K-afal)</b>	<b>Klf7_0974.2_PWM_4.txt (K-afal)</b> [Z-Beta = 25.4468] [Pearson Full 0.408970294, Pearson Zeroed 0.398485]	

<p>Klf7_0974.2_PWM_5.txt (K-afal)</p> 	<p>Klf7_0974.2_PWM_5.txt (K-afal)  [Z-Beta = 8.56147]  [Pearson Full 0.408970294, Pearson Zeroed 0.408294]</p> 	<p>Klf7_0974.2_combined_PWM_5.txt (K-afal)  No PWM Available</p>
<p>Klf7_0974.2_PWM_6.txt (K-afal)</p> 	<p>Klf7_0974.2_PWM_6.txt (K-afal)  No PWM Available</p>	<p>Klf7_0974.2_combined_PWM_6.txt (K-afal)  No PWM Available</p>
<p>Klf7_0974.2_PWM_7.txt (K-afal)</p> 	<p>Klf7_0974.2_PWM_7.txt (K-afal)  No PWM Available</p>	<p>Klf7_0974.2_combined_PWM_7.txt (K-afal)  No PWM Available</p>
<p>Klf7_0974.2_PWM_8.txt (K-afal)  [Z-Beta = 63.7677]  [Pearson Full 0.654470594, Pearson Zeroed 0.632416]</p> 	<p>Klf7_0974.2_PWM_8.txt (K-afal)  No PWM Available</p>	<p>Klf7_0974.2_combined_PWM_8.txt (K-afal)  No PWM Available</p>
<p>Klf7_0974.2_PWM_9.txt (K-afal)</p> 	<p>Klf7_0974.2_PWM_9.txt (K-afal)  No PWM Available</p>	<p>Klf7_0974.2_combined_PWM_9.txt (K-afal)  No PWM Available</p>



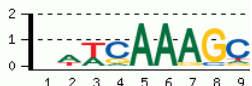
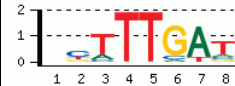





















# Lef1\_3504.1

Array Version 1	Array Version 2	Combined
<p>Lef1_3504.1_v1_prim_auc.txt (SW)</p>	<p>Lef1_3504.1_v2_prim_auc.txt (SW)</p>	<p>Lef1_3504.1_combined_prim_auc.txt (SW)</p>
<p>Lef1_3504.1_v1_prim_ic.txt (SW)</p>	<p>Lef1_3504.1_v2_prim_ic.txt (SW)</p>	<p>Lef1_3504.1_combined_prim_ic_trimmed.txt (SW)</p>
<p>Lef1_3504.1_v1_prim_untrimmed.txt (SW) [Z-Beta = 10.3656] [Pearson Full 0.767559603, Pearson Zeroed 0.767408]</p>	<p>Lef1_3504.1_v2_prim_untrimmed.txt (SW) [Z-Beta = 19.4007] [Pearson Full 0.736290458, Pearson Zeroed 0.737224]</p>	<p>Lef1_3504.1_combined_prim_untrimmed.txt (SW)</p>
<p>Lef1_3504.1_v1_sec_auc.txt (SW)</p>	<p>Lef1_3504.1_v2_sec_auc.txt (SW)</p>	<p>Lef1_3504.1_combined_sec_auc.txt (SW)</p>
<p>Lef1_3504.1_v1_sec_ic.txt (SW)</p>	<p>Lef1_3504.1_v2_sec_ic.txt (SW) [Z-Beta = 7.0229] [Pearson Full 0.736290458, Pearson Zeroed 0.729981]</p>	<p>Lef1_3504.1_combined_sec_ic_trimmed.txt (SW)</p>
<p>Lef1_3504.1_v1_sec_untrimmed.txt (SW)</p>	<p>Lef1_3504.1_v2_sec_untrimmed.txt (SW) [Z-Beta = 23.91] [Pearson Full 0.736290458, Pearson Zeroed 0.738565]</p>	<p>Lef1_3504.1_combined_sec_untrimmed.txt (SW)</p>
<p>Lef1_3504.1_v1_prim_trimmed.txt (RM8)</p>	<p>Lef1_3504.1_v2_prim_trimmed.txt (RM8) [Z-Beta = 31.0857] [Pearson Full 0.736290458, Pearson Zeroed 0.715771]</p>	<p>Lef1_3504.1_RM8_combined_prim_trimmed.txt (RM8) No PWM Available</p>
<p>Lef1_3504.1_v1_sec_trimmed.txt (RM8)</p>	<p>Lef1_3504.1_v2_sec_trimmed.txt (RM8) [Z-Beta = 69.7547] [Pearson Full 0.736290458, Pearson Zeroed 0.681583]</p>	<p>Lef1_3504.1_RM8_combined_sec_trimmed.txt (RM8) No PWM Available</p>
<p>Lef1_3504.1_v1_prim_untrimmed.txt (RM8)</p>	<p>Lef1_3504.1_v2_prim_untrimmed.txt (RM8) [Z-Beta = 19.4007] [Pearson Full 0.736290458, Pearson Zeroed 0.737224]</p>	<p>Lef1_3504.1_RM8_combined_prim_untrimmed.txt (RM8) No PWM Available</p>



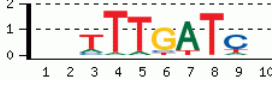





<p>Lef1_3504.1_v1_sec_untrimmed.txt (RM8)</p>	<p>Lef1_3504.1_v2_sec_untrimmed.txt (RM8) [Z-Beta = 23.91] [Pearson Full 0.736290458, Pearson Zeroed 0.738565]</p>	<p>Lef1_3504.1_RM8_combined_sec_untrimmed.txt (RM8) No PWM Available</p>
<p>Lef1_3504.1_v1_RM35_prim_trimmed.txt (RM35)</p>	<p>Lef1_3504.1_v2_RM35_prim_trimmed.txt (RM35)</p>	<p>Lef1_3504.1_RM_combined_prim_trimmed.txt (RM35) [Z-Beta = 8.7958] [Pearson Full 0.725966311, Pearson Zeroed 0.722638]</p>
<p>Lef1_3504.1_v1_RM35_sec_trimmed.txt (RM35) [Z-Beta = 224.909] [Pearson Full 0.767559603, Pearson Zeroed 0.721558]</p>	<p>Lef1_3504.1_v2_RM35_sec_trimmed.txt (RM35)</p>	<p>Lef1_3504.1_RM_combined_sec_trimmed.txt (RM35) [Z-Beta = 104.913] [Pearson Full 0.725966311, Pearson Zeroed 0.662804]</p>
<p>Lef1_3504.1_v1_RM35_prim_untrimmed.txt (RM35) [Z-Beta = 83.3695] [Pearson Full 0.767559603, Pearson Zeroed 0.744669]</p>	<p>Lef1_3504.1_v2_RM35_prim_untrimmed.txt (RM35)</p>	<p>Lef1_3504.1_RM_combined_prim_untrimmed.txt (RM35) [Z-Beta = 53.8451] [Pearson Full 0.725966311, Pearson Zeroed 0.693494]</p>
<p>Lef1_3504.1_v1_RM35_sec_untrimmed.txt (RM35)</p>	<p>Lef1_3504.1_v2_RM35_sec_untrimmed.txt (RM35)</p>	<p>Lef1_3504.1_RM_combined_sec_untrimmed.txt (RM35)</p>
<p>Lef1_3504.1_PWM_10.txt (K-afal)</p>	<p>Lef1_3504.1_PWM_10.txt (K-afal)</p>	<p>Lef1_3504.1_combined_PWM_10.txt (K-afal) No PWM Available</p>
<p>Lef1_3504.1_PWM_11.txt (K-afal)</p>	<p>Lef1_3504.1_PWM_11.txt (K-afal) [Z-Beta = 4.87246] [Pearson Full 0.736290458, Pearson Zeroed 0.730423]</p>	<p>Lef1_3504.1_combined_PWM_11.txt (K-afal) No PWM Available</p>
<p>Lef1_3504.1_PWM_12.txt (K-afal)</p>	<p>Lef1_3504.1_PWM_12.txt (K-afal)</p>	<p>Lef1_3504.1_combined_PWM_12.txt (K-afal) No PWM Available</p>
<p>Lef1_3504.1_PWM_13.txt (K-afal) [Z-Beta = 5.44519] [Pearson Full 0.767559603, Pearson Zeroed 0.766198]</p>	<p>Lef1_3504.1_PWM_13.txt (K-afal) [Z-Beta = 0.251711] [Pearson Full 0.736290458, Pearson Zeroed 0.736066]</p>	<p>Lef1_3504.1_combined_PWM_13.txt (K-afal) No PWM Available</p>
<p>Lef1_3504.1_PWM_14.txt (K-afal) [Z-Beta = 99.8307] [Pearson Full 0.767559603, Pearson Zeroed 0.753374]</p>	<p>Lef1_3504.1_PWM_14.txt (K-afal)</p>	<p>Lef1_3504.1_combined_PWM_14.txt (K-afal) No PWM Available</p>

		
Lef1_3504.1_PWM_15.txt (K-afal) 	Lef1_3504.1_PWM_15.txt (K-afal) 	Lef1_3504.1_combined_PWM_15.txt (K-afal) No PWM Available
Lef1_3504.1_PWM_16.txt (K-afal) 	Lef1_3504.1_PWM_16.txt (K-afal) [Z-Beta = 3.26418] [Pearson Full 0.736290458, Pearson Zeroed 0.733054] 	Lef1_3504.1_combined_PWM_16.txt (K-afal) No PWM Available
Lef1_3504.1_PWM_17.txt (K-afal) 	Lef1_3504.1_PWM_17.txt (K-afal) 	Lef1_3504.1_combined_PWM_17.txt (K-afal) No PWM Available
Lef1_3504.1_PWM_18.txt (K-afal) 	Lef1_3504.1_PWM_18.txt (K-afal) No PWM Available	Lef1_3504.1_combined_PWM_18.txt (K-afal) No PWM Available
Lef1_3504.1_PWM_19.txt (K-afal) 	Lef1_3504.1_PWM_19.txt (K-afal) No PWM Available	Lef1_3504.1_combined_PWM_19.txt (K-afal) No PWM Available
Lef1_3504.1_PWM_1.txt (K-afal) 	Lef1_3504.1_PWM_1.txt (K-afal) 	Lef1_3504.1_combined_PWM_1.txt (K-afal) 
Lef1_3504.1_PWM_20.txt (K-afal) 	Lef1_3504.1_PWM_20.txt (K-afal) No PWM Available	Lef1_3504.1_combined_PWM_20.txt (K-afal) No PWM Available
Lef1_3504.1_PWM_21.txt (K-afal) 	Lef1_3504.1_PWM_21.txt (K-afal) No PWM Available	Lef1_3504.1_combined_PWM_21.txt (K-afal) No PWM Available
Lef1_3504.1_PWM_22.txt (K-afal) [Z-Beta = 4.31453] [Pearson Full 0.767559603, Pearson Zeroed 0.766734] 	Lef1_3504.1_PWM_22.txt (K-afal) No PWM Available	Lef1_3504.1_combined_PWM_22.txt (K-afal) No PWM Available
Lef1_3504.1_PWM_23.txt (K-afal) 	Lef1_3504.1_PWM_23.txt (K-afal) No PWM Available	Lef1_3504.1_combined_PWM_23.txt (K-afal) No PWM Available
Lef1_3504.1_PWM_24.txt (K-afal) 	Lef1_3504.1_PWM_24.txt (K-afal) No PWM Available	Lef1_3504.1_combined_PWM_24.txt (K-afal) No PWM Available
Lef1_3504.1_PWM_25.txt (K-afal) [Z-Beta = 81.7191] [Pearson Full 0.767559603, Pearson Zeroed 0.733697] 	Lef1_3504.1_PWM_25.txt (K-afal) No PWM Available	Lef1_3504.1_combined_PWM_25.txt (K-afal) No PWM Available



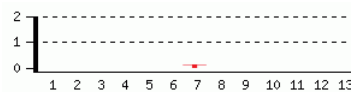
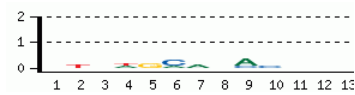













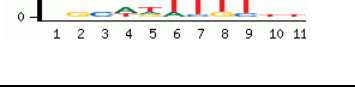

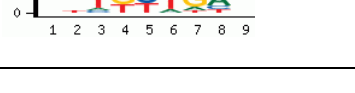
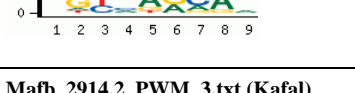



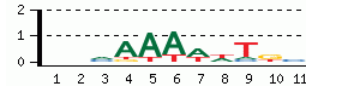

		
<p>Lef1_3504.1_PWM_26.txt (K-afal)  [Z-Beta = 61.3533]  [Pearson Full 0.767559603, Pearson Zeroed 0.762066]</p> 	<p>Lef1_3504.1_PWM_26.txt (K-afal)  No PWM Available</p>	<p>Lef1_3504.1_combined_PWM_26.txt (K-afal)  No PWM Available</p>
<p>Lef1_3504.1_PWM_2.txt (K-afal)</p> 	<p>Lef1_3504.1_PWM_2.txt (K-afal)  [Z-Beta = 24.7414]  [Pearson Full 0.736290458, Pearson Zeroed 0.702588]</p> 	<p>Lef1_3504.1_combined_PWM_2.txt (K-afal)</p> 
<p>Lef1_3504.1_PWM_3.txt (K-afal)  [Z-Beta = 33.9755]  [Pearson Full 0.767559603, Pearson Zeroed 0.757177]</p> 	<p>Lef1_3504.1_PWM_3.txt (K-afal)</p> 	<p>Lef1_3504.1_combined_PWM_3.txt (K-afal)</p> 
<p>Lef1_3504.1_PWM_4.txt (K-afal)</p> 	<p>Lef1_3504.1_PWM_4.txt (K-afal)</p> 	<p>Lef1_3504.1_combined_PWM_4.txt (K-afal)</p> 
<p>Lef1_3504.1_PWM_5.txt (K-afal)</p> 	<p>Lef1_3504.1_PWM_5.txt (K-afal)</p> 	<p>Lef1_3504.1_combined_PWM_5.txt (K-afal)  [Z-Beta = 10.5139]  [Pearson Full 0.725966311, Pearson Zeroed 0.721392]</p> 
<p>Lef1_3504.1_PWM_6.txt (K-afal)  [Z-Beta = 43.5581]  [Pearson Full 0.767559603, Pearson Zeroed 0.760848]</p> 	<p>Lef1_3504.1_PWM_6.txt (K-afal)</p> 	<p>Lef1_3504.1_combined_PWM_6.txt (K-afal)  [Z-Beta = 108.239]  [Pearson Full 0.725966311, Pearson Zeroed 0.677481]</p> 
<p>Lef1_3504.1_PWM_7.txt (K-afal)</p> 	<p>Lef1_3504.1_PWM_7.txt (K-afal)</p> 	<p>Lef1_3504.1_combined_PWM_7.txt (K-afal)</p> 
<p>Lef1_3504.1_PWM_8.txt (K-afal)</p> 	<p>Lef1_3504.1_PWM_8.txt (K-afal)</p> 	<p>Lef1_3504.1_combined_PWM_8.txt (K-afal)  [Z-Beta = 45.8186]  [Pearson Full 0.725966311, Pearson Zeroed 0.709439]</p> 
<p>Lef1_3504.1_PWM_9.txt (K-afal)</p> 	<p>Lef1_3504.1_PWM_9.txt (K-afal)</p> 	<p>Lef1_3504.1_combined_PWM_9.txt (K-afal)  No PWM Available</p>
<p>Lef1_3504.1_PWM_10.txt (Kafal)  No PWM Available</p>	<p>Lef1_3504.1_PWM_10.txt (Kafal)  No PWM Available</p>	<p>Lef1_3504.1_combined_PWM_10.txt (Kafal)  No PWM Available</p>



		
Lef1_3504.1_PWM_11.txt (Kafal) No PWM Available	<b>Lef1_3504.1_PWM_11.txt (Kafal)</b> [Z-Beta = 4.87246] [Pearson Full 0.736290458, Pearson Zeroed 0.730423] 	Lef1_3504.1_combined_PWM_11.txt (Kafal) No PWM Available
Lef1_3504.1_PWM_12.txt (Kafal) No PWM Available	<b>Lef1_3504.1_PWM_12.txt (Kafal)</b> 	Lef1_3504.1_combined_PWM_12.txt (Kafal) No PWM Available
Lef1_3504.1_PWM_13.txt (Kafal) No PWM Available	<b>Lef1_3504.1_PWM_13.txt (Kafal)</b> [Z-Beta = 0.251711] [Pearson Full 0.736290458, Pearson Zeroed 0.736066] 	Lef1_3504.1_combined_PWM_13.txt (Kafal) No PWM Available
Lef1_3504.1_PWM_14.txt (Kafal) No PWM Available	<b>Lef1_3504.1_PWM_14.txt (Kafal)</b> 	Lef1_3504.1_combined_PWM_14.txt (Kafal) No PWM Available
Lef1_3504.1_PWM_15.txt (Kafal) No PWM Available	<b>Lef1_3504.1_PWM_15.txt (Kafal)</b> 	Lef1_3504.1_combined_PWM_15.txt (Kafal) No PWM Available
Lef1_3504.1_PWM_16.txt (Kafal) No PWM Available	<b>Lef1_3504.1_PWM_16.txt (Kafal)</b> [Z-Beta = 3.26418] [Pearson Full 0.736290458, Pearson Zeroed 0.733054] 	Lef1_3504.1_combined_PWM_16.txt (Kafal) No PWM Available
Lef1_3504.1_PWM_17.txt (Kafal) No PWM Available	<b>Lef1_3504.1_PWM_17.txt (Kafal)</b> 	Lef1_3504.1_combined_PWM_17.txt (Kafal) No PWM Available

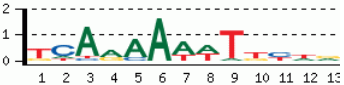

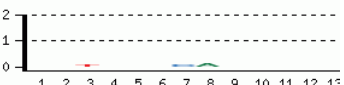











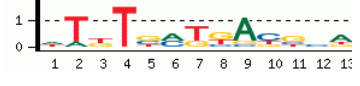



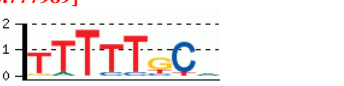
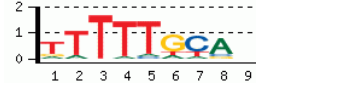
# Mafb\_2914.2

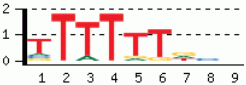
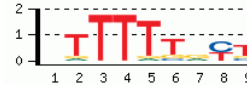


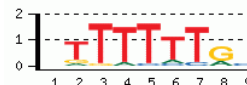







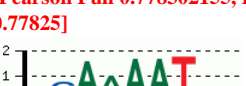
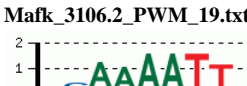

Array Version 1	Array Version 2	Combined
<p>Mafb_2914.2_v1_prim_auc.txt (SW)</p>	<p>Mafb_2914.2_v2_prim_auc.txt (SW)</p>	<p>Mafb_2914.2_combined_prim_auc.txt (SW)</p>
<p>Mafb_2914.2_v1_prim_ic.txt (SW)</p>	<p>Mafb_2914.2_v2_prim_ic.txt (SW)</p>	<p>Mafb_2914.2_combined_prim_ic_trimmed.txt (SW)</p>
<p>Mafb_2914.2_v1_prim_untrimmed.txt (SW)</p>	<p>Mafb_2914.2_v2_prim_untrimmed.txt (SW)</p>	<p>Mafb_2914.2_combined_prim_untrimmed.txt (SW)</p>
<p>Mafb_2914.2_v1_sec_auc.txt (SW)</p>	<p>Mafb_2914.2_v2_sec_auc.txt (SW)  [Z-Beta = 1.09765]  [Pearson Full 0.551306308, Pearson Zeroed 0.550573]</p>	<p>Mafb_2914.2_combined_sec_auc.txt (SW)</p>
<p>Mafb_2914.2_v1_sec_ic.txt (SW)  [Z-Beta = 7.75335]  [Pearson Full 0.515712937, Pearson Zeroed 0.503791]</p>	<p>Mafb_2914.2_v2_sec_ic.txt (SW)</p>	<p>Mafb_2914.2_combined_sec_ic_trimmed.txt (SW)</p>
<p>Mafb_2914.2_v1_sec_untrimmed.txt (SW)</p>	<p>Mafb_2914.2_v2_sec_untrimmed.txt (SW)  [Z-Beta = 13.2522]  [Pearson Full 0.551306308, Pearson Zeroed 0.53318]</p>	<p>Mafb_2914.2_combined_sec_untrimmed.txt (SW)  [Z-Beta = 18.3988]  [Pearson Full 0.50123884, Pearson Zeroed 0.491634]</p>
<p>Mafb_2914.2_v1_prim_trimmed.txt (RM8)</p>	<p>Mafb_2914.2_v2_prim_trimmed.txt (RM8)</p>	<p>Mafb_2914.2_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Mafb_2914.2_v1_sec_trimmed.txt (RM8)  [Z-Beta = 54.4914]  [Pearson Full 0.515712937, Pearson Zeroed 0.512042]</p>	<p>Mafb_2914.2_v2_sec_trimmed.txt (RM8)</p>	<p>Mafb_2914.2_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Mafb_2914.2_v1_prim_untrimmed.txt (RM8)</p>	<p>Mafb_2914.2_v2_prim_untrimmed.txt (RM8)</p>	<p>Mafb_2914.2_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Mafb_2914.2_v1_sec_untrimmed.txt (RM8)  [Z-Beta = 25.114]  [Pearson Full 0.515712937, Pearson Zeroed 0.515325]</p>	<p>Mafb_2914.2_v2_sec_untrimmed.txt (RM8)  [Z-Beta = 13.2522]  [Pearson Full 0.551306308, Pearson Zeroed 0.53318]</p>	<p>Mafb_2914.2_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>



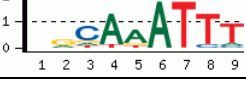









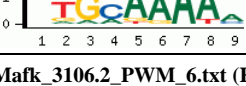

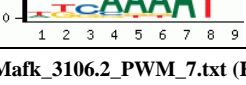

		
<b>Mafb_2914.2_v1_RM35_prim_trimmed.txt (RM35)</b> 	<b>Mafb_2914.2_v2_RM35_prim_trimmed.txt (RM35)</b> [Z-Beta = 89.2689] [Pearson Full 0.551306308, Pearson Zeroed 0.523064] 	<b>Mafb_2914.2_RM_combined_prim_trimmed.txt (RM35)</b> [Z-Beta = 86.0615] [Pearson Full 0.50123884, Pearson Zeroed 0.329927] 
<b>Mafb_2914.2_v1_RM35_sec_trimmed.txt (RM35)</b> 	<b>Mafb_2914.2_v2_RM35_sec_trimmed.txt (RM35)</b> 	<b>Mafb_2914.2_RM_combined_sec_trimmed.txt (RM35)</b> 
<b>Mafb_2914.2_v1_RM35_prim_untrimmed.txt (RM35)</b> 	<b>Mafb_2914.2_v2_RM35_prim_untrimmed.txt (RM35)</b> [Z-Beta = 7.29442] [Pearson Full 0.551306308, Pearson Zeroed 0.543993] 	<b>Mafb_2914.2_RM_combined_prim_untrimmed.txt (RM35)</b> 
<b>Mafb_2914.2_v1_RM35_sec_untrimmed.txt (RM35)</b> 	<b>Mafb_2914.2_v2_RM35_sec_untrimmed.txt (RM35)</b> 	<b>Mafb_2914.2_RM_combined_sec_untrimmed.txt (RM35)</b> 
<b>Mafb_2914.2_PWM_1.txt (K-afal)</b> [Z-Beta = 7.61663] [Pearson Full 0.515712937, Pearson Zeroed 0.500513] 	<b>Mafb_2914.2_PWM_1.txt (K-afal)</b> 	<b>Mafb_2914.2_combined_PWM_1.txt (K-afal)</b> [Z-Beta = 17.7088] [Pearson Full 0.50123884, Pearson Zeroed 0.491344] 
<b>Mafb_2914.2_PWM_2.txt (K-afal)</b> 	<b>Mafb_2914.2_PWM_2.txt (K-afal)</b> 	<b>Mafb_2914.2_combined_PWM_2.txt (K-afal)</b> [Z-Beta = 18.6003] [Pearson Full 0.50123884, Pearson Zeroed 0.491128] 
<b>Mafb_2914.2_PWM_3.txt (Kafal)</b> No PWM Available	<b>Mafb_2914.2_PWM_3.txt (Kafal)</b> 	<b>Mafb_2914.2_combined_PWM_3.txt (Kafal)</b> 
<b>Mafb_2914.2_PWM_4.txt (Kafal)</b> No PWM Available	<b>Mafb_2914.2_PWM_4.txt (Kafal)</b> [Z-Beta = 18.8106] [Pearson Full 0.551306308, Pearson Zeroed 0.543458] 	<b>Mafb_2914.2_combined_PWM_4.txt (Kafal)</b> 

# Mafk\_3106.2

Array Version 1	Array Version 2	Combined
<p>Mafk_3106.2_v1_prim_auc.txt (SW)  [Z-Beta = -120.188]  [Pearson Full 0.778302155, Pearson Zeroed 0.641992]</p>	<p>Mafk_3106.2_v2_prim_auc.txt (SW)</p>	<p>Mafk_3106.2_combined_prim_auc.txt (SW)</p>
<p>Mafk_3106.2_v1_prim_ic.txt (SW)  [Z-Beta = -5.40551]  [Pearson Full 0.778302155, Pearson Zeroed 0.777882]</p>	<p>Mafk_3106.2_v2_prim_ic.txt (SW)</p>	<p>Mafk_3106.2_combined_prim_ic_trimmed.txt (SW)</p>
<p>Mafk_3106.2_v1_prim_untrimmed.txt (SW)  [Z-Beta = 139.595]  [Pearson Full 0.778302155, Pearson Zeroed 0.524108]</p>	<p>Mafk_3106.2_v2_prim_untrimmed.txt (SW)</p>	<p>Mafk_3106.2_combined_prim_untrimmed.txt (SW)</p>
<p>Mafk_3106.2_v1_sec_auc.txt (SW)  [Z-Beta = 3.65503]  [Pearson Full 0.778302155, Pearson Zeroed 0.778116]</p>	<p>Mafk_3106.2_v2_sec_auc.txt (SW)</p>	<p>Mafk_3106.2_combined_sec_auc.txt (SW)</p>
<p>Mafk_3106.2_v1_sec_ic.txt (SW)  [Z-Beta = 1.92782]  [Pearson Full 0.778302155, Pearson Zeroed 0.778247]</p>	<p>Mafk_3106.2_v2_sec_ic.txt (SW)</p>	<p>Mafk_3106.2_combined_sec_ic_trimmed.txt (SW)</p>
<p>Mafk_3106.2_v1_sec_untrimmed.txt (SW)  [Z-Beta = -7.75955]  [Pearson Full 0.778302155, Pearson Zeroed 0.777493]</p>	<p>Mafk_3106.2_v2_sec_untrimmed.txt (SW)  [Z-Beta = 133.47]  [Pearson Full 0.666662301, Pearson Zeroed 0.470711]</p>	<p>Mafk_3106.2_combined_sec_untrimmed.txt (SW)</p>
<p>Mafk_3106.2_v1_prim_trimmed.txt (RM8)  [Z-Beta = -6.4476]  [Pearson Full 0.778302155, Pearson Zeroed 0.777879]</p>	<p>Mafk_3106.2_v2_prim_trimmed.txt (RM8)</p>	<p>Mafk_3106.2_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Mafk_3106.2_v1_sec_trimmed.txt (RM8)  [Z-Beta = 12.9233]  [Pearson Full 0.778302155, Pearson Zeroed 0.776796]</p>	<p>Mafk_3106.2_v2_sec_trimmed.txt (RM8)</p>	<p>Mafk_3106.2_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>


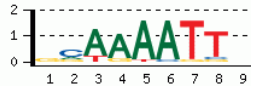

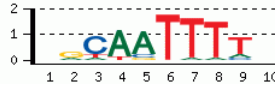

<p>Mafk_3106.2_v1_prim_untrimmed.txt (RM8)  [Z-Beta = -9.97842]  [Pearson Full 0.778302155, Pearson Zeroed 0.77727]</p> 	<p>Mafk_3106.2_v2_prim_untrimmed.txt (RM8)</p> 	<p>Mafk_3106.2_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Mafk_3106.2_v1_sec_untrimmed.txt (RM8)  [Z-Beta = 139.991]  [Pearson Full 0.778302155, Pearson Zeroed 0.590272]</p> 	<p>Mafk_3106.2_v2_sec_untrimmed.txt (RM8)  [Z-Beta = 133.47]  [Pearson Full 0.666662301, Pearson Zeroed 0.470711]</p> 	<p>Mafk_3106.2_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>
<p>Mafk_3106.2_v1_RM35_prim_trimmed.txt (RM35)  [Z-Beta = -15.9463]  [Pearson Full 0.778302155, Pearson Zeroed 0.775752]</p> 	<p>Mafk_3106.2_v2_RM35_prim_trimmed.txt (RM35)</p> 	<p>Mafk_3106.2_RM_combined_prim_trimmed.txt (RM35)</p> 
<p>Mafk_3106.2_v1_RM35_sec_trimmed.txt (RM35)  [Z-Beta = 2.68529]  [Pearson Full 0.778302155, Pearson Zeroed 0.778221]</p> 	<p>Mafk_3106.2_v2_RM35_sec_trimmed.txt (RM35)  [Z-Beta = 11.2403]  [Pearson Full 0.666662301, Pearson Zeroed 0.655323]</p> 	<p>Mafk_3106.2_RM_combined_sec_trimmed.txt (RM35)</p> 
<p>Mafk_3106.2_v1_RM35_prim_untrimmed.txt (RM35)  [Z-Beta = 18.9492]  [Pearson Full 0.778302155, Pearson Zeroed 0.774106]</p> 	<p>Mafk_3106.2_v2_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Mafk_3106.2_RM_combined_prim_untrimmed.txt (RM35)</p> 
<p>Mafk_3106.2_v1_RM35_sec_untrimmed.txt (RM35)  [Z-Beta = 19.0745]  [Pearson Full 0.778302155, Pearson Zeroed 0.774253]</p> 	<p>Mafk_3106.2_v2_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Mafk_3106.2_RM_combined_sec_untrimmed.txt (RM35)</p> 
<p>Mafk_3106.2_PWM_10.txt (K-afal)  [Z-Beta = 67.7679]  [Pearson Full 0.778302155, Pearson Zeroed 0.709025]</p> 	<p>Mafk_3106.2_PWM_10.txt (K-afal)</p> 	<p>Mafk_3106.2_combined_PWM_10.txt (K-afal)  No PWM Available</p>
<p>Mafk_3106.2_PWM_11.txt (K-afal)  [Z-Beta = -5.13771]  [Pearson Full 0.778302155, Pearson Zeroed 0.777989]</p> 	<p>Mafk_3106.2_PWM_11.txt (K-afal)</p> 	<p>Mafk_3106.2_combined_PWM_11.txt (K-afal)  No PWM Available</p>

<p>Mafk_3106.2_PWM_12.txt (K-afal)  [Z-Beta = 2.70123]  [Pearson Full 0.778302155, Pearson Zeroed 0.778215]</p> 	<p>Mafk_3106.2_PWM_12.txt (K-afal)  [Z-Beta = 14.6293]  [Pearson Full 0.666662301, Pearson Zeroed 0.655009]</p> 	<p>Mafk_3106.2_combined_PWM_12.txt (K-afal)  No PWM Available</p>
<p>Mafk_3106.2_PWM_13.txt (K-afal)  [Z-Beta = 9.48284]  [Pearson Full 0.778302155, Pearson Zeroed 0.77725]</p> 	<p>Mafk_3106.2_PWM_13.txt (K-afal)  [Z-Beta = 5.79906]  [Pearson Full 0.666662301, Pearson Zeroed 0.663627]</p> 	<p>Mafk_3106.2_combined_PWM_13.txt (K-afal)  No PWM Available</p>
<p>Mafk_3106.2_PWM_14.txt (K-afal)  [Z-Beta = 32.2639]  [Pearson Full 0.778302155, Pearson Zeroed 0.76488]</p> 	<p>Mafk_3106.2_PWM_14.txt (K-afal)</p> 	<p>Mafk_3106.2_combined_PWM_14.txt (K-afal)  No PWM Available</p>
<p>Mafk_3106.2_PWM_15.txt (K-afal)  [Z-Beta = 1.78893]  [Pearson Full 0.778302155, Pearson Zeroed 0.778268]</p> 	<p>Mafk_3106.2_PWM_15.txt (K-afal)</p> 	<p>Mafk_3106.2_combined_PWM_15.txt (K-afal)  No PWM Available</p>
<p>Mafk_3106.2_PWM_16.txt (K-afal)  [Z-Beta = 11.8908]  [Pearson Full 0.778302155, Pearson Zeroed 0.776708]</p> 	<p>Mafk_3106.2_PWM_16.txt (K-afal)  [Z-Beta = 11.7158]  [Pearson Full 0.666662301, Pearson Zeroed 0.65837]</p> 	<p>Mafk_3106.2_combined_PWM_16.txt (K-afal)  No PWM Available</p>
<p>Mafk_3106.2_PWM_17.txt (K-afal)  [Z-Beta = 22.6311]  [Pearson Full 0.778302155, Pearson Zeroed 0.771695]</p> 	<p>Mafk_3106.2_PWM_17.txt (K-afal)  [Z-Beta = 30.8202]  [Pearson Full 0.666662301, Pearson Zeroed 0.637141]</p> 	<p>Mafk_3106.2_combined_PWM_17.txt (K-afal)  No PWM Available</p>
<p>Mafk_3106.2_PWM_18.txt (K-afal)  [Z-Beta = 5.68906]  [Pearson Full 0.778302155, Pearson Zeroed 0.777907]</p> 	<p>Mafk_3106.2_PWM_18.txt (K-afal)</p> 	<p>Mafk_3106.2_combined_PWM_18.txt (K-afal)  No PWM Available</p>
<p>Mafk_3106.2_PWM_19.txt (K-afal)  [Z-Beta = -2.07382]  [Pearson Full 0.778302155, Pearson Zeroed 0.77825]</p> 	<p>Mafk_3106.2_PWM_19.txt (K-afal)</p> 	<p>Mafk_3106.2_combined_PWM_19.txt (K-afal)  No PWM Available</p>
<p>Mafk_3106.2_PWM_1.txt (K-afal)  [Z-Beta = 22.9616]  [Pearson Full 0.778302155, Pearson Zeroed 0.772049]</p>	<p>Mafk_3106.2_PWM_1.txt (K-afal)  [Z-Beta = 24.5683]  [Pearson Full 0.666662301, Pearson Zeroed 0.643633]</p>	<p>Mafk_3106.2_combined_PWM_1.txt (K-afal)  [Z-Beta = 66.6066]  [Pearson Full 0.51315206, Pearson Zeroed NaN]</p> 

 <p><b>Mafk_3106.2_PWM_20.txt (K-afal)</b>  [Z-Beta = 10.9947]  [Pearson Full 0.778302155, Pearson Zeroed 0.776656]</p>	 <p><b>Mafk_3106.2_PWM_20.txt (K-afal)</b></p>	Mafk_3106.2_combined_PWM_20.txt (K-afal) No PWM Available
 <p><b>Mafk_3106.2_PWM_21.txt (K-afal)</b>  [Z-Beta = 7.06158]  [Pearson Full 0.778302155, Pearson Zeroed 0.77658]</p>	 <p><b>Mafk_3106.2_PWM_21.txt (K-afal)</b></p>	Mafk_3106.2_combined_PWM_21.txt (K-afal) No PWM Available
 <p><b>Mafk_3106.2_PWM_2.txt (K-afal)</b>  [Z-Beta = -0.948851]  [Pearson Full 0.778302155, Pearson Zeroed 0.778292]</p>	 <p><b>Mafk_3106.2_PWM_2.txt (K-afal)</b></p>	Mafk_3106.2_combined_PWM_2.txt (K-afal)
 <p><b>Mafk_3106.2_PWM_3.txt (K-afal)</b>  [Z-Beta = 20.4661]  [Pearson Full 0.778302155, Pearson Zeroed 0.773079]</p>	 <p><b>Mafk_3106.2_PWM_3.txt (K-afal)</b></p>	Mafk_3106.2_combined_PWM_3.txt (K-afal) No PWM Available
 <p><b>Mafk_3106.2_PWM_4.txt (K-afal)</b>  [Z-Beta = -55.8147]  [Pearson Full 0.778302155, Pearson Zeroed 0.741291]</p>	 <p><b>Mafk_3106.2_PWM_4.txt (K-afal)</b></p>	Mafk_3106.2_combined_PWM_4.txt (K-afal) No PWM Available
 <p><b>Mafk_3106.2_PWM_5.txt (K-afal)</b>  [Z-Beta = 16.8642]  [Pearson Full 0.778302155, Pearson Zeroed 0.774666]</p>	 <p><b>Mafk_3106.2_PWM_5.txt (K-afal)</b></p>	Mafk_3106.2_combined_PWM_5.txt (K-afal) No PWM Available
 <p><b>Mafk_3106.2_PWM_6.txt (K-afal)</b>  [Z-Beta = 6.70971]  [Pearson Full 0.778302155, Pearson Zeroed 0.777666]</p>	 <p><b>Mafk_3106.2_PWM_6.txt (K-afal)</b></p>	Mafk_3106.2_combined_PWM_6.txt (K-afal) No PWM Available
 <p><b>Mafk_3106.2_PWM_7.txt (K-afal)</b>  [Z-Beta = 5.63784]  [Pearson Full 0.778302155, Pearson Zeroed 0.777926]</p>	 <p><b>Mafk_3106.2_PWM_7.txt (K-afal)</b></p>	Mafk_3106.2_combined_PWM_7.txt (K-afal) No PWM Available



<p>Mafk_3106.2_PWM_8.txt (K-afal)  [Z-Beta = -4.76704]  [Pearson Full 0.778302155, Pearson Zeroed 0.778014]</p> 	<p>Mafk_3106.2_PWM_8.txt (K-afal)</p> 	<p>Mafk_3106.2_combined_PWM_8.txt (K-afal)  No PWM Available</p>
<p>Mafk_3106.2_PWM_9.txt (K-afal)  [Z-Beta = -3.06923]  [Pearson Full 0.778302155, Pearson Zeroed 0.778167]</p> 	<p>Mafk_3106.2_PWM_9.txt (K-afal)  [Z-Beta = 4.64301]  [Pearson Full 0.666662301, Pearson Zeroed 0.662683]</p> 	<p>Mafk_3106.2_combined_PWM_9.txt (K-afal)  No PWM Available</p>
<p>Mafk_3106.2_PWM_10.txt (Kafal)  No PWM Available</p>	<p>Mafk_3106.2_PWM_10.txt (Kafal)</p> 	<p>Mafk_3106.2_combined_PWM_10.txt (Kafal)  No PWM Available</p>
<p>Mafk_3106.2_PWM_11.txt (Kafal)  No PWM Available</p>	<p>Mafk_3106.2_PWM_11.txt (Kafal)</p> 	<p>Mafk_3106.2_combined_PWM_11.txt (Kafal)  No PWM Available</p>
<p>Mafk_3106.2_PWM_12.txt (Kafal)  No PWM Available</p>	<p>Mafk_3106.2_PWM_12.txt (Kafal)  [Z-Beta = 14.6293]  [Pearson Full 0.666662301, Pearson Zeroed 0.655009]</p> 	<p>Mafk_3106.2_combined_PWM_12.txt (Kafal)  No PWM Available</p>
<p>Mafk_3106.2_PWM_13.txt (Kafal)  No PWM Available</p>	<p>Mafk_3106.2_PWM_13.txt (Kafal)  [Z-Beta = 5.79906]  [Pearson Full 0.666662301, Pearson Zeroed 0.663627]</p> 	<p>Mafk_3106.2_combined_PWM_13.txt (Kafal)  No PWM Available</p>
<p>Mafk_3106.2_PWM_14.txt (Kafal)  No PWM Available</p>	<p>Mafk_3106.2_PWM_14.txt (Kafal)</p> 	<p>Mafk_3106.2_combined_PWM_14.txt (Kafal)  No PWM Available</p>
<p>Mafk_3106.2_PWM_15.txt (Kafal)  No PWM Available</p>	<p>Mafk_3106.2_PWM_15.txt (Kafal)</p> 	<p>Mafk_3106.2_combined_PWM_15.txt (Kafal)  No PWM Available</p>
<p>Mafk_3106.2_PWM_16.txt (Kafal)  No PWM Available</p>	<p>Mafk_3106.2_PWM_16.txt (Kafal)  [Z-Beta = 11.7158]  [Pearson Full 0.666662301, Pearson Zeroed 0.65837]</p> 	<p>Mafk_3106.2_combined_PWM_16.txt (Kafal)  No PWM Available</p>
<p>Mafk_3106.2_PWM_17.txt (Kafal)  No PWM Available</p>	<p>Mafk_3106.2_PWM_17.txt (Kafal)  [Z-Beta = 30.8202]  [Pearson Full 0.666662301, Pearson Zeroed 0.637141]</p> 	<p>Mafk_3106.2_combined_PWM_17.txt (Kafal)  No PWM Available</p>

<p>Mafk_3106.2_PWM_18.txt (Kafal) No PWM Available</p>	<p>Mafk_3106.2_PWM_18.txt (Kafal)</p> 	<p>Mafk_3106.2_combined_PWM_18.txt (Kafal) No PWM Available</p>
<p>Mafk_3106.2_PWM_19.txt (Kafal) No PWM Available</p>	<p>Mafk_3106.2_PWM_19.txt (Kafal)</p> 	<p>Mafk_3106.2_combined_PWM_19.txt (Kafal) No PWM Available</p>
<p>Mafk_3106.2_PWM_20.txt (Kafal) No PWM Available</p>	<p>Mafk_3106.2_PWM_20.txt (Kafal)</p> 	<p>Mafk_3106.2_combined_PWM_20.txt (Kafal) No PWM Available</p>
<p>Mafk_3106.2_PWM_21.txt (Kafal) No PWM Available</p>	<p>Mafk_3106.2_PWM_21.txt (Kafal)</p> 	<p>Mafk_3106.2_combined_PWM_21.txt (Kafal) No PWM Available</p>
<p>Mafk_3106.2_PWM_22.txt (Kafal) No PWM Available</p>	<p>Mafk_3106.2_PWM_22.txt (Kafal)</p> 	<p>Mafk_3106.2_combined_PWM_22.txt (Kafal) No PWM Available</p>

# Max\_3863.1



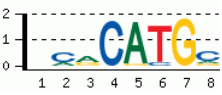

Array Version 1	Array Version 2	Combined
Max_3863.1_v1_prim_auc.txt (SW) 	Max_3863.1_v2_prim_auc.txt (SW) 	Max_3863.1_combined_prim_auc.txt (SW) No PWM Available
Max_3863.1_v1_prim_ic.txt (SW) 	Max_3863.1_v2_prim_ic.txt (SW) 	Max_3863.1_combined_prim_ic_trimmed.txt (SW) 
Max_3863.1_v1_prim_untrimmed.txt (SW) 	Max_3863.1_v2_prim_untrimmed.txt (SW) 	Max_3863.1_combined_prim_untrimmed.txt (SW) 
Max_3863.1_v1_sec_auc.txt (SW) 	Max_3863.1_v2_sec_auc.txt (SW) 	Max_3863.1_combined_sec_auc.txt (SW) 
Max_3863.1_v1_sec_ic.txt (SW) 	Max_3863.1_v2_sec_ic.txt (SW) 	Max_3863.1_combined_sec_ic_trimmed.txt (SW) 
Max_3863.1_v1_sec_untrimmed.txt (SW) 	Max_3863.1_v2_sec_untrimmed.txt (SW) 	Max_3863.1_combined_sec_untrimmed.txt (SW) 
Max_3863.1_v1_prim_trimmed.txt (RM8) 	Max_3863.1_v2_prim_trimmed.txt (RM8) 	Max_3863.1_RM8_combined_prim_trimmed.txt (RM8) No PWM Available
Max_3863.1_v1_sec_trimmed.txt (RM8) 	Max_3863.1_v2_sec_trimmed.txt (RM8) 	Max_3863.1_RM8_combined_sec_trimmed.txt (RM8) No PWM Available
Max_3863.1_v1_prim_untrimmed.txt (RM8) 	Max_3863.1_v2_prim_untrimmed.txt (RM8) 	Max_3863.1_RM8_combined_prim_untrimmed.txt (RM8) No PWM Available
Max_3863.1_v1_sec_untrimmed.txt (RM8) 	Max_3863.1_v2_sec_untrimmed.txt (RM8) 	Max_3863.1_RM8_combined_sec_untrimmed.txt (RM8) No PWM Available
Max_3863.1_v1_RM35_prim_trimmed.txt (RM35) 	Max_3863.1_v2_RM35_prim_trimmed.txt (RM35) [Z-Beta = 109.02] [Pearson Full 0.726892138, Pearson Zeroed 0.685212] 	Max_3863.1_RM_combined_prim_trimmed.txt (RM35) 
Max_3863.1_v1_RM35_sec_trimmed.txt (RM35) 	Max_3863.1_v2_RM35_sec_trimmed.txt (RM35) 	Max_3863.1_RM_combined_sec_trimmed.txt (RM35)

<b>Max_3863.1_v1_RM35_prim_untrimmed.txt (RM35)</b> [Z-Beta = 32.6105] [Pearson Full 0.656835836, Pearson Zeroed 0.635247]	<b>Max_3863.1_v2_RM35_prim_untrimmed.txt (RM35)</b> 	<b>Max_3863.1_RM_combined_prim_untrimmed.txt (RM35)</b> 
<b>Max_3863.1_v1_RM35_sec_untrimmed.txt (RM35)</b> 	<b>Max_3863.1_v2_RM35_sec_untrimmed.txt (RM35)</b> 	<b>Max_3863.1_RM_combined_sec_untrimmed.txt (RM35)</b> 
<b>Max_3863.1_PWM_1.txt (K-afal)</b> [Z-Beta = 99.6099] [Pearson Full 0.656835836, Pearson Zeroed 0.615325]	<b>Max_3863.1_PWM_1.txt (K-afal)</b> 	<b>Max_3863.1_combined_PWM_1.txt (K-afal)</b> [Z-Beta = 359.501] [Pearson Full 0.77130854, Pearson Zeroed NaN]
<b>Max_3863.1_PWM_2.txt (K-afal)</b> 	<b>Max_3863.1_PWM_2.txt (K-afal)</b> [Z-Beta = 55.5123] [Pearson Full 0.726892138, Pearson Zeroed 0.709492]	<b>Max_3863.1_combined_PWM_2.txt (K-afal)</b> No PWM Available
<b>Max_3863.1_PWM_3.txt (K-afal)</b> 	<b>Max_3863.1_PWM_3.txt (K-afal)</b> [Z-Beta = 237.362] [Pearson Full 0.726892138, Pearson Zeroed 0.643289]	<b>Max_3863.1_combined_PWM_3.txt (K-afal)</b> No PWM Available
<b>Max_3863.1_PWM_4.txt (K-afal)</b> 	<b>Max_3863.1_PWM_4.txt (K-afal)</b> 	<b>Max_3863.1_combined_PWM_4.txt (K-afal)</b> No PWM Available
<b>Max_3863.1_PWM_5.txt (K-afal)</b> 	<b>Max_3863.1_PWM_5.txt (K-afal)</b> 	<b>Max_3863.1_combined_PWM_5.txt (K-afal)</b> No PWM Available
<b>Max_3863.1_PWM_6.txt (K-afal)</b> 	<b>Max_3863.1_PWM_6.txt (K-afal)</b> No PWM Available	<b>Max_3863.1_combined_PWM_6.txt (K-afal)</b> No PWM Available
<b>Max_3863.1_PWM_7.txt (K-afal)</b> 	<b>Max_3863.1_PWM_7.txt (K-afal)</b> No PWM Available	<b>Max_3863.1_combined_PWM_7.txt (K-afal)</b> No PWM Available

# Max\_3864.1










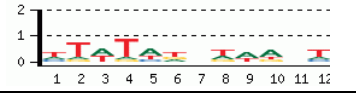
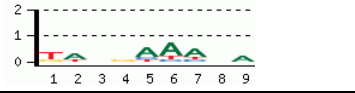

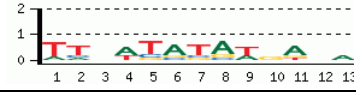
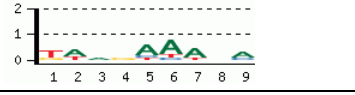

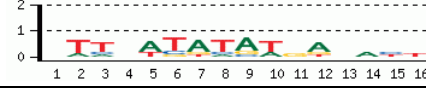
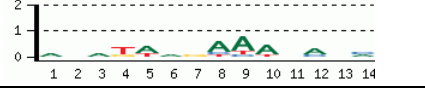
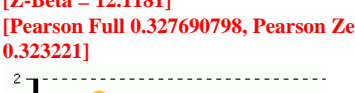
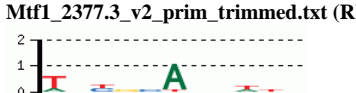
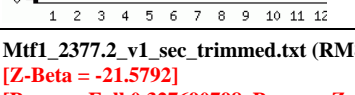
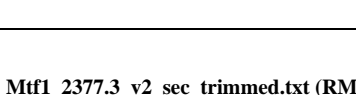
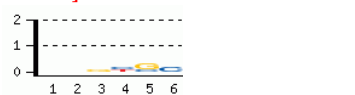
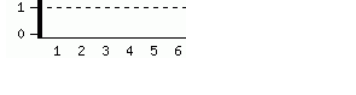
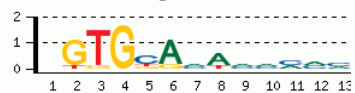
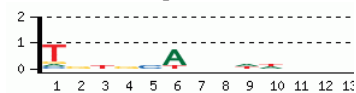
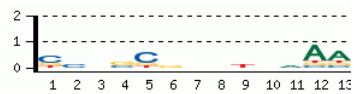
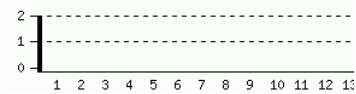
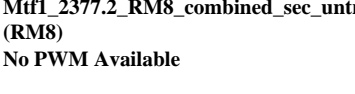
Array Version 1	Array Version 2	Combined
<p>Max_3864.1_v1_prim_auc.txt (SW)</p>	<p>Max_3864.1_v2_prim_auc.txt (SW)</p>	<p>Max_3864.1_combined_prim_auc.txt (SW)</p> <p>No PWM Available</p>
<p>Max_3864.1_v1_prim_ic.txt (SW)</p>	<p>Max_3864.1_v2_prim_ic.txt (SW)</p>	<p>Max_3864.1_combined_prim_ic_trimmed.txt (SW)</p>
<p>Max_3864.1_v1_prim_untrimmed.txt (SW)</p>	<p>Max_3864.1_v2_prim_untrimmed.txt (SW)</p> <p>[Z-Beta = 26.7056] [Pearson Full 0.564833381, Pearson Zeroed 0.548871]</p>	<p>Max_3864.1_combined_prim_untrimmed.txt (SW)</p>
<p>Max_3864.1_v1_sec_auc.txt (SW)</p>	<p>Max_3864.1_v2_sec_auc.txt (SW)</p>	<p>Max_3864.1_combined_sec_auc.txt (SW)</p>
<p>Max_3864.1_v1_sec_ic.txt (SW)</p>	<p>Max_3864.1_v2_sec_ic.txt (SW)</p>	<p>Max_3864.1_combined_sec_ic_trimmed.txt (SW)</p>
<p>Max_3864.1_v1_sec_untrimmed.txt (SW)</p>	<p>Max_3864.1_v2_sec_untrimmed.txt (SW)</p>	<p>Max_3864.1_combined_sec_untrimmed.txt (SW)</p>
<p>Max_3864.1_v1_prim_trimmed.txt (RM8)</p>	<p>Max_3864.1_v2_prim_trimmed.txt (RM8)</p>	<p>Max_3864.1_RM8_combined_prim_trimmed.txt (RM8)</p> <p>No PWM Available</p>
<p>Max_3864.1_v1_sec_trimmed.txt (RM8)</p>	<p>Max_3864.1_v2_sec_trimmed.txt (RM8)</p>	<p>Max_3864.1_RM8_combined_sec_trimmed.txt (RM8)</p> <p>No PWM Available</p>
<p>Max_3864.1_v1_prim_untrimmed.txt (RM8)</p>	<p>Max_3864.1_v2_prim_untrimmed.txt (RM8)</p> <p>[Z-Beta = 26.7056] [Pearson Full 0.564833381, Pearson Zeroed 0.548871]</p>	<p>Max_3864.1_RM8_combined_prim_untrimmed.txt (RM8)</p> <p>No PWM Available</p>
<p>Max_3864.1_v1_sec_untrimmed.txt (RM8)</p>	<p>Max_3864.1_v2_sec_untrimmed.txt (RM8)</p>	<p>Max_3864.1_RM8_combined_sec_untrimmed.txt (RM8)</p> <p>No PWM Available</p>
<p>Max_3864.1_v1_RM35_prim_trimmed.txt (RM35)</p> <p>[Z-Beta = 227.359] [Pearson Full 0.66589908, Pearson Zeroed NaN]</p>	<p>Max_3864.1_v2_RM35_prim_trimmed.txt (RM35)</p>	<p>Max_3864.1_RM_combined_prim_trimmed.txt (RM35)</p> <p>[Z-Beta = 155.228] [Pearson Full 0.72559958, Pearson Zeroed NaN]</p>

Max_3864.1_v1_RM35_sec_trimmed.txt (RM35) 	Max_3864.1_v2_RM35_sec_trimmed.txt (RM35) 	Max_3864.1_RM_combined_sec_trimmed.txt (RM35) 
Max_3864.1_v1_RM35_prim_untrimmed.txt (RM35) 	Max_3864.1_v2_RM35_prim_untrimmed.txt (RM35) [Z-Beta = 34.0032] [Pearson Full 0.564833381, Pearson Zeroed 0.547595] 	Max_3864.1_RM_combined_prim_untrimmed.txt (RM35) 
Max_3864.1_v1_RM35_sec_untrimmed.txt (RM35) 	Max_3864.1_v2_RM35_sec_untrimmed.txt (RM35) 	Max_3864.1_RM_combined_sec_untrimmed.txt (RM35) 
Max_3864.1_PWM_10.txt (K-afal) 	Max_3864.1_PWM_10.txt (K-afal) No PWM Available	Max_3864.1_combined_PWM_10.txt (K-afal) No PWM Available
Max_3864.1_PWM_11.txt (K-afal) 	Max_3864.1_PWM_11.txt (K-afal) No PWM Available	Max_3864.1_combined_PWM_11.txt (K-afal) No PWM Available
Max_3864.1_PWM_12.txt (K-afal) 	Max_3864.1_PWM_12.txt (K-afal) No PWM Available	Max_3864.1_combined_PWM_12.txt (K-afal) No PWM Available
Max_3864.1_PWM_1.txt (K-afal) 	Max_3864.1_PWM_1.txt (K-afal) 	Max_3864.1_combined_PWM_1.txt (K-afal) 
Max_3864.1_PWM_2.txt (K-afal) 	Max_3864.1_PWM_2.txt (K-afal) 	Max_3864.1_combined_PWM_2.txt (K-afal) No PWM Available
Max_3864.1_PWM_3.txt (K-afal) 	Max_3864.1_PWM_3.txt (K-afal) 	Max_3864.1_combined_PWM_3.txt (K-afal) No PWM Available
Max_3864.1_PWM_4.txt (K-afal) 	Max_3864.1_PWM_4.txt (K-afal) 	Max_3864.1_combined_PWM_4.txt (K-afal) No PWM Available
Max_3864.1_PWM_5.txt (K-afal) 	Max_3864.1_PWM_5.txt (K-afal) 	Max_3864.1_combined_PWM_5.txt (K-afal) No PWM Available
Max_3864.1_PWM_6.txt (K-afal) 	Max_3864.1_PWM_6.txt (K-afal) 	Max_3864.1_combined_PWM_6.txt (K-afal) No PWM Available
Max_3864.1_PWM_7.txt (K-afal)	Max_3864.1_PWM_7.txt (K-afal)	Max_3864.1_combined_PWM_7.txt (K-afal) No PWM Available

		
<p><b>Max_3864.1_PWM_8.txt (K-afal)</b></p> 	<p><b>Max_3864.1_PWM_8.txt (K-afal)</b> No PWM Available</p>	<p><b>Max_3864.1_combined_PWM_8.txt (K-afal)</b> No PWM Available</p>
<p><b>Max_3864.1_PWM_9.txt (K-afal)</b></p> 	<p><b>Max_3864.1_PWM_9.txt (K-afal)</b> No PWM Available</p>	<p><b>Max_3864.1_combined_PWM_9.txt (K-afal)</b> No PWM Available</p>





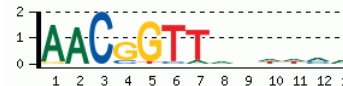
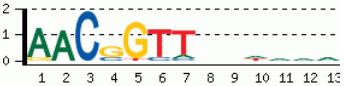
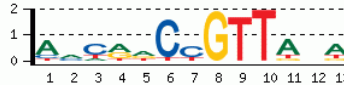


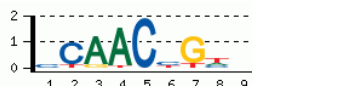


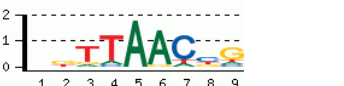




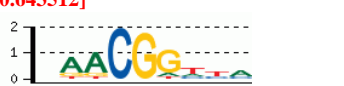

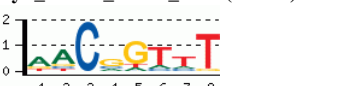



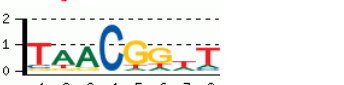

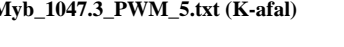

# Mtf1\_2377.3

Array Version 1	Array Version 2	Combined
Mtf1_2377.2_v1_prim_auc.txt (SW) 	Mtf1_2377.3_v2_prim_auc.txt (SW) 	Mtf1_2377.3_combined_prim_auc.txt (SW) No PWM Available
Mtf1_2377.2_v1_prim_ic.txt (SW) [Z-Beta = 41.9695] [Pearson Full 0.327690798, Pearson Zeroed 0.247334] 	Mtf1_2377.3_v2_prim_ic.txt (SW) 	Mtf1_2377.3_combined_prim_ic_trimmed.txt (SW) [Z-Beta = 9.27732] [Pearson Full 0.490329981, Pearson Zeroed 0.469125] 
Mtf1_2377.2_v1_prim_untrimmed.txt (SW) 	Mtf1_2377.3_v2_prim_untrimmed.txt (SW) 	Mtf1_2377.2_combined_prim_untrimmed.txt (SW) 
Mtf1_2377.2_v1_sec_auc.txt (SW) 	Mtf1_2377.3_v2_sec_auc.txt (SW) 	Mtf1_2377.2_combined_sec_auc.txt (SW) 
Mtf1_2377.2_v1_sec_ic.txt (SW) 	Mtf1_2377.3_v2_sec_ic.txt (SW) 	Mtf1_2377.2_combined_sec_ic_trimmed.txt (SW) 
Mtf1_2377.2_v1_sec_untrimmed.txt (SW) 	Mtf1_2377.3_v2_sec_untrimmed.txt (SW) 	Mtf1_2377.2_combined_sec_untrimmed.txt (SW) 
Mtf1_2377.2_v1_prim_trimmed.txt (RM8) [Z-Beta = 12.1181] [Pearson Full 0.327690798, Pearson Zeroed 0.323221] 	Mtf1_2377.3_v2_prim_trimmed.txt (RM8) 	Mtf1_2377.2_RM8_combined_prim_trimmed.txt (RM8) No PWM Available
Mtf1_2377.2_v1_sec_trimmed.txt (RM8) [Z-Beta = -21.5792] [Pearson Full 0.327690798, Pearson Zeroed 0.26964] 	Mtf1_2377.3_v2_sec_trimmed.txt (RM8) 	Mtf1_2377.2_RM8_combined_sec_trimmed.txt (RM8) No PWM Available
Mtf1_2377.2_v1_prim_untrimmed.txt (RM8) 	Mtf1_2377.3_v2_prim_untrimmed.txt (RM8) 	Mtf1_2377.2_RM8_combined_prim_untrimmed.txt (RM8) No PWM Available
Mtf1_2377.2_v1_sec_untrimmed.txt (RM8) 	Mtf1_2377.3_v2_sec_untrimmed.txt (RM8) 	Mtf1_2377.2_RM8_combined_sec_untrimmed.txt (RM8) No PWM Available
Mtf1_2377.2_v1_RM35_prim_trimmed.txt (RM35) 	Mtf1_2377.3_v2_RM35_prim_trimmed.txt (RM35) 	Mtf1_2377.2_RM_combined_prim_trimmed.txt (RM35) [Z-Beta = 14.9939] [Pearson Full 0.490329981, Pearson Zeroed 0.463544] 

<p>Mtf1_2377.2_v1_RM35_sec_trimmed.txt (RM35)</p>	<p>Mtf1_2377.3_v2_RM35_sec_trimmed.txt (RM35)</p>	<p>Mtf1_2377.2_RM_combined_sec_trimmed.txt (RM35)</p>
<p>Mtf1_2377.2_v1_RM35_prim_untrimmed.txt (RM35)</p>	<p>Mtf1_2377.3_v2_RM35_prim_untrimmed.txt (RM35)</p>	<p>Mtf1_2377.2_RM_combined_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = 0.303087] [Pearson Full 0.490329981, Pearson Zeroed 0.489727]</p>
<p>Mtf1_2377.2_v1_RM35_sec_untrimmed.txt (RM35)</p>	<p>Mtf1_2377.3_v2_RM35_sec_untrimmed.txt (RM35)</p>	<p>Mtf1_2377.2_RM_combined_sec_untrimmed.txt (RM35)</p>
<p>Mtf1_2377.2_PWM_1.txt (K-afal)</p> <p>[Z-Beta = 8.3172] [Pearson Full 0.327690798, Pearson Zeroed 0.321279]</p>	<p>Mtf1_2377.3_PWM_1.txt (K-afal)</p>	<p>Mtf1_2377.2_combined_PWM_1.txt (K-afal)</p>
<p>Mtf1_2377.2_PWM_2.txt (K-afal)</p>	<p>Mtf1_2377.3_PWM_2.txt (K-afal)</p>	<p>Mtf1_2377.2_combined_PWM_2.txt (K-afal)</p>
<p>Mtf1_2377.2_PWM_3.txt (K-afal)</p>	<p>Mtf1_2377.3_PWM_3.txt (K-afal)</p>	<p>Mtf1_2377.2_combined_PWM_3.txt (K-afal)</p>
<p>Mtf1_2377.2_PWM_4.txt (K-afal)</p> <p>[Z-Beta = 35.6374] [Pearson Full 0.327690798, Pearson Zeroed 0.269815]</p>	<p>Mtf1_2377.3_PWM_4.txt (K-afal)</p>	<p>Mtf1_2377.2_combined_PWM_4.txt (K-afal)</p>

# Myb\_1047.3

Array Version 1	Array Version 2	Combined
Myb_1047.3_v1_prim_auc.txt (SW) 	Myb_1047.3_v2_prim_auc.txt (SW) 	Myb_1047.3_combined_prim_auc.txt (SW) 
Myb_1047.3_v1_prim_ic.txt (SW) 	Myb_1047.3_v2_prim_ic.txt (SW) 	Myb_1047.3_combined_prim_ic_trimmed.txt (SW) 
Myb_1047.3_v1_prim_untrimmed.txt (SW) 	Myb_1047.3_v2_prim_untrimmed.txt (SW) 	Myb_1047.3_combined_prim_untrimmed.txt (SW) 
Myb_1047.3_v1_sec_auc.txt (SW) 	Myb_1047.3_v2_sec_auc.txt (SW) 	Myb_1047.3_combined_sec_auc.txt (SW) 
Myb_1047.3_v1_sec_ic.txt (SW) 	Myb_1047.3_v2_sec_ic.txt (SW) 	Myb_1047.3_combined_sec_ic_trimmed.txt (SW) 
Myb_1047.3_v1_sec_untrimmed.txt (SW) 	Myb_1047.3_v2_sec_untrimmed.txt (SW) 	Myb_1047.3_combined_sec_untrimmed.txt (SW) 
Myb_1047.3_v1_prim_trimmed.txt (RM8) 	Myb_1047.3_v2_prim_trimmed.txt (RM8) 	Myb_1047.3_RM8_combined_prim_trimmed.txt (RM8) No PWM Available
Myb_1047.3_v1_sec_trimmed.txt (RM8) 	Myb_1047.3_v2_sec_trimmed.txt (RM8) 	Myb_1047.3_RM8_combined_sec_trimmed.txt (RM8) No PWM Available
Myb_1047.3_v1_prim_untrimmed.txt (RM8) 	Myb_1047.3_v2_prim_untrimmed.txt (RM8) 	Myb_1047.3_RM8_combined_prim_untrimmed.txt (RM8) No PWM Available
Myb_1047.3_v1_sec_untrimmed.txt (RM8) 	Myb_1047.3_v2_sec_untrimmed.txt (RM8) 	Myb_1047.3_RM8_combined_sec_untrimmed.txt (RM8) No PWM Available
Myb_1047.3_v1_RM35_prim_trimmed.txt (RM35) 	Myb_1047.3_v2_RM35_prim_trimmed.txt (RM35) 	Myb_1047.3_RM_combined_prim_trimmed.txt (RM35) 
Myb_1047.3_v1_RM35_sec_trimmed.txt (RM35) 	Myb_1047.3_v2_RM35_sec_trimmed.txt (RM35) 	Myb_1047.3_RM_combined_sec_trimmed.txt (RM35) 


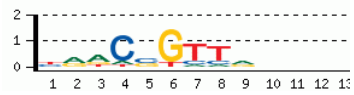


<p>Myb_1047.3_v1_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Myb_1047.3_v2_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Myb_1047.3_RM_combined_prim_untrimmed.txt (RM35)</p> 
<p>Myb_1047.3_v1_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Myb_1047.3_v2_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Myb_1047.3_RM_combined_sec_untrimmed.txt (RM35)</p> 
<p>Myb_1047.3_PWM_10.txt (K-afal) [Z-Beta = 246.288] [Pearson Full 0.701125881, Pearson Zeroed 0.667916]</p> 	<p>Myb_1047.3_PWM_10.txt (K-afal) [Z-Beta = 99.3267] [Pearson Full 0.649041509, Pearson Zeroed 0.587452]</p> 	<p>Myb_1047.3_combined_PWM_10.txt (K-afal) No PWM Available</p>
<p>Myb_1047.3_PWM_11.txt (K-afal) [Z-Beta = 289.402] [Pearson Full 0.701125881, Pearson Zeroed 0.656339]</p> 	<p>Myb_1047.3_PWM_11.txt (K-afal) [Z-Beta = 31.5273] [Pearson Full 0.649041509, Pearson Zeroed 0.645473]</p> 	<p>Myb_1047.3_combined_PWM_11.txt (K-afal) No PWM Available</p>
<p>Myb_1047.3_PWM_12.txt (K-afal)</p> 	<p>Myb_1047.3_PWM_12.txt (K-afal)</p> 	<p>Myb_1047.3_combined_PWM_12.txt (K-afal) No PWM Available</p>
<p>Myb_1047.3_PWM_13.txt (K-afal) [Z-Beta = 23.8313] [Pearson Full 0.701125881, Pearson Zeroed 0.696157]</p> 	<p>Myb_1047.3_PWM_13.txt (K-afal)</p> 	<p>Myb_1047.3_combined_PWM_13.txt (K-afal) No PWM Available</p>
<p>Myb_1047.3_PWM_1.txt (K-afal) [Z-Beta = 362.865] [Pearson Full 0.701125881, Pearson Zeroed 0.653944]</p> 	<p>Myb_1047.3_PWM_1.txt (K-afal) [Z-Beta = 32.3178] [Pearson Full 0.649041509, Pearson Zeroed 0.643512]</p> 	<p>Myb_1047.3_combined_PWM_1.txt (K-afal) [Z-Beta = 108.193] [Pearson Full 0.415215722, Pearson Zeroed NaN]</p> 
<p>Myb_1047.3_PWM_2.txt (K-afal)</p> 	<p>Myb_1047.3_PWM_2.txt (K-afal)</p> 	<p>Myb_1047.3_combined_PWM_2.txt (K-afal) No PWM Available</p>
<p>Myb_1047.3_PWM_3.txt (K-afal)</p> 	<p>Myb_1047.3_PWM_3.txt (K-afal)</p> 	<p>Myb_1047.3_combined_PWM_3.txt (K-afal) No PWM Available</p>
<p>Myb_1047.3_PWM_4.txt (K-afal) [Z-Beta = 87.9494] [Pearson Full 0.701125881, Pearson Zeroed 0.69944]</p> 	<p>Myb_1047.3_PWM_4.txt (K-afal)</p> 	<p>Myb_1047.3_combined_PWM_4.txt (K-afal) No PWM Available</p>
<p>Myb_1047.3_PWM_5.txt (K-afal)</p> 	<p>Myb_1047.3_PWM_5.txt (K-afal) [Z-Beta = 38.0928] [Pearson Full 0.649041509, Pearson Zeroed]</p> 	<p>Myb_1047.3_combined_PWM_5.txt (K-afal) No PWM Available</p>

	<p><b>0.631532]</b></p>	
<p>Myb_1047.3_PWM_6.txt (K-afal)  [Z-Beta = 152.153]  [Pearson Full 0.701125881, Pearson Zeroed 0.692314]</p>	<p>Myb_1047.3_PWM_6.txt (K-afal)  [Z-Beta = 49.9522]  [Pearson Full 0.649041509, Pearson Zeroed 0.64624]</p>	<p>Myb_1047.3_combined_PWM_6.txt (K-afal)  No PWM Available</p>
<p>Myb_1047.3_PWM_7.txt (K-afal)  [Z-Beta = 248.445]  [Pearson Full 0.701125881, Pearson Zeroed 0.667552]</p>	<p>Myb_1047.3_PWM_7.txt (K-afal)  [Z-Beta = 13.3831]  [Pearson Full 0.649041509, Pearson Zeroed 0.642508]</p>	<p>Myb_1047.3_combined_PWM_7.txt (K-afal)  No PWM Available</p>
<p>Myb_1047.3_PWM_8.txt (K-afal)  [Z-Beta = 58.273]  [Pearson Full 0.701125881, Pearson Zeroed 0.698513]</p>	<p>Myb_1047.3_PWM_8.txt (K-afal)</p>	<p>Myb_1047.3_combined_PWM_8.txt (K-afal)  No PWM Available</p>
<p>Myb_1047.3_PWM_9.txt (K-afal)</p>	<p>Myb_1047.3_PWM_9.txt (K-afal)  [Z-Beta = 269.979]  [Pearson Full 0.649041509, Pearson Zeroed 0.573341]</p>	<p>Myb_1047.3_combined_PWM_9.txt (K-afal)  No PWM Available</p>
<p>Myb_1047.3_PWM_10.txt (Kafal)  No PWM Available</p>	<p>Myb_1047.3_PWM_10.txt (Kafal)  [Z-Beta = 99.3267]  [Pearson Full 0.649041509, Pearson Zeroed 0.587452]</p>	<p>Myb_1047.3_combined_PWM_10.txt (Kafal)  No PWM Available</p>
<p>Myb_1047.3_PWM_11.txt (Kafal)  No PWM Available</p>	<p>Myb_1047.3_PWM_11.txt (Kafal)  [Z-Beta = 31.5273]  [Pearson Full 0.649041509, Pearson Zeroed 0.645473]</p>	<p>Myb_1047.3_combined_PWM_11.txt (Kafal)  No PWM Available</p>
<p>Myb_1047.3_PWM_12.txt (Kafal)  No PWM Available</p>	<p>Myb_1047.3_PWM_12.txt (Kafal)</p>	<p>Myb_1047.3_combined_PWM_12.txt (Kafal)  No PWM Available</p>
<p>Myb_1047.3_PWM_13.txt (Kafal)  No PWM Available</p>	<p>Myb_1047.3_PWM_13.txt (Kafal)</p>	<p>Myb_1047.3_combined_PWM_13.txt (Kafal)  No PWM Available</p>
<p>Myb_1047.3_PWM_14.txt (Kafal)  No PWM Available</p>	<p>Myb_1047.3_PWM_14.txt (Kafal)  [Z-Beta = 74.4015]  [Pearson Full 0.649041509, Pearson Zeroed 0.632425]</p>	<p>Myb_1047.3_combined_PWM_14.txt (Kafal)  No PWM Available</p>

# Mybl1\_1717.2

Array Version 1	Array Version 2	Combined
<p>Mybl1_1717.2_v1_prim_auc.txt (SW)</p>	<p>Mybl1_1717.2_v2_prim_auc.txt (SW)</p>	<p>Mybl1_1717.2_combined_prim_auc.txt (SW)</p>
<p>Mybl1_1717.2_v1_prim_ic.txt (SW)</p>	<p>Mybl1_1717.2_v2_prim_ic.txt (SW)</p>	<p>Mybl1_1717.2_combined_prim_ic_trimmed.txt (SW)</p>
<p>Mybl1_1717.2_v1_prim_untrimmed.txt (SW)</p>	<p>Mybl1_1717.2_v2_prim_untrimmed.txt (SW)</p>	<p>Mybl1_1717.2_combined_prim_untrimmed.txt (SW)</p>
<p>Mybl1_1717.2_v1_sec_auc.txt (SW)</p>	<p>Mybl1_1717.2_v2_sec_auc.txt (SW)</p>	<p>Mybl1_1717.2_combined_sec_auc.txt (SW)</p>
<p>Mybl1_1717.2_v1_sec_ic.txt (SW)</p>	<p>Mybl1_1717.2_v2_sec_ic.txt (SW)</p>	<p>Mybl1_1717.2_combined_sec_ic_trimmed.txt (SW)</p>
<p>Mybl1_1717.2_v1_sec_untrimmed.txt (SW)</p>	<p>Mybl1_1717.2_v2_sec_untrimmed.txt (SW)</p>	<p>Mybl1_1717.2_combined_sec_untrimmed.txt (SW)</p>
<p>Mybl1_1717.2_v1_prim_trimmed.txt (RM8)</p>	<p>Mybl1_1717.2_v2_prim_trimmed.txt (RM8)</p>	<p>Mybl1_1717.2_RM8_combined_prim_trimmed.txt (RM8)</p> <p>No PWM Available</p>
<p>Mybl1_1717.2_v1_sec_trimmed.txt (RM8)</p>	<p>Mybl1_1717.2_v2_sec_trimmed.txt (RM8)</p>	<p>Mybl1_1717.2_RM8_combined_sec_trimmed.txt (RM8)</p> <p>No PWM Available</p>
<p>Mybl1_1717.2_v1_prim_untrimmed.txt (RM8)</p>	<p>Mybl1_1717.2_v2_prim_untrimmed.txt (RM8)</p>	<p>Mybl1_1717.2_RM8_combined_prim_untrimmed.txt (RM8)</p> <p>No PWM Available</p>
<p>Mybl1_1717.2_v1_sec_untrimmed.txt (RM8)</p>	<p>Mybl1_1717.2_v2_sec_untrimmed.txt (RM8)</p>	<p>Mybl1_1717.2_RM8_combined_sec_untrimmed.txt (RM8)</p> <p>No PWM Available</p>
<p>Mybl1_1717.2_v1_RM35_prim_trimmed.txt (RM35)</p>	<p>Mybl1_1717.2_v2_RM35_prim_trimmed.txt (RM35)</p>	<p>Mybl1_1717.2_RM_combined_prim_trimmed.txt (RM35)</p> <p>[Z-Beta = 159.484] [Pearson Full 0.731253883, Pearson Zeroed 0.687331]</p>
<p>Mybl1_1717.2_v1_RM35_sec_trimmed.txt (RM35)</p>	<p>Mybl1_1717.2_v2_RM35_sec_trimmed.txt (RM35)</p>	<p>Mybl1_1717.2_RM_combined_sec_trimmed.txt (RM35)</p>

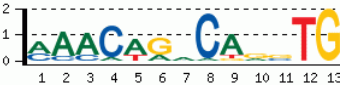

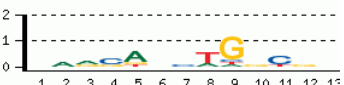

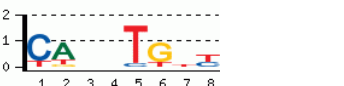
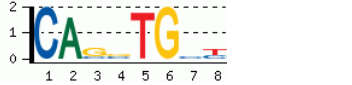
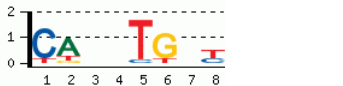



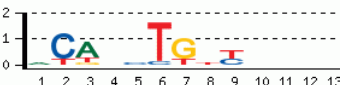

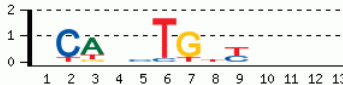
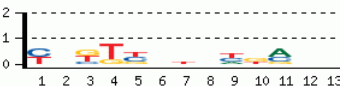
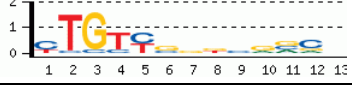
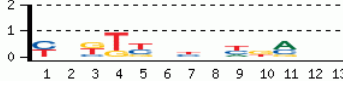



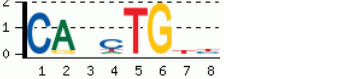







 <p>Mybl1_1717.2_v1_RM35_prim_untrimmed.txt (RM35)  [Z-Beta = 219.767]  [Pearson Full 0.636470571, Pearson Zeroed 0.560682]</p>	 <p>Mybl1_1717.2_v2_RM35_prim_untrimmed.txt (RM35)  [Z-Beta = 44.5743]  [Pearson Full 0.725361631, Pearson Zeroed 0.716815]</p>	 <p>Mybl1_1717.2_RM_combined_prim_untrimmed.txt (RM35)  [Z-Beta = 103.947]  [Pearson Full 0.731253883, Pearson Zeroed 0.716993]</p>
 <p>Mybl1_1717.2_v1_RM35_sec_untrimmed.txt (RM35)</p>	 <p>Mybl1_1717.2_v2_RM35_sec_untrimmed.txt (RM35)  [Z-Beta = 455.416]  [Pearson Full 0.725361631, Pearson Zeroed 0.602461]</p>	 <p>Mybl1_1717.2_RM_combined_sec_untrimmed.txt (RM35)  [Z-Beta = 293.045]  [Pearson Full 0.731253883, Pearson Zeroed 0.558502]</p>
 <p>Mybl1_1717.2_PWM_1.txt (K-afal)</p>	 <p>Mybl1_1717.2_PWM_1.txt (K-afal)  [Z-Beta = 12.4598]  [Pearson Full 0.725361631, Pearson Zeroed 0.722441]</p>	 <p>Mybl1_1717.2_combined_PWM_1.txt (K-afal)</p>
 <p>Mybl1_1717.2_PWM_2.txt (K-afal)</p>	 <p>Mybl1_1717.2_PWM_2.txt (K-afal)  [Z-Beta = 82.4985]  [Pearson Full 0.725361631, Pearson Zeroed 0.690114]</p>	 <p>Mybl1_1717.2_combined_PWM_2.txt (K-afal)</p>
 <p>Mybl1_1717.2_PWM_3.txt (K-afal)</p>	 <p>Mybl1_1717.2_PWM_3.txt (K-afal)</p>	 <p>Mybl1_1717.2_combined_PWM_3.txt (K-afal)</p>
 <p>Mybl1_1717.2_PWM_4.txt (K-afal)  [Z-Beta = 187.855]  [Pearson Full 0.636470571, Pearson Zeroed 0.570852]</p>	 <p>Mybl1_1717.2_PWM_4.txt (K-afal)</p>	 <p>Mybl1_1717.2_combined_PWM_4.txt (K-afal)</p>
 <p>Mybl1_1717.2_PWM_5.txt (K-afal)</p>	 <p>Mybl1_1717.2_PWM_5.txt (K-afal)  [Z-Beta = 113.897]  [Pearson Full 0.725361631, Pearson Zeroed 0.709373]</p>	 <p>Mybl1_1717.2_combined_PWM_5.txt (K-afal)</p>









# Myf6\_3824.2

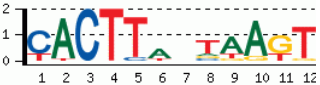
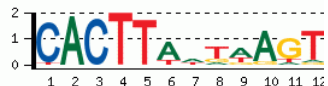










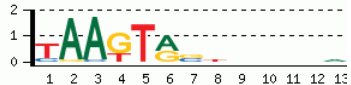




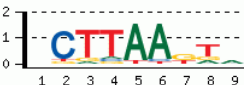
Array Version 1	Array Version 2	Combined
<p>Myf6_3824.2_v1_prim_auc.txt (SW) [Z-Beta = -2868.46] [Pearson Full 0.332114212, Pearson Zeroed 0.159378]</p>	<p>Myf6_3824.2_v2_prim_auc.txt (SW)</p>	<p>Myf6_3824.2_combined_prim_auc.txt (SW)</p>
<p>Myf6_3824.2_v1_prim_ic.txt (SW) [Z-Beta = 2893.54] [Pearson Full 0.332114212, Pearson Zeroed 0.136089]</p>	<p>Myf6_3824.2_v2_prim_ic.txt (SW)</p>	<p>Myf6_3824.2_combined_prim_ic_trimmed.txt (SW)</p>
<p>Myf6_3824.2_v1_prim_untrimmed.txt (SW) [Z-Beta = -4.66264] [Pearson Full 0.332114212, Pearson Zeroed 0.331897]</p>	<p>Myf6_3824.2_v2_prim_untrimmed.txt (SW)</p>	<p>Myf6_3824.2_combined_prim_untrimmed.txt (SW)</p>
<p>Myf6_3824.2_v1_sec_auc.txt (SW) [Z-Beta = 11.4376] [Pearson Full 0.332114212, Pearson Zeroed 0.329823]</p>	<p>Myf6_3824.2_v2_sec_auc.txt (SW)</p>	<p>Myf6_3824.2_combined_sec_auc.txt (SW)</p>
<p>Myf6_3824.2_v1_sec_ic.txt (SW) [Z-Beta = -3.84974] [Pearson Full 0.332114212, Pearson Zeroed 0.331851]</p>	<p>Myf6_3824.2_v2_sec_ic.txt (SW)</p>	<p>Myf6_3824.2_combined_sec_ic_trimmed.txt (SW)</p>
<p>Myf6_3824.2_v1_sec_untrimmed.txt (SW) [Z-Beta = -2.14003] [Pearson Full 0.332114212, Pearson Zeroed 0.332038]</p>	<p>Myf6_3824.2_v2_sec_untrimmed.txt (SW) [Z-Beta = 57.7174] [Pearson Full 0.480221515, Pearson Zeroed 0.384213]</p>	<p>Myf6_3824.2_combined_sec_untrimmed.txt (SW) [Z-Beta = 11.5976] [Pearson Full 0.442015758, Pearson Zeroed 0.401532]</p>
<p>Myf6_3824.2_v1_prim_trimmed.txt (RM8) [Z-Beta = 7.50679] [Pearson Full 0.332114212, Pearson Zeroed 0.331515]</p>	<p>Myf6_3824.2_v2_prim_trimmed.txt (RM8)</p>	<p>Myf6_3824.2_RM8_combined_prim_trimmed.txt (RM8) No PWM Available</p>
<p>Myf6_3824.2_v1_sec_trimmed.txt (RM8) [Z-Beta = 20.4373] [Pearson Full 0.332114212, Pearson Zeroed 0.327038]</p>	<p>Myf6_3824.2_v2_sec_trimmed.txt (RM8)</p>	<p>Myf6_3824.2_RM8_combined_sec_trimmed.txt (RM8) No PWM Available</p>

<p>Myf6_3824.2_v1_prim_untrimmed.txt (RM8)  [Z-Beta = 24.505]  [Pearson Full 0.332114212, Pearson Zeroed 0.324683]</p> 	<p>Myf6_3824.2_v2_prim_untrimmed.txt (RM8)</p> 	<p>Myf6_3824.2_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Myf6_3824.2_v1_sec_untrimmed.txt (RM8)  [Z-Beta = 17.7383]  [Pearson Full 0.332114212, Pearson Zeroed 0.328893]</p> 	<p>Myf6_3824.2_v2_sec_untrimmed.txt (RM8)  [Z-Beta = 57.7174]  [Pearson Full 0.480221515, Pearson Zeroed 0.384213]</p> 	<p>Myf6_3824.2_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>
<p>Myf6_3824.2_v1_RM35_prim_trimmed.txt (RM35)  [Z-Beta = 20.5784]  [Pearson Full 0.332114212, Pearson Zeroed 0.329732]</p> 	<p>Myf6_3824.2_v2_RM35_prim_trimmed.txt (RM35)</p> 	<p>Myf6_3824.2_RM_combined_prim_trimmed.txt (RM35)  [Z-Beta = 40.785]  [Pearson Full 0.442015758, Pearson Zeroed 0.437868]</p> 
<p>Myf6_3824.2_v1_RM35_sec_trimmed.txt (RM35)  [Z-Beta = 24.8244]  [Pearson Full 0.332114212, Pearson Zeroed 0.321772]</p> 	<p>Myf6_3824.2_v2_RM35_sec_trimmed.txt (RM35)  [Z-Beta = 5.84921]  [Pearson Full 0.480221515, Pearson Zeroed 0.469896]</p> 	<p>Myf6_3824.2_RM_combined_sec_trimmed.txt (RM35)</p> 
<p>Myf6_3824.2_v1_RM35_prim_untrimmed.txt (RM35)  [Z-Beta = 1.58896]  [Pearson Full 0.332114212, Pearson Zeroed 0.332104]</p> 	<p>Myf6_3824.2_v2_RM35_prim_untrimmed.txt (RM35)  [Z-Beta = 13.0366]  [Pearson Full 0.480221515, Pearson Zeroed 0.470473]</p> 	<p>Myf6_3824.2_RM_combined_prim_untrimmed.txt (RM35)  [Z-Beta = 0.917592]  [Pearson Full 0.442015758, Pearson Zeroed 0.442243]</p> 
<p>Myf6_3824.2_v1_RM35_sec_untrimmed.txt (RM35)  [Z-Beta = 33.9458]  [Pearson Full 0.332114212, Pearson Zeroed 0.313521]</p> 	<p>Myf6_3824.2_v2_RM35_sec_untrimmed.txt (RM35)  [Z-Beta = 1.49506]  [Pearson Full 0.480221515, Pearson Zeroed 0.477704]</p> 	<p>Myf6_3824.2_RM_combined_sec_untrimmed.txt (RM35)</p> 
<p>Myf6_3824.2_PWM_1.txt (K-afal)  [Z-Beta = -26.5771]  [Pearson Full 0.332114212, Pearson Zeroed 0.328959]</p> 	<p>Myf6_3824.2_PWM_1.txt (K-afal)  [Z-Beta = 2.2805]  [Pearson Full 0.480221515, Pearson Zeroed 0.478439]</p> 	<p>Myf6_3824.2_combined_PWM_1.txt (K-afal)  [Z-Beta = 40.2319]  [Pearson Full 0.442015758, Pearson Zeroed 0.437615]</p> 
<p>Myf6_3824.2_PWM_2.txt (K-afal)  [Z-Beta = 56.6973]  [Pearson Full 0.332114212, Pearson Zeroed 0.296626]</p> 	<p>Myf6_3824.2_PWM_2.txt (K-afal)  [Z-Beta = 24.153]  [Pearson Full 0.480221515, Pearson Zeroed 0.456387]</p> 	<p>Myf6_3824.2_combined_PWM_2.txt (K-afal)</p> 



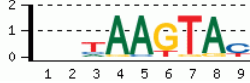
<p>Myf6_3824.2_PWM_3.txt (Kafal) No PWM Available</p>	<p>Myf6_3824.2_PWM_3.txt (Kafal)</p> 	<p>Myf6_3824.2_combined_PWM_3.txt (Kafal) No PWM Available</p>
<p>Myf6_3824.2_PWM_4.txt (Kafal) No PWM Available</p>	<p>Myf6_3824.2_PWM_4.txt (Kafal) [Z-Beta = 18.2231] [Pearson Full 0.480221515, Pearson Zeroed 0.467837]</p> 	<p>Myf6_3824.2_combined_PWM_4.txt (Kafal) No PWM Available</p>
<p>Myf6_3824.2_PWM_5.txt (Kafal) No PWM Available</p>	<p>Myf6_3824.2_PWM_5.txt (Kafal)</p> 	<p>Myf6_3824.2_combined_PWM_5.txt (Kafal) No PWM Available</p>

# Nkx3-1\_2923.2

Array Version 1	Array Version 2	Combined
<p>Nkx3-1_2923.2_v1_prim_auc.txt (SW)  [Z-Beta = 361.509]  [Pearson Full 0.837885061, Pearson Zeroed 0.73111]</p> 	<p>Nkx3-1_2923.2_v2_prim_auc.txt (SW)</p> 	<p>Nkx3-1_2923.2_combined_prim_auc.txt (SW)  No PWM Available</p>
<p>Nkx3-1_2923.2_v1_prim_ic.txt (SW)  [Z-Beta = 11.2597]  [Pearson Full 0.837885061, Pearson Zeroed 0.837803]</p> 	<p>Nkx3-1_2923.2_v2_prim_ic.txt (SW)</p> 	<p>Nkx3-1_2923.2_combined_prim_ic_trimmed.txt (SW)</p> 
<p>Nkx3-1_2923.2_v1_prim_untrimmed.txt (SW)  [Z-Beta = -290.754]  [Pearson Full 0.837885061, Pearson Zeroed 0.798297]</p> 	<p>Nkx3-1_2923.2_v2_prim_untrimmed.txt (SW)</p> 	<p>Nkx3-1_2923.2_combined_prim_untrimmed.txt (SW)</p> 
<p>Nkx3-1_2923.2_v1_sec_auc.txt (SW)  [Z-Beta = -357.274]  [Pearson Full 0.837885061, Pearson Zeroed 0.787525]</p> 	<p>Nkx3-1_2923.2_v2_sec_auc.txt (SW)</p> 	<p>Nkx3-1_2923.2_combined_sec_auc.txt (SW)</p> 
<p>Nkx3-1_2923.2_v1_sec_ic.txt (SW)  [Z-Beta = -7.71512]  [Pearson Full 0.837885061, Pearson Zeroed 0.837835]</p> 	<p>Nkx3-1_2923.2_v2_sec_ic.txt (SW)</p> 	<p>Nkx3-1_2923.2_combined_sec_ic_trimmed.txt (SW)  [Z-Beta = 5.25039]  [Pearson Full 0.797903095, Pearson Zeroed 0.796901]</p> 
<p>Nkx3-1_2923.2_v1_sec_untrimmed.txt (SW)  [Z-Beta = 331.528]  [Pearson Full 0.837885061, Pearson Zeroed 0.751546]</p> 	<p>Nkx3-1_2923.2_v2_sec_untrimmed.txt (SW)</p> 	<p>Nkx3-1_2923.2_combined_sec_untrimmed.txt (SW)</p> 
<p>Nkx3-1_2923.2_v1_prim_trimmed.txt (RM8)  [Z-Beta = -88.0001]  [Pearson Full 0.837885061, Pearson Zeroed 0.832906]</p> 	<p>Nkx3-1_2923.2_v2_prim_trimmed.txt (RM8)</p> 	<p>Nkx3-1_2923.2_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Nkx3-1_2923.2_v1_sec_trimmed.txt (RM8)  [Z-Beta = 5.48259]  [Pearson Full 0.837885061, Pearson Zeroed 0.837862]</p> 	<p>Nkx3-1_2923.2_v2_sec_trimmed.txt (RM8)</p> 	<p>Nkx3-1_2923.2_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>


<p>Nkx3-1_2923.2_v1_prim_untrimmed.txt (RM8)  [Z-Beta = 89.8964]  [Pearson Full 0.837885061, Pearson Zeroed 0.832599]</p> 	<p>Nkx3-1_2923.2_v2_prim_untrimmed.txt (RM8)</p> 	<p>Nkx3-1_2923.2_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Nkx3-1_2923.2_v1_sec_untrimmed.txt (RM8)  [Z-Beta = 13.1287]  [Pearson Full 0.837885061, Pearson Zeroed 0.837787]</p> 	<p>Nkx3-1_2923.2_v2_sec_untrimmed.txt (RM8)</p> 	<p>Nkx3-1_2923.2_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>
<p>Nkx3-1_2923.2_v1_RM35_prim_trimmed.txt (RM35)  [Z-Beta = 48.5646]  [Pearson Full 0.837885061, Pearson Zeroed 0.836737]</p> 	<p>Nkx3-1_2923.2_v2_RM35_prim_trimmed.txt (RM35)  [Z-Beta = 17.2164]  [Pearson Full 0.786887717, Pearson Zeroed 0.78684]</p> 	<p>Nkx3-1_2923.2_RM_combined_prim_trimmed.txt (RM35)  [Z-Beta = 36.5897]  [Pearson Full 0.797903095, Pearson Zeroed 0.798593]</p> 
<p>Nkx3-1_2923.2_v1_RM35_sec_trimmed.txt (RM35)  [Z-Beta = 158.298]  [Pearson Full 0.837885061, Pearson Zeroed 0.82846]</p> 	<p>Nkx3-1_2923.2_v2_RM35_sec_trimmed.txt (RM35)</p> 	<p>Nkx3-1_2923.2_RM_combined_sec_trimmed.txt (RM35)  [Z-Beta = 203.282]  [Pearson Full 0.797903095, Pearson Zeroed 0.769602]</p> 
<p>Nkx3-1_2923.2_v1_RM35_prim_untrimmed.txt (RM35)  [Z-Beta = 91.5151]  [Pearson Full 0.837885061, Pearson Zeroed 0.833609]</p> 	<p>Nkx3-1_2923.2_v2_RM35_prim_untrimmed.txt (RM35)  [Z-Beta = 150.612]  [Pearson Full 0.786887717, Pearson Zeroed 0.770268]</p> 	<p>Nkx3-1_2923.2_RM_combined_prim_untrimmed.txt (RM35)  [Z-Beta = 230.639]  [Pearson Full 0.797903095, Pearson Zeroed 0.764272]</p> 
<p>Nkx3-1_2923.2_v1_RM35_sec_untrimmed.txt (RM35)  [Z-Beta = 73.8598]  [Pearson Full 0.837885061, Pearson Zeroed 0.835082]</p> 	<p>Nkx3-1_2923.2_v2_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Nkx3-1_2923.2_RM_combined_sec_untrimmed.txt (RM35)  [Z-Beta = 35.2312]  [Pearson Full 0.797903095, Pearson Zeroed 0.795659]</p> 
<p>Nkx3-1_2923.2_PWM_10.txt (K-afal)  [Z-Beta = 183.022]  [Pearson Full 0.837885061, Pearson Zeroed 0.813032]</p> 	<p>Nkx3-1_2923.2_PWM_10.txt (K-afal)  No PWM Available</p>	<p>Nkx3-1_2923.2_combined_PWM_10.txt (K-afal)  No PWM Available</p>
<p>Nkx3-1_2923.2_PWM_11.txt (K-afal)  [Z-Beta = -10.882]  [Pearson Full 0.837885061, Pearson Zeroed 0.837805]</p> 	<p>Nkx3-1_2923.2_PWM_11.txt (K-afal)  No PWM Available</p>	<p>Nkx3-1_2923.2_combined_PWM_11.txt (K-afal)  No PWM Available</p>





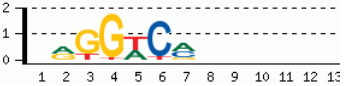
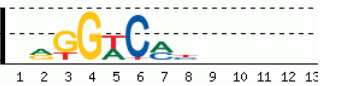
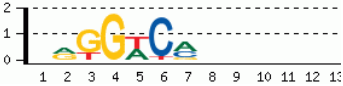

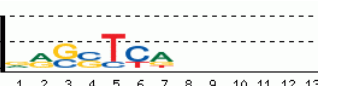













<p>Nkx3-1_2923.2_PWM_12.txt (K-afal)  [Z-Beta = 150.862]  [Pearson Full 0.837885061, Pearson Zeroed 0.824001]</p> 	<p>Nkx3-1_2923.2_PWM_12.txt (K-afal)  No PWM Available</p>	<p>Nkx3-1_2923.2_combined_PWM_12.txt (K-afal)  No PWM Available</p>
<p>Nkx3-1_2923.2_PWM_13.txt (K-afal)  [Z-Beta = 413.781]  [Pearson Full 0.837885061, Pearson Zeroed 0.7108]</p> 	<p>Nkx3-1_2923.2_PWM_13.txt (K-afal)  No PWM Available</p>	<p>Nkx3-1_2923.2_combined_PWM_13.txt (K-afal)  No PWM Available</p>
<p>Nkx3-1_2923.2_PWM_1.txt (K-afal)  [Z-Beta = 140.712]  [Pearson Full 0.837885061, Pearson Zeroed 0.828618]</p> 	<p>Nkx3-1_2923.2_PWM_1.txt (K-afal)</p> 	<p>Nkx3-1_2923.2_combined_PWM_1.txt (K-afal)  No PWM Available</p>
<p>Nkx3-1_2923.2_PWM_2.txt (K-afal)  [Z-Beta = 76.3154]  [Pearson Full 0.837885061, Pearson Zeroed 0.833582]</p> 	<p>Nkx3-1_2923.2_PWM_2.txt (K-afal)  [Z-Beta = 136.556]  [Pearson Full 0.786887717, Pearson Zeroed 0.757276]</p> 	<p>Nkx3-1_2923.2_combined_PWM_2.txt (K-afal)  No PWM Available</p>
<p>Nkx3-1_2923.2_PWM_3.txt (K-afal)  [Z-Beta = 0.525697]  [Pearson Full 0.837885061, Pearson Zeroed 0.837885]</p> 	<p>Nkx3-1_2923.2_PWM_3.txt (K-afal)</p> 	<p>Nkx3-1_2923.2_combined_PWM_3.txt (K-afal)  No PWM Available</p>
<p>Nkx3-1_2923.2_PWM_4.txt (K-afal)  [Z-Beta = -267.59]  [Pearson Full 0.837885061, Pearson Zeroed 0.805467]</p> 	<p>Nkx3-1_2923.2_PWM_4.txt (K-afal)  [Z-Beta = 282.179]  [Pearson Full 0.786887717, Pearson Zeroed 0.717558]</p> 	<p>Nkx3-1_2923.2_combined_PWM_4.txt (K-afal)  No PWM Available</p>
<p>Nkx3-1_2923.2_PWM_5.txt (K-afal)  [Z-Beta = 161.723]  [Pearson Full 0.837885061, Pearson Zeroed 0.824361]</p> 	<p>Nkx3-1_2923.2_PWM_5.txt (K-afal)  [Z-Beta = 9.6595]  [Pearson Full 0.786887717, Pearson Zeroed 0.785428]</p> 	<p>Nkx3-1_2923.2_combined_PWM_5.txt (K-afal)  No PWM Available</p>
<p>Nkx3-1_2923.2_PWM_6.txt (K-afal)  [Z-Beta = 64.5908]  [Pearson Full 0.837885061, Pearson Zeroed 0.836339]</p> 	<p>Nkx3-1_2923.2_PWM_6.txt (K-afal)  [Z-Beta = 182.632]  [Pearson Full 0.786887717, Pearson Zeroed 0.761272]</p> 	<p>Nkx3-1_2923.2_combined_PWM_6.txt (K-afal)  No PWM Available</p>
<p>Nkx3-1_2923.2_PWM_7.txt (K-afal)  [Z-Beta = 35.3735]  [Pearson Full 0.837885061, Pearson Zeroed 0.83699]</p>	<p>Nkx3-1_2923.2_PWM_7.txt (K-afal)  No PWM Available</p>	<p>Nkx3-1_2923.2_combined_PWM_7.txt (K-afal)  No PWM Available</p>

 <p>Sequence logo for PWM 8. The x-axis is labeled 1 to 9. The y-axis is labeled 0, 1, 2. The sequence is C T A A G T G. The 'A' at position 4 is the most conserved, with a height of approximately 1.8.</p>		
<p><b>Nkx3-1_2923.2_PWM_8.txt (K-afal)</b>  <b>[Z-Beta = 69.0502]</b>  <b>[Pearson Full 0.837885061, Pearson Zeroed 0.834435]</b></p>  <p>Sequence logo for PWM 8. The x-axis is labeled 1 to 10. The y-axis is labeled 0, 1, 2. The sequence is A A G T G. The 'A' at position 4 is the most conserved, with a height of approximately 1.8.</p>	<p>Nkx3-1_2923.2_PWM_8.txt (K-afal)  No PWM Available</p>	<p>Nkx3-1_2923.2_combined_PWM_8.txt (K-afal)  No PWM Available</p>
<p><b>Nkx3-1_2923.2_PWM_9.txt (K-afal)</b>  <b>[Z-Beta = 43.1057]</b>  <b>[Pearson Full 0.837885061, Pearson Zeroed 0.836698]</b></p>  <p>Sequence logo for PWM 9. The x-axis is labeled 1 to 9. The y-axis is labeled 0, 1, 2. The sequence is A A G T A. The 'A' at position 4 is the most conserved, with a height of approximately 1.8.</p>	<p>Nkx3-1_2923.2_PWM_9.txt (K-afal)  No PWM Available</p>	<p>Nkx3-1_2923.2_combined_PWM_9.txt (K-afal)  No PWM Available</p>















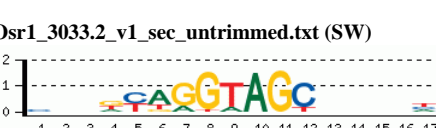
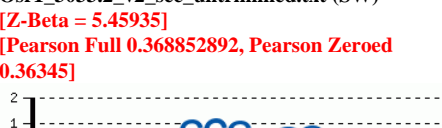
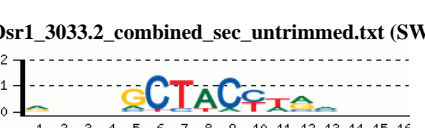




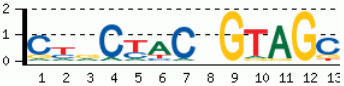



# Nr2f2\_2192.2

Array Version 1	Array Version 2	Combined
Nr2f2_2192.2_v1_prim_auc.txt (SW) <b>[Z-Beta = 403.888]</b> <b>[Pearson Full 0.742798926, Pearson Zeroed 0.726695]</b> 	Nr2f2_2192.2_v2_prim_auc.txt (SW) 	Nr2f2_2192.2_combined_prim_auc.txt (SW) 
Nr2f2_2192.2_v1_prim_ic.txt (SW) 	Nr2f2_2192.2_v2_prim_ic.txt (SW) 	Nr2f2_2192.2_combined_prim_ic_trimmed.txt (SW) 
Nr2f2_2192.2_v1_prim_untrimmed.txt (SW) <b>[Z-Beta = 198.426]</b> <b>[Pearson Full 0.742798926, Pearson Zeroed 0.722952]</b> 	Nr2f2_2192.2_v2_prim_untrimmed.txt (SW) 	Nr2f2_2192.2_combined_prim_untrimmed.txt (SW) 
Nr2f2_2192.2_v1_sec_auc.txt (SW) 	Nr2f2_2192.2_v2_sec_auc.txt (SW) 	Nr2f2_2192.2_combined_sec_auc.txt (SW) 
Nr2f2_2192.2_v1_sec_ic.txt (SW) 	Nr2f2_2192.2_v2_sec_ic.txt (SW) 	Nr2f2_2192.2_combined_sec_ic_trimmed.txt (SW) 
Nr2f2_2192.2_v1_sec_untrimmed.txt (SW) 	Nr2f2_2192.2_v2_sec_untrimmed.txt (SW) 	Nr2f2_2192.2_combined_sec_untrimmed.txt (SW) 
Nr2f2_2192.2_v1_prim_trimmed.txt (RM8) 	Nr2f2_2192.2_v2_prim_trimmed.txt (RM8) 	Nr2f2_2192.2_RM8_combined_prim_trimmed.txt (RM8) No PWM Available
Nr2f2_2192.2_v1_sec_trimmed.txt (RM8) 	Nr2f2_2192.2_v2_sec_trimmed.txt (RM8) 	Nr2f2_2192.2_RM8_combined_sec_trimmed.txt (RM8) No PWM Available
Nr2f2_2192.2_v1_prim_untrimmed.txt (RM8) 	Nr2f2_2192.2_v2_prim_untrimmed.txt (RM8) 	Nr2f2_2192.2_RM8_combined_prim_untrimmed.txt (RM8) No PWM Available
Nr2f2_2192.2_v1_sec_untrimmed.txt (RM8) 	Nr2f2_2192.2_v2_sec_untrimmed.txt (RM8) 	Nr2f2_2192.2_RM8_combined_sec_untrimmed.txt (RM8) No PWM Available
Nr2f2_2192.2_v1_RM35_prim_trimmed.txt (RM35) <b>[Z-Beta = 839.533]</b> <b>[Pearson Full 0.742798926, Pearson Zeroed 0.739505]</b> 	Nr2f2_2192.2_v2_RM35_prim_trimmed.txt (RM35) 	Nr2f2_2192.2_RM_combined_prim_trimmed.txt (RM35) 




















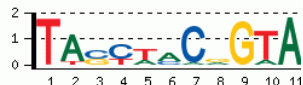


		
<p>Nr2f2_2192.2_v1_RM35_sec_trimmed.txt (RM35)  [Z-Beta = 226.489]  [Pearson Full 0.742798926, Pearson Zeroed 0.736383]</p> 	<p>Nr2f2_2192.2_v2_RM35_sec_trimmed.txt (RM35)  [Z-Beta = 192.571]  [Pearson Full 0.648109831, Pearson Zeroed 0.632457]</p> 	<p>Nr2f2_2192.2_RM_combined_sec_trimmed.txt (RM35)</p> 
<p>Nr2f2_2192.2_v1_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Nr2f2_2192.2_v2_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Nr2f2_2192.2_RM_combined_prim_untrimmed.txt (RM35)</p> 
<p>Nr2f2_2192.2_v1_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Nr2f2_2192.2_v2_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Nr2f2_2192.2_RM_combined_sec_untrimmed.txt (RM35)</p> 
<p>Nr2f2_2192.2_PWM_1.txt (K-afal)</p> 	<p>Nr2f2_2192.2_PWM_1.txt (K-afal)  [Z-Beta = 56.8004]  [Pearson Full 0.648109831, Pearson Zeroed 0.64679]</p> 	<p>Nr2f2_2192.2_combined_PWM_1.txt (K-afal)</p> 
<p>Nr2f2_2192.2_PWM_2.txt (K-afal)</p> 	<p>Nr2f2_2192.2_PWM_2.txt (K-afal)</p> 	<p>Nr2f2_2192.2_combined_PWM_2.txt (K-afal)</p> 
<p>Nr2f2_2192.2_PWM_3.txt (K-afal)  [Z-Beta = 271.303]  [Pearson Full 0.742798926, Pearson Zeroed 0.740449]</p> 	<p>Nr2f2_2192.2_PWM_3.txt (K-afal)  [Z-Beta = 884.336]  [Pearson Full 0.648109831, Pearson Zeroed 0.588471]</p> 	<p>Nr2f2_2192.2_combined_PWM_3.txt (K-afal)  [Z-Beta = 717.899]  [Pearson Full 0.734022985, Pearson Zeroed 0.622942]</p> 
<p>Nr2f2_2192.2_PWM_4.txt (K-afal)  [Z-Beta = 137.859]  [Pearson Full 0.742798926, Pearson Zeroed 0.735772]</p> 	<p>Nr2f2_2192.2_PWM_4.txt (K-afal)  No PWM Available</p>	<p>Nr2f2_2192.2_combined_PWM_4.txt (K-afal)  [Z-Beta = 701.75]  [Pearson Full 0.734022985, Pearson Zeroed 0.625837]</p> 
<p>Nr2f2_2192.2_PWM_5.txt (K-afal)</p> 	<p>Nr2f2_2192.2_PWM_5.txt (K-afal)  No PWM Available</p>	<p>Nr2f2_2192.2_combined_PWM_5.txt (K-afal)  No PWM Available</p>




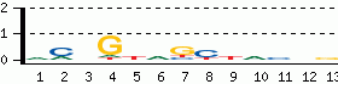





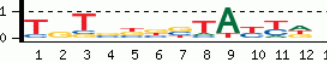





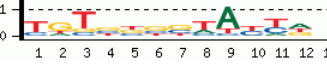





# Osr1\_3033.2





Array Version 1	Array Version 2	Combined
<p>Osr1_3033.2_v1_prim_auc.txt (SW)  [Z-Beta = 105.464]  [Pearson Full 0.706140306, Pearson Zeroed 0.668882]</p> 	<p>Osr1_3033.2_v2_prim_auc.txt (SW)  [Z-Beta = 60.5411]  [Pearson Full 0.368852892, Pearson Zeroed 0.348907]</p> 	<p>Osr1_3033.2_combined_prim_auc.txt (SW)  No PWM Available</p>
<p>Osr1_3033.2_v1_prim_ic.txt (SW)  [Z-Beta = 10.7009]  [Pearson Full 0.706140306, Pearson Zeroed 0.706909]</p> 	<p>Osr1_3033.2_v2_prim_ic.txt (SW)  [Z-Beta = 10.9964]  [Pearson Full 0.368852892, Pearson Zeroed 0.369931]</p> 	<p>Osr1_3033.2_combined_prim_ic_trimmed.txt (SW)</p> 
<p>Osr1_3033.2_v1_prim_untrimmed.txt (SW)</p> 	<p>Osr1_3033.2_v2_prim_untrimmed.txt (SW)</p> 	<p>Osr1_3033.2_combined_prim_untrimmed.txt (SW)</p> 
<p>Osr1_3033.2_v1_sec_auc.txt (SW)</p> 	<p>Osr1_3033.2_v2_sec_auc.txt (SW)</p> 	<p>Osr1_3033.2_combined_sec_auc.txt (SW)</p> 
<p>Osr1_3033.2_v1_sec_ic.txt (SW)</p> 	<p>Osr1_3033.2_v2_sec_ic.txt (SW)</p> 	<p>Osr1_3033.2_combined_sec_ic_trimmed.txt (SW)</p> 
<p>Osr1_3033.2_v1_sec_untrimmed.txt (SW)</p> 	<p>Osr1_3033.2_v2_sec_untrimmed.txt (SW)  [Z-Beta = 5.45935]  [Pearson Full 0.368852892, Pearson Zeroed 0.36345]</p> 	<p>Osr1_3033.2_combined_sec_untrimmed.txt (SW)</p> 
<p>Osr1_3033.2_v1_prim_trimmed.txt (RM8)</p> 	<p>Osr1_3033.2_v2_prim_trimmed.txt (RM8)  [Z-Beta = 8.26033]  [Pearson Full 0.368852892, Pearson Zeroed 0.362967]</p> 	<p>Osr1_3033.2_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Osr1_3033.2_v1_sec_trimmed.txt (RM8)</p> 	<p>Osr1_3033.2_v2_sec_trimmed.txt (RM8)  [Z-Beta = 18.9327]  [Pearson Full 0.368852892, Pearson Zeroed 0.356606]</p> 	<p>Osr1_3033.2_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Osr1_3033.2_v1_prim_untrimmed.txt (RM8)</p> 	<p>Osr1_3033.2_v2_prim_untrimmed.txt (RM8)</p> 	<p>Osr1_3033.2_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Osr1_3033.2_v1_sec_untrimmed.txt (RM8)</p>	<p>Osr1_3033.2_v2_sec_untrimmed.txt (RM8)  [Z-Beta = 5.45935]  [Pearson Full 0.368852892, Pearson Zeroed 0.36345]</p>	<p>Osr1_3033.2_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>

<p>Osr1_3033.2_v1_RM35_prim_trimmed.txt (RM35)</p>	<p>Osr1_3033.2_v2_RM35_prim_trimmed.txt (RM35)  [Z-Beta = 20.565]  [Pearson Full 0.368852892, Pearson Zeroed 0.360077]</p>	<p>Osr1_3033.2_RM_combined_prim_trimmed.txt (RM35)  [Z-Beta = 38.2726]  [Pearson Full 0.643646218, Pearson Zeroed 0.646842]</p>
<p>Osr1_3033.2_v1_RM35_sec_trimmed.txt (RM35)</p>	<p>Osr1_3033.2_v2_RM35_sec_trimmed.txt (RM35)</p>	<p>Osr1_3033.2_RM_combined_sec_trimmed.txt (RM35)</p>
<p>Osr1_3033.2_v1_RM35_prim_untrimmed.txt (RM35)  [Z-Beta = 45.1103]  [Pearson Full 0.706140306, Pearson Zeroed 0.705265]</p>	<p>Osr1_3033.2_v2_RM35_prim_untrimmed.txt (RM35)  [Z-Beta = 13.715]  [Pearson Full 0.368852892, Pearson Zeroed 0.357906]</p>	<p>Osr1_3033.2_RM_combined_prim_untrimmed.txt (RM35)  [Z-Beta = 114.988]  [Pearson Full 0.643646218, Pearson Zeroed 0.633449]</p>
<p>Osr1_3033.2_v1_RM35_sec_untrimmed.txt (RM35)  [Z-Beta = 22.1859]  [Pearson Full 0.706140306, Pearson Zeroed 0.696896]</p>	<p>Osr1_3033.2_v2_RM35_sec_untrimmed.txt (RM35)</p>	<p>Osr1_3033.2_RM_combined_sec_untrimmed.txt (RM35)</p>
<p>Osr1_3033.2_PWM_1.txt (K-afal)</p>	<p>Osr1_3033.2_PWM_1.txt (K-afal)</p>	<p>Osr1_3033.2_combined_PWM_1.txt (K-afal)  [Z-Beta = 25.8811]  [Pearson Full 0.643646218, Pearson Zeroed 0.627529]</p>
<p>Osr1_3033.2_PWM_2.txt (K-afal)</p>	<p>Osr1_3033.2_PWM_2.txt (K-afal)  No PWM Available</p>	<p>Osr1_3033.2_combined_PWM_2.txt (K-afal)</p>
<p>Osr1_3033.2_PWM_3.txt (K-afal)  [Z-Beta = 81.6607]  [Pearson Full 0.706140306, Pearson Zeroed 0.638231]</p>	<p>Osr1_3033.2_PWM_3.txt (K-afal)  No PWM Available</p>	<p>Osr1_3033.2_combined_PWM_3.txt (K-afal)  [Z-Beta = 14.6517]  [Pearson Full 0.643646218, Pearson Zeroed 0.630493]</p>
<p>Osr1_3033.2_PWM_4.txt (K-afal)</p>	<p>Osr1_3033.2_PWM_4.txt (K-afal)  No PWM Available</p>	<p>Osr1_3033.2_combined_PWM_4.txt (K-afal)</p>
<p>Osr1_3033.2_PWM_5.txt (K-afal)  [Z-Beta = 113.393]  [Pearson Full 0.706140306, Pearson Zeroed 0.651727]</p>	<p>Osr1_3033.2_PWM_5.txt (K-afal)  No PWM Available</p>	<p>Osr1_3033.2_combined_PWM_5.txt (K-afal)</p>

# Osr2\_1727.2

Array Version 1	Array Version 2	Combined
<p>Osr2_1727.2_v1_prim_auc.txt (SW)  [Z-Beta = -4130.68]  [Pearson Full 0.547985928, Pearson Zeroed 0.352595]</p> 	<p>Osr2_1727.2_v2_prim_auc.txt (SW)  [Z-Beta = 50.863]  [Pearson Full 0.484886784, Pearson Zeroed 0.440612]</p> 	<p>Osr2_1727.2_combined_prim_auc.txt (SW)</p> 
<p>Osr2_1727.2_v1_prim_ic.txt (SW)  [Z-Beta = 4180.46]  [Pearson Full 0.547985928, Pearson Zeroed 0.315936]</p> 	<p>Osr2_1727.2_v2_prim_ic.txt (SW)</p> 	<p>Osr2_1727.2_combined_prim_ic_trimmed.txt (SW)</p> 
<p>Osr2_1727.2_v1_prim_untrimmed.txt (SW)  [Z-Beta = 19.6942]  [Pearson Full 0.547985928, Pearson Zeroed 0.546023]</p> 	<p>Osr2_1727.2_v2_prim_untrimmed.txt (SW)</p> 	<p>Osr2_1727.2_combined_prim_untrimmed.txt (SW)</p> 
<p>Osr2_1727.2_v1_sec_auc.txt (SW)  [Z-Beta = 37.4554]  [Pearson Full 0.547985928, Pearson Zeroed 0.533295]</p> 	<p>Osr2_1727.2_v2_sec_auc.txt (SW)</p> 	<p>Osr2_1727.2_combined_sec_auc.txt (SW)</p> 
<p>Osr2_1727.2_v1_sec_ic.txt (SW)  [Z-Beta = 20.446]  [Pearson Full 0.547985928, Pearson Zeroed 0.54363]</p> 	<p>Osr2_1727.2_v2_sec_ic.txt (SW)</p> 	<p>Osr2_1727.2_combined_sec_ic_trimmed.txt (SW)</p> 
<p>Osr2_1727.2_v1_sec_untrimmed.txt (SW)  [Z-Beta = 5.69325]  [Pearson Full 0.547985928, Pearson Zeroed 0.547683]</p> 	<p>Osr2_1727.2_v2_sec_untrimmed.txt (SW)</p> 	<p>Osr2_1727.2_combined_sec_untrimmed.txt (SW)</p> 
<p>Osr2_1727.2_v1_prim_trimmed.txt (RM8)  [Z-Beta = -20.2713]  [Pearson Full 0.547985928, Pearson Zeroed 0.544557]</p> 	<p>Osr2_1727.2_v2_prim_trimmed.txt (RM8)</p> 	<p>Osr2_1727.2_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Osr2_1727.2_v1_sec_trimmed.txt (RM8)  [Z-Beta = -16.6337]  [Pearson Full 0.547985928, Pearson Zeroed 0.545274]</p> 	<p>Osr2_1727.2_v2_sec_trimmed.txt (RM8)</p> 	<p>Osr2_1727.2_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>

<p>Osr2_1727.2_v1_prim_untrimmed.txt (RM8)  [Z-Beta = 23.2893]  [Pearson Full 0.547985928, Pearson Zeroed 0.542779]</p> 	<p>Osr2_1727.2_v2_prim_untrimmed.txt (RM8)</p> 	<p>Osr2_1727.2_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Osr2_1727.2_v1_sec_untrimmed.txt (RM8)  [Z-Beta = 5.47966]  [Pearson Full 0.547985928, Pearson Zeroed 0.547713]</p> 	<p>Osr2_1727.2_v2_sec_untrimmed.txt (RM8)</p> 	<p>Osr2_1727.2_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>
<p>Osr2_1727.2_v1_RM35_prim_trimmed.txt (RM35)  [Z-Beta = 19.3651]  [Pearson Full 0.547985928, Pearson Zeroed 0.54487]</p> 	<p>Osr2_1727.2_v2_RM35_prim_trimmed.txt (RM35)</p> 	<p>Osr2_1727.2_RM_combined_prim_trimmed.txt (RM35)</p> 
<p>Osr2_1727.2_v1_RM35_sec_trimmed.txt (RM35)  [Z-Beta = 20.3873]  [Pearson Full 0.547985928, Pearson Zeroed 0.543852]</p> 	<p>Osr2_1727.2_v2_RM35_sec_trimmed.txt (RM35)  [Z-Beta = 29.2378]  [Pearson Full 0.484886784, Pearson Zeroed 0.451423]</p> 	<p>Osr2_1727.2_RM_combined_sec_trimmed.txt (RM35)</p> 
<p>Osr2_1727.2_v1_RM35_prim_untrimmed.txt (RM35)  [Z-Beta = 26.9357]  [Pearson Full 0.547985928, Pearson Zeroed 0.543532]</p> 	<p>Osr2_1727.2_v2_RM35_prim_untrimmed.txt (RM35)  [Z-Beta = 24.9003]  [Pearson Full 0.484886784, Pearson Zeroed 0.463594]</p> 	<p>Osr2_1727.2_RM_combined_prim_untrimmed.txt (RM35)  [Z-Beta = 33.3116]  [Pearson Full 0.563833756, Pearson Zeroed 0.528168]</p> 
<p>Osr2_1727.2_v1_RM35_sec_untrimmed.txt (RM35)  [Z-Beta = 10.3806]  [Pearson Full 0.547985928, Pearson Zeroed 0.546856]</p> 	<p>Osr2_1727.2_v2_RM35_sec_untrimmed.txt (RM35)  [Z-Beta = 37.7774]  [Pearson Full 0.484886784, Pearson Zeroed 0.4527]</p> 	<p>Osr2_1727.2_RM_combined_sec_untrimmed.txt (RM35)</p> 
<p>Osr2_1727.2_PWM_1.txt (K-afal)  [Z-Beta = 182.851]  [Pearson Full 0.547985928, Pearson Zeroed 0.200679]</p> 	<p>Osr2_1727.2_PWM_1.txt (K-afal)</p> 	<p>Osr2_1727.2_combined_PWM_1.txt (K-afal)</p> 
<p>Osr2_1727.2_PWM_2.txt (K-afal)  [Z-Beta = 52.8891]  [Pearson Full 0.547985928, Pearson Zeroed 0.522912]</p> 	<p>Osr2_1727.2_PWM_2.txt (K-afal)  No PWM Available</p>	<p>Osr2_1727.2_combined_PWM_2.txt (K-afal)  [Z-Beta = 111.324]  [Pearson Full 0.563833756, Pearson Zeroed 0.379583]</p> 









<p>Osr2_1727.2_PWM_3.txt (K-afal)  [Z-Beta = -101.037]  [Pearson Full 0.547985928, Pearson Zeroed 0.519924]</p> 	<p>Osr2_1727.2_PWM_3.txt (K-afal)  No PWM Available</p>	<p>Osr2_1727.2_combined_PWM_3.txt (K-afal)</p> 
<p>Osr2_1727.2_PWM_4.txt (K-afal)  [Z-Beta = 46.0143]  [Pearson Full 0.547985928, Pearson Zeroed 0.533483]</p> 	<p>Osr2_1727.2_PWM_4.txt (K-afal)  No PWM Available</p>	<p>Osr2_1727.2_combined_PWM_4.txt (K-afal)  No PWM Available</p>
<p>Osr2_1727.2_PWM_5.txt (K-afal)  [Z-Beta = 1.62987]  [Pearson Full 0.547985928, Pearson Zeroed 0.547971]</p> 	<p>Osr2_1727.2_PWM_5.txt (K-afal)  No PWM Available</p>	<p>Osr2_1727.2_combined_PWM_5.txt (K-afal)  No PWM Available</p>



# Plagl1\_0972.2






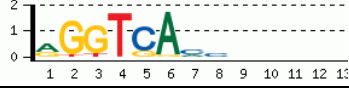





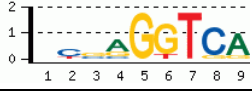









Array Version 1	Array Version 2	Combined
<p>Plagl1_0972.2_v1_prim_auc.txt (SW)</p>	<p>Plagl1_0972.2_v2_prim_auc.txt (SW)</p>	<p>Plagl1_0972.2_combined_prim_auc.txt (SW)</p>
<p>Plagl1_0972.2_v1_prim_ic.txt (SW)</p>	<p>Plagl1_0972.2_v2_prim_ic.txt (SW)</p>	<p>Plagl1_0972.2_combined_prim_ic_trimmed.txt (SW)  [Z-Beta = -118.551]  [Pearson Full 0.874841156, Pearson Zeroed 0.872314]</p>
<p>Plagl1_0972.2_v1_prim_untrimmed.txt (SW)</p>	<p>Plagl1_0972.2_v2_prim_untrimmed.txt (SW)  [Z-Beta = 1.82132]  [Pearson Full 0.799095402, Pearson Zeroed 0.798622]</p>	<p>Plagl1_0972.2_combined_prim_untrimmed.txt (SW)</p>
<p>Plagl1_0972.2_v1_sec_auc.txt (SW)</p>	<p>Plagl1_0972.2_v2_sec_auc.txt (SW)</p>	<p>Plagl1_0972.2_combined_sec_auc.txt (SW)</p>
<p>Plagl1_0972.2_v1_sec_ic.txt (SW)</p>	<p>Plagl1_0972.2_v2_sec_ic.txt (SW)</p>	<p>Plagl1_0972.2_combined_sec_ic_trimmed.txt (SW)  [Z-Beta = -10.1855]  [Pearson Full 0.874841156, Pearson Zeroed 0.874816]</p>
<p>Plagl1_0972.2_v1_sec_untrimmed.txt (SW)</p>	<p>Plagl1_0972.2_v2_sec_untrimmed.txt (SW)</p>	<p>Plagl1_0972.2_combined_sec_untrimmed.txt (SW)  [Z-Beta = 118.797]  [Pearson Full 0.874841156, Pearson Zeroed 0.870989]</p>
<p>Plagl1_0972.2_v1_prim_trimmed.txt (RM8)</p>	<p>Plagl1_0972.2_v2_prim_trimmed.txt (RM8)</p>	<p>Plagl1_0972.2_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Plagl1_0972.2_v1_sec_trimmed.txt (RM8)</p>	<p>Plagl1_0972.2_v2_sec_trimmed.txt (RM8)</p>	<p>Plagl1_0972.2_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Plagl1_0972.2_v1_prim_untrimmed.txt (RM8)  [Z-Beta = 40.5897]  [Pearson Full 0.776278441, Pearson Zeroed 0.772357]</p>	<p>Plagl1_0972.2_v2_prim_untrimmed.txt (RM8)  [Z-Beta = 1.82132]  [Pearson Full 0.799095402, Pearson Zeroed 0.798622]</p>	<p>Plagl1_0972.2_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Plagl1_0972.2_v1_sec_untrimmed.txt (RM8)</p>	<p>Plagl1_0972.2_v2_sec_untrimmed.txt (RM8)</p>	<p>Plagl1_0972.2_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>





		
<p>PlagI1_0972.2_v1_RM35_prim_trimmed.txt (RM35)  [Z-Beta = 77.4919]  [Pearson Full 0.776278441, Pearson Zeroed 0.770685]</p> 	<p>PlagI1_0972.2_v2_RM35_prim_trimmed.txt (RM35)</p> 	<p>PlagI1_0972.2_RM_combined_prim_trimmed.txt (RM35)  [Z-Beta = 81.9076]  [Pearson Full 0.874841156, Pearson Zeroed 0.873955]</p> 
<p>PlagI1_0972.2_v1_RM35_sec_trimmed.txt (RM35)</p> 	<p>PlagI1_0972.2_v2_RM35_sec_trimmed.txt (RM35)</p> 	<p>PlagI1_0972.2_RM_combined_sec_trimmed.txt (RM35)  [Z-Beta = -4.94209]  [Pearson Full 0.874841156, Pearson Zeroed 0.874833]</p> 
<p>PlagI1_0972.2_v1_RM35_prim_untrimmed.txt (RM35)</p> 	<p>PlagI1_0972.2_v2_RM35_prim_untrimmed.txt (RM35)</p> 	<p>PlagI1_0972.2_RM_combined_prim_untrimmed.txt (RM35)  [Z-Beta = -229.149]  [Pearson Full 0.874841156, Pearson Zeroed 0.868217]</p> 
<p>PlagI1_0972.2_v1_RM35_sec_untrimmed.txt (RM35)</p> 	<p>PlagI1_0972.2_v2_RM35_sec_untrimmed.txt (RM35)</p> 	<p>PlagI1_0972.2_RM_combined_sec_untrimmed.txt (RM35)  [Z-Beta = 33.714]  [Pearson Full 0.874841156, Pearson Zeroed 0.874581]</p> 
<p>PlagI1_0972.2_PWM_1.txt (K-afal)  [Z-Beta = 24.0264]  [Pearson Full 0.776278441, Pearson Zeroed 0.773334]</p> 	<p>PlagI1_0972.2_PWM_1.txt (K-afal)</p> 	<p>PlagI1_0972.2_combined_PWM_1.txt (K-afal)  [Z-Beta = 1040.44]  [Pearson Full 0.874841156, Pearson Zeroed 0.718268]</p> 
<p>PlagI1_0972.2_PWM_2.txt (K-afal)  [Z-Beta = 299.162]  [Pearson Full 0.776278441, Pearson Zeroed 0.755145]</p> 	<p>PlagI1_0972.2_PWM_2.txt (K-afal)</p> 	<p>PlagI1_0972.2_combined_PWM_2.txt (K-afal)  [Z-Beta = -188.744]  [Pearson Full 0.874841156, Pearson Zeroed 0.872968]</p> 
<p>PlagI1_0972.2_PWM_3.txt (K-afal)  [Z-Beta = 339.371]  [Pearson Full 0.776278441, Pearson Zeroed 0.715429]</p> 	<p>PlagI1_0972.2_PWM_3.txt (K-afal)  [Z-Beta = 32.2104]  [Pearson Full 0.799095402, Pearson Zeroed 0.793804]</p> 	<p>PlagI1_0972.2_combined_PWM_3.txt (K-afal)  [Z-Beta = 551.313]  [Pearson Full 0.874841156, Pearson Zeroed 0.827718]</p> 
<p>PlagI1_0972.2_PWM_4.txt (K-afal)  [Z-Beta = 810.275]  [Pearson Full 0.776278441, Pearson Zeroed 0.62773]</p> 	<p>PlagI1_0972.2_PWM_4.txt (K-afal)</p> 	<p>PlagI1_0972.2_combined_PWM_4.txt (K-afal)  [Z-Beta = 704.574]  [Pearson Full 0.874841156, Pearson Zeroed 0.823546]</p> 

		
<p>PlagI1_0972.2_PWM_5.txt (K-afal)</p> 	<p>PlagI1_0972.2_PWM_5.txt (K-afal)</p> 	<p>PlagI1_0972.2_combined_PWM_5.txt (K-afal) No PWM Available</p>
<p>PlagI1_0972.2_PWM_6.txt (K-afal)</p> 	<p>PlagI1_0972.2_PWM_6.txt (K-afal)</p> 	<p>PlagI1_0972.2_combined_PWM_6.txt (K-afal) No PWM Available</p>
<p>PlagI1_0972.2_PWM_7.txt (Kafal) No PWM Available</p>	<p>PlagI1_0972.2_PWM_7.txt (Kafal)</p> 	<p>PlagI1_0972.2_combined_PWM_7.txt (Kafal) No PWM Available</p>
<p>PlagI1_0972.2_PWM_8.txt (Kafal) No PWM Available</p>	<p>PlagI1_0972.2_PWM_8.txt (Kafal) [Z-Beta = 754.483] [Pearson Full 0.799095402, Pearson Zeroed 0.572631]</p> 	<p>PlagI1_0972.2_combined_PWM_8.txt (Kafal) No PWM Available</p>























# Rara\_1051.3

Array Version 1	Array Version 2	Combined
<p>Rara_1051.2_v1_prim_auc.txt (SW)</p>	<p>Rara_1051.3_v2_prim_auc.txt (SW)</p>	<p>Rara_1051.3_combined_prim_auc.txt (SW)</p>
<p>Rara_1051.2_v1_prim_ic.txt (SW)</p>	<p>Rara_1051.3_v2_prim_ic.txt (SW)</p>	<p>Rara_1051.3_combined_prim_ic_trimmed.txt (SW)</p>
<p>Rara_1051.2_v1_prim_untrimmed.txt (SW)  [Z-Beta = 47.154]  [Pearson Full 0.757780059, Pearson Zeroed 0.735574]</p>	<p>Rara_1051.3_v2_prim_untrimmed.txt (SW)</p>	<p>Rara_1051.2_combined_prim_untrimmed.txt (SW)</p>
<p>Rara_1051.2_v1_sec_auc.txt (SW)</p>	<p>Rara_1051.3_v2_sec_auc.txt (SW)</p>	<p>Rara_1051.2_combined_sec_auc.txt (SW)</p>
<p>Rara_1051.2_v1_sec_ic.txt (SW)</p>	<p>Rara_1051.3_v2_sec_ic.txt (SW)</p>	<p>Rara_1051.2_combined_sec_ic_trimmed.txt (SW)</p>
<p>Rara_1051.2_v1_sec_untrimmed.txt (SW)</p>	<p>Rara_1051.3_v2_sec_untrimmed.txt (SW)</p>	<p>Rara_1051.2_combined_sec_untrimmed.txt (SW)</p>
<p>Rara_1051.2_v1_prim_trimmed.txt (RM8)</p>	<p>Rara_1051.3_v2_prim_trimmed.txt (RM8)</p>	<p>Rara_1051.2_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Rara_1051.2_v1_sec_trimmed.txt (RM8)</p>	<p>Rara_1051.3_v2_sec_trimmed.txt (RM8)</p>	<p>Rara_1051.2_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Rara_1051.2_v1_prim_untrimmed.txt (RM8)</p>	<p>Rara_1051.3_v2_prim_untrimmed.txt (RM8)</p>	<p>Rara_1051.2_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Rara_1051.2_v1_sec_untrimmed.txt (RM8)</p>	<p>Rara_1051.3_v2_sec_untrimmed.txt (RM8)</p>	<p>Rara_1051.2_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>
<p>Rara_1051.2_v1_RM35_prim_trimmed.txt (RM35)  [Z-Beta = 305.866]  [Pearson Full 0.757780059, Pearson Zeroed 0.718885]</p>	<p>Rara_1051.3_v2_RM35_prim_trimmed.txt (RM35)</p>	<p>Rara_1051.2_RM_combined_prim_trimmed.txt (RM35)  [Z-Beta = 251.131]  [Pearson Full 0.786597733, Pearson Zeroed 0.715165]</p>

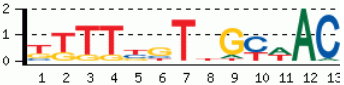


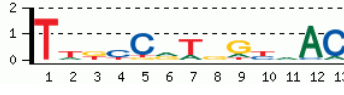

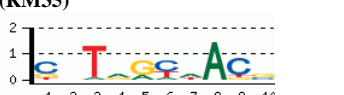



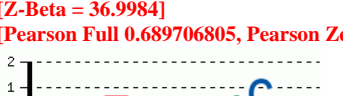

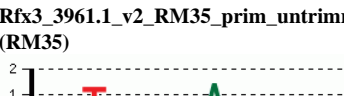
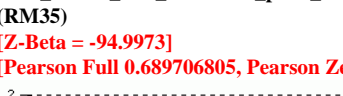
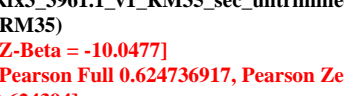
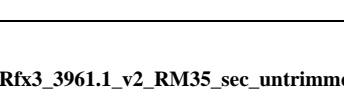

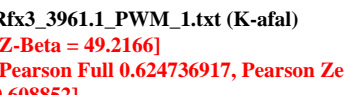
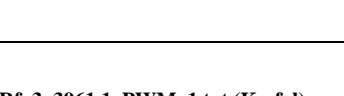
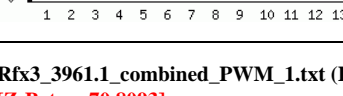
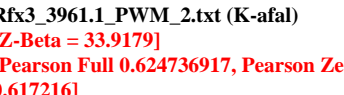
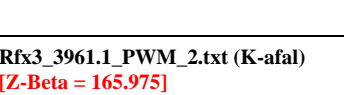
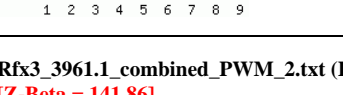
<p>Rara_1051.2_v1_RM35_sec_trimmed.txt (RM35)  [Z-Beta = 179.844]  [Pearson Full 0.757780059, Pearson Zeroed 0.72225]</p> 	<p>Rara_1051.3_v2_RM35_sec_trimmed.txt (RM35)</p> 	<p>Rara_1051.2_RM_combined_sec_trimmed.txt (RM35)  [Z-Beta = 217.567]  [Pearson Full 0.786597733, Pearson Zeroed 0.720476]</p> 
<p>Rara_1051.2_v1_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Rara_1051.3_v2_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Rara_1051.2_RM_combined_prim_untrimmed.txt (RM35)</p> 
<p>Rara_1051.2_v1_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Rara_1051.3_v2_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Rara_1051.2_RM_combined_sec_untrimmed.txt (RM35)  [Z-Beta = 12.0622]  [Pearson Full 0.786597733, Pearson Zeroed 0.785979]</p> 
<p>Rara_1051.2_PWM_10.txt (K-afal)</p> 	<p>Rara_1051.3_PWM_10.txt (K-afal)  No PWM Available</p>	<p>Rara_1051.2_combined_PWM_10.txt (K-afal)  No PWM Available</p>
<p>Rara_1051.2_PWM_11.txt (K-afal)</p> 	<p>Rara_1051.3_PWM_11.txt (K-afal)  No PWM Available</p>	<p>Rara_1051.2_combined_PWM_11.txt (K-afal)  No PWM Available</p>
<p>Rara_1051.2_PWM_12.txt (K-afal)</p> 	<p>Rara_1051.3_PWM_12.txt (K-afal)  No PWM Available</p>	<p>Rara_1051.2_combined_PWM_12.txt (K-afal)  No PWM Available</p>
<p>Rara_1051.2_PWM_13.txt (K-afal)</p> 	<p>Rara_1051.3_PWM_13.txt (K-afal)  No PWM Available</p>	<p>Rara_1051.2_combined_PWM_13.txt (K-afal)  No PWM Available</p>
<p>Rara_1051.2_PWM_1.txt (K-afal)</p> 	<p>Rara_1051.3_PWM_1.txt (K-afal)</p> 	<p>Rara_1051.2_combined_PWM_1.txt (K-afal)</p> 
<p>Rara_1051.2_PWM_2.txt (K-afal)</p> 	<p>Rara_1051.3_PWM_2.txt (K-afal)</p> 	<p>Rara_1051.2_combined_PWM_2.txt (K-afal)</p> 
<p>Rara_1051.2_PWM_3.txt (K-afal)</p> 	<p>Rara_1051.3_PWM_3.txt (K-afal)</p> 	<p>Rara_1051.2_combined_PWM_3.txt (K-afal)</p> 
<p>Rara_1051.2_PWM_4.txt (K-afal)</p> 	<p>Rara_1051.3_PWM_4.txt (K-afal)  No PWM Available</p>	<p>Rara_1051.2_combined_PWM_4.txt (K-afal)</p> 
<p>Rara_1051.2_PWM_5.txt (K-afal)</p> 	<p>Rara_1051.3_PWM_5.txt (K-afal)  No PWM Available</p>	<p>Rara_1051.2_combined_PWM_5.txt (K-afal)  No PWM Available</p>

<p><b>Rara_1051.2_PWM_6.txt (K-afal)</b></p> 	<p><b>Rara_1051.3_PWM_6.txt (K-afal)</b> No PWM Available</p>	<p><b>Rara_1051.2_combined_PWM_6.txt (K-afal)</b> No PWM Available</p>
<p><b>Rara_1051.2_PWM_7.txt (K-afal)</b></p> 	<p><b>Rara_1051.3_PWM_7.txt (K-afal)</b> No PWM Available</p>	<p><b>Rara_1051.2_combined_PWM_7.txt (K-afal)</b> No PWM Available</p>
<p><b>Rara_1051.2_PWM_8.txt (K-afal)</b></p> 	<p><b>Rara_1051.3_PWM_8.txt (K-afal)</b> No PWM Available</p>	<p><b>Rara_1051.2_combined_PWM_8.txt (K-afal)</b> No PWM Available</p>
<p><b>Rara_1051.2_PWM_9.txt (K-afal)</b></p> 	<p><b>Rara_1051.3_PWM_9.txt (K-afal)</b> No PWM Available</p>	<p><b>Rara_1051.2_combined_PWM_9.txt (K-afal)</b> No PWM Available</p>

# Rfx3\_3961.1

Array Version 1	Array Version 2	Combined
<p>Rfx3_3961.1_v1_prim_auc.txt (SW)  [Z-Beta = -1011.84]  [Pearson Full 0.624736917, Pearson Zeroed 0.394757]</p> 	<p>Rfx3_3961.1_v2_prim_auc.txt (SW)</p> 	<p>Rfx3_3961.1_combined_prim_auc.txt (SW)</p> 
<p>Rfx3_3961.1_v1_prim_ic.txt (SW)  [Z-Beta = -58.3262]  [Pearson Full 0.624736917, Pearson Zeroed 0.612989]</p> 	<p>Rfx3_3961.1_v2_prim_ic.txt (SW)</p> 	<p>Rfx3_3961.1_combined_prim_ic_trimmed.txt (SW)  [Z-Beta = -16.5861]  [Pearson Full 0.689706805, Pearson Zeroed 0.6887]</p> 
<p>Rfx3_3961.1_v1_prim_untrimmed.txt (SW)  [Z-Beta = 1103.15]  [Pearson Full 0.624736917, Pearson Zeroed 0.152405]</p> 	<p>Rfx3_3961.1_v2_prim_untrimmed.txt (SW)</p> 	<p>Rfx3_3961.1_combined_prim_untrimmed.txt (SW)  [Z-Beta = -211.565]  [Pearson Full 0.689706805, Pearson Zeroed 0.625085]</p> 
<p>Rfx3_3961.1_v1_sec_auc.txt (SW)  [Z-Beta = -52.4781]  [Pearson Full 0.624736917, Pearson Zeroed 0.608295]</p> 	<p>Rfx3_3961.1_v2_sec_auc.txt (SW)</p> 	<p>Rfx3_3961.1_combined_sec_auc.txt (SW)</p> 
<p>Rfx3_3961.1_v1_sec_ic.txt (SW)  [Z-Beta = -12.8067]  [Pearson Full 0.624736917, Pearson Zeroed 0.6235]</p> 	<p>Rfx3_3961.1_v2_sec_ic.txt (SW)</p> 	<p>Rfx3_3961.1_combined_sec_ic_trimmed.txt (SW)  [Z-Beta = 36.1877]  [Pearson Full 0.689706805, Pearson Zeroed 0.680976]</p> 
<p>Rfx3_3961.1_v1_sec_untrimmed.txt (SW)  [Z-Beta = 98.0822]  [Pearson Full 0.624736917, Pearson Zeroed 0.552196]</p> 	<p>Rfx3_3961.1_v2_sec_untrimmed.txt (SW)</p> 	<p>Rfx3_3961.1_combined_sec_untrimmed.txt (SW)  [Z-Beta = 35.1906]  [Pearson Full 0.689706805, Pearson Zeroed 0.681352]</p> 
<p>Rfx3_3961.1_v1_prim_trimmed.txt (RM8)  [Z-Beta = -13.5368]  [Pearson Full 0.624736917, Pearson Zeroed 0.623798]</p> 	<p>Rfx3_3961.1_v2_prim_trimmed.txt (RM8)</p> 	<p>Rfx3_3961.1_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Rfx3_3961.1_v1_sec_trimmed.txt (RM8)  [Z-Beta = 13.0488]  [Pearson Full 0.624736917, Pearson Zeroed 0.623591]</p> 	<p>Rfx3_3961.1_v2_sec_trimmed.txt (RM8)</p> 	<p>Rfx3_3961.1_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>



<p>Rfx3_3961.1_v1_prim_untrimmed.txt (RM8)  [Z-Beta = 23.767]  [Pearson Full 0.624736917, Pearson Zeroed 0.621154]</p> 	<p>Rfx3_3961.1_v2_prim_untrimmed.txt (RM8)</p> 	<p>Rfx3_3961.1_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Rfx3_3961.1_v1_sec_untrimmed.txt (RM8)  [Z-Beta = 27.5804]  [Pearson Full 0.624736917, Pearson Zeroed 0.620639]</p> 	<p>Rfx3_3961.1_v2_sec_untrimmed.txt (RM8)</p> 	<p>Rfx3_3961.1_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>
<p>Rfx3_3961.1_v1_RM35_prim_trimmed.txt (RM35)  [Z-Beta = -35.5065]  [Pearson Full 0.624736917, Pearson Zeroed 0.620283]</p> 	<p>Rfx3_3961.1_v2_RM35_prim_trimmed.txt (RM35)</p> 	<p>Rfx3_3961.1_RM_combined_prim_trimmed.txt (RM35)  [Z-Beta = 52.9511]  [Pearson Full 0.689706805, Pearson Zeroed 0.677233]</p> 
<p>Rfx3_3961.1_v1_RM35_sec_trimmed.txt (RM35)  [Z-Beta = 36.174]  [Pearson Full 0.624736917, Pearson Zeroed 0.619315]</p> 	<p>Rfx3_3961.1_v2_RM35_sec_trimmed.txt (RM35)</p> 	<p>Rfx3_3961.1_RM_combined_sec_trimmed.txt (RM35)  [Z-Beta = 36.9984]  [Pearson Full 0.689706805, Pearson Zeroed 0.683975]</p> 
<p>Rfx3_3961.1_v1_RM35_prim_untrimmed.txt (RM35)  [Z-Beta = 7.96494]  [Pearson Full 0.624736917, Pearson Zeroed 0.62446]</p> 	<p>Rfx3_3961.1_v2_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Rfx3_3961.1_RM_combined_prim_untrimmed.txt (RM35)  [Z-Beta = -94.9973]  [Pearson Full 0.689706805, Pearson Zeroed 0.668465]</p> 
<p>Rfx3_3961.1_v1_RM35_sec_untrimmed.txt (RM35)  [Z-Beta = -10.0477]  [Pearson Full 0.624736917, Pearson Zeroed 0.624394]</p> 	<p>Rfx3_3961.1_v2_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Rfx3_3961.1_RM_combined_sec_untrimmed.txt (RM35)  [Z-Beta = 3.58762]  [Pearson Full 0.689706805, Pearson Zeroed 0.689656]</p> 
<p>Rfx3_3961.1_PWM_1.txt (K-afal)  [Z-Beta = 49.2166]  [Pearson Full 0.624736917, Pearson Zeroed 0.608852]</p> 	<p>Rfx3_3961.1_PWM_1.txt (K-afal)</p> 	<p>Rfx3_3961.1_combined_PWM_1.txt (K-afal)  [Z-Beta = 70.8093]  [Pearson Full 0.689706805, Pearson Zeroed 0.677795]</p> 
<p>Rfx3_3961.1_PWM_2.txt (K-afal)  [Z-Beta = 33.9179]  [Pearson Full 0.624736917, Pearson Zeroed 0.617216]</p> 	<p>Rfx3_3961.1_PWM_2.txt (K-afal)  [Z-Beta = 165.975]  [Pearson Full 0.555223817, Pearson Zeroed NaN]</p> 	<p>Rfx3_3961.1_combined_PWM_2.txt (K-afal)  [Z-Beta = 141.86]  [Pearson Full 0.689706805, Pearson Zeroed 0.615424]</p> 











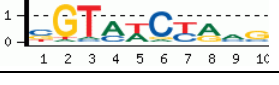















<p>Rfx3_3961.1_PWM_3.txt (K-afal)  [Z-Beta = -43.3509]  [Pearson Full 0.624736917, Pearson Zeroed 0.617519]</p> 	<p>Rfx3_3961.1_PWM_3.txt (K-afal)</p> 	<p>Rfx3_3961.1_combined_PWM_3.txt (K-afal)  [Z-Beta = -1.27715]  [Pearson Full 0.689706805, Pearson Zeroed 0.689704]</p> 
<p>Rfx3_3961.1_PWM_4.txt (K-afal)  [Z-Beta = 110.96]  [Pearson Full 0.624736917, Pearson Zeroed 0.547468]</p> 	<p>Rfx3_3961.1_PWM_4.txt (K-afal)  No PWM Available</p>	<p>Rfx3_3961.1_combined_PWM_4.txt (K-afal)  [Z-Beta = 74.4514]  [Pearson Full 0.689706805, Pearson Zeroed 0.66283]</p> 
<p>Rfx3_3961.1_PWM_5.txt (K-afal)  [Z-Beta = 55.0385]  [Pearson Full 0.624736917, Pearson Zeroed 0.608152]</p> 	<p>Rfx3_3961.1_PWM_5.txt (K-afal)  No PWM Available</p>	<p>Rfx3_3961.1_combined_PWM_5.txt (K-afal)  No PWM Available</p>
<p>Rfx3_3961.1_PWM_6.txt (K-afal)  [Z-Beta = 8.12215]  [Pearson Full 0.624736917, Pearson Zeroed 0.624341]</p> 	<p>Rfx3_3961.1_PWM_6.txt (K-afal)  No PWM Available</p>	<p>Rfx3_3961.1_combined_PWM_6.txt (K-afal)  No PWM Available</p>
<p>Rfx3_3961.1_PWM_7.txt (K-afal)  [Z-Beta = 74.0392]  [Pearson Full 0.624736917, Pearson Zeroed 0.605956]</p> 	<p>Rfx3_3961.1_PWM_7.txt (K-afal)  No PWM Available</p>	<p>Rfx3_3961.1_combined_PWM_7.txt (K-afal)  No PWM Available</p>
<p>Rfx3_3961.1_PWM_8.txt (K-afal)  [Z-Beta = 32.5284]  [Pearson Full 0.624736917, Pearson Zeroed 0.621348]</p> 	<p>Rfx3_3961.1_PWM_8.txt (K-afal)  No PWM Available</p>	<p>Rfx3_3961.1_combined_PWM_8.txt (K-afal)  No PWM Available</p>

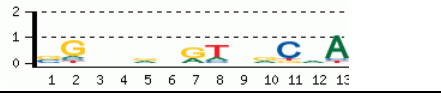




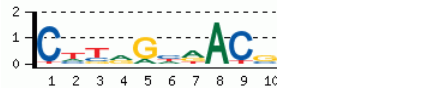

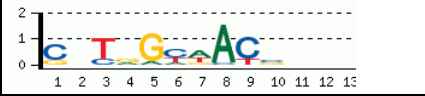


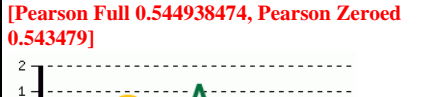

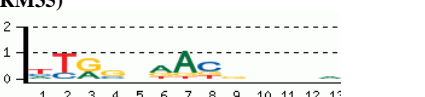
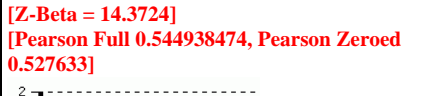

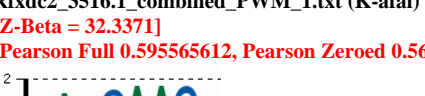
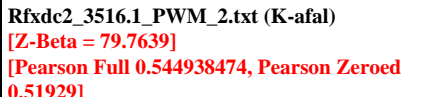
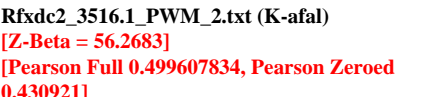
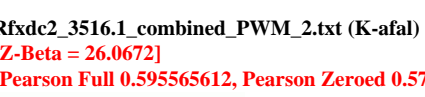
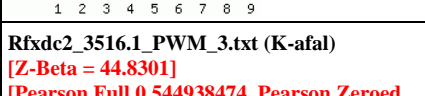
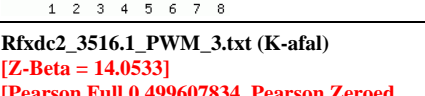
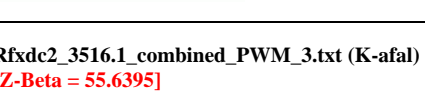
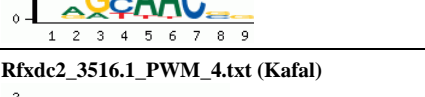
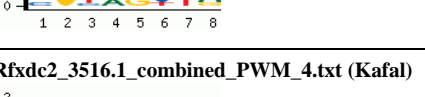
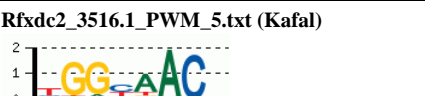
# Rfx4\_3761.1

Array Version 1	Array Version 2	Combined
<p>Rfx4_3761.1_v1_prim_auc.txt (SW) [Z-Beta = 27.999] [Pearson Full 0.526423904, Pearson Zeroed 0.472016]</p>	<p>Rfx4_3761.1_v2_prim_auc.txt (SW) [Z-Beta = 67.8966] [Pearson Full 0.501945586, Pearson Zeroed 0.495136]</p>	<p>Rfx4_3761.1_combined_prim_auc.txt (SW)</p>
<p>Rfx4_3761.1_v1_prim_ic.txt (SW)</p>	<p>Rfx4_3761.1_v2_prim_ic.txt (SW)</p>	<p>Rfx4_3761.1_combined_prim_ic_trimmed.txt (SW)</p>
<p>Rfx4_3761.1_v1_prim_untrimmed.txt (SW) [Z-Beta = 13.9085] [Pearson Full 0.526423904, Pearson Zeroed 0.50606]</p>	<p>Rfx4_3761.1_v2_prim_untrimmed.txt (SW)</p>	<p>Rfx4_3761.1_combined_prim_untrimmed.txt (SW) [Z-Beta = 57.7727] [Pearson Full 0.615129856, Pearson Zeroed 0.580401]</p>
<p>Rfx4_3761.1_v1_sec_auc.txt (SW)</p>	<p>Rfx4_3761.1_v2_sec_auc.txt (SW)</p>	<p>Rfx4_3761.1_combined_sec_auc.txt (SW)</p>
<p>Rfx4_3761.1_v1_sec_ic.txt (SW)</p>	<p>Rfx4_3761.1_v2_sec_ic.txt (SW)</p>	<p>Rfx4_3761.1_combined_sec_ic_trimmed.txt (SW)</p>
<p>Rfx4_3761.1_v1_sec_untrimmed.txt (SW)</p>	<p>Rfx4_3761.1_v2_sec_untrimmed.txt (SW)</p>	<p>Rfx4_3761.1_combined_sec_untrimmed.txt (SW)</p>
<p>Rfx4_3761.1_v1_prim_trimmed.txt (RM8)</p>	<p>Rfx4_3761.1_v2_prim_trimmed.txt (RM8)</p>	<p>Rfx4_3761.1_RM8_combined_prim_trimmed.txt (RM8) No PWM Available</p>
<p>Rfx4_3761.1_v1_sec_trimmed.txt (RM8)</p>	<p>Rfx4_3761.1_v2_sec_trimmed.txt (RM8)</p>	<p>Rfx4_3761.1_RM8_combined_sec_trimmed.txt (RM8) No PWM Available</p>
<p>Rfx4_3761.1_v1_prim_untrimmed.txt (RM8)</p>	<p>Rfx4_3761.1_v2_prim_untrimmed.txt (RM8)</p>	<p>Rfx4_3761.1_RM8_combined_prim_untrimmed.txt (RM8) No PWM Available</p>
<p>Rfx4_3761.1_v1_sec_untrimmed.txt (RM8)</p>	<p>Rfx4_3761.1_v2_sec_untrimmed.txt (RM8)</p>	<p>Rfx4_3761.1_RM8_combined_sec_untrimmed.txt (RM8) No PWM Available</p>
<p>Rfx4_3761.1_v1_RM35_prim_trimmed.txt (RM35)</p>	<p>Rfx4_3761.1_v2_RM35_prim_trimmed.txt (RM35)</p>	<p>Rfx4_3761.1_RM_combined_prim_trimmed.txt (RM35)</p>

<p>Rfx4_3761.1_v1_RM35_sec_trimmed.txt (RM35)</p> 	<p>Rfx4_3761.1_v2_RM35_sec_trimmed.txt (RM35)</p> 	<p>Rfx4_3761.1_RM_combined_sec_trimmed.txt (RM35)</p> 
<p>Rfx4_3761.1_v1_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Rfx4_3761.1_v2_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Rfx4_3761.1_RM_combined_prim_untrimmed.txt (RM35)</p> 
<p>Rfx4_3761.1_v1_RM35_sec_untrimmed.txt (RM35)  [Z-Beta = 96.8355]  [Pearson Full 0.526423904, Pearson Zeroed 0.360756]</p> 	<p>Rfx4_3761.1_v2_RM35_sec_untrimmed.txt (RM35)  [Z-Beta = 11.8874]  [Pearson Full 0.501945586, Pearson Zeroed 0.479924]</p> 	<p>Rfx4_3761.1_RM_combined_sec_untrimmed.txt (RM35)  [Z-Beta = 56.3903]  [Pearson Full 0.615129856, Pearson Zeroed 0.524156]</p> 
<p>Rfx4_3761.1_PWM_1.txt (K-afal)  [Z-Beta = 15.0343]  [Pearson Full 0.526423904, Pearson Zeroed 0.502755]</p> 	<p>Rfx4_3761.1_PWM_1.txt (K-afal)  [Z-Beta = 78.0283]  [Pearson Full 0.501945586, Pearson Zeroed 0.449227]</p> 	<p>Rfx4_3761.1_combined_PWM_1.txt (K-afal)  [Z-Beta = 109.534]  [Pearson Full 0.615129856, Pearson Zeroed 0.574835]</p> 
<p>Rfx4_3761.1_PWM_2.txt (Kafal)  No PWM Available</p>	<p>Rfx4_3761.1_PWM_2.txt (Kafal)  [Z-Beta = 2.45865]  [Pearson Full 0.501945586, Pearson Zeroed 0.495957]</p> 	<p>Rfx4_3761.1_combined_PWM_2.txt (Kafal)</p> 
<p>Rfx4_3761.1_PWM_3.txt (Kafal)  No PWM Available</p>	<p>Rfx4_3761.1_PWM_3.txt (Kafal)</p> 	<p>Rfx4_3761.1_combined_PWM_3.txt (Kafal)</p> 
<p>Rfx4_3761.1_PWM_4.txt (Kafal)  No PWM Available</p>	<p>Rfx4_3761.1_PWM_4.txt (Kafal)</p> 	<p>Rfx4_3761.1_combined_PWM_4.txt (Kafal)  [Z-Beta = 8.8614]  [Pearson Full 0.615129856, Pearson Zeroed 0.596004]</p> 

# Rfxdc2\_3516.1

Array Version 1	Array Version 2	Combined
<p>Rfxdc2_3516.1_v1_prim_auc.txt (SW)  [Z-Beta = 11.5469]  [Pearson Full 0.544938474, Pearson Zeroed 0.538463]</p> 	<p>Rfxdc2_3516.1_v2_prim_auc.txt (SW)</p> 	<p>Rfxdc2_3516.1_combined_prim_auc.txt (SW)</p> 
<p>Rfxdc2_3516.1_v1_prim_ic.txt (SW)</p> 	<p>Rfxdc2_3516.1_v2_prim_ic.txt (SW)</p> 	<p>Rfxdc2_3516.1_combined_prim_ic_trimmed.txt (SW)</p> 
<p>Rfxdc2_3516.1_v1_prim_untrimmed.txt (SW)  [Z-Beta = 7.39141]  [Pearson Full 0.544938474, Pearson Zeroed 0.541218]</p> 	<p>Rfxdc2_3516.1_v2_prim_untrimmed.txt (SW)</p> 	<p>Rfxdc2_3516.1_combined_prim_untrimmed.txt (SW)</p> 
<p>Rfxdc2_3516.1_v1_sec_auc.txt (SW)</p> 	<p>Rfxdc2_3516.1_v2_sec_auc.txt (SW)  [Z-Beta = 7.68405]  [Pearson Full 0.499607834, Pearson Zeroed 0.487673]</p> 	<p>Rfxdc2_3516.1_combined_sec_auc.txt (SW)</p> 
<p>Rfxdc2_3516.1_v1_sec_ic.txt (SW)</p> 	<p>Rfxdc2_3516.1_v2_sec_ic.txt (SW)</p> 	<p>Rfxdc2_3516.1_combined_sec_ic_trimmed.txt (SW)</p> 
<p>Rfxdc2_3516.1_v1_sec_untrimmed.txt (SW)</p> 	<p>Rfxdc2_3516.1_v2_sec_untrimmed.txt (SW)  [Z-Beta = 0.456228]  [Pearson Full 0.499607834, Pearson Zeroed 0.499273]</p> 	<p>Rfxdc2_3516.1_combined_sec_untrimmed.txt (SW)</p> 
<p>Rfxdc2_3516.1_v1_prim_trimmed.txt (RM8)</p> 	<p>Rfxdc2_3516.1_v2_prim_trimmed.txt (RM8)</p> 	<p>Rfxdc2_3516.1_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Rfxdc2_3516.1_v1_sec_trimmed.txt (RM8)</p> 	<p>Rfxdc2_3516.1_v2_sec_trimmed.txt (RM8)</p> 	<p>Rfxdc2_3516.1_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Rfxdc2_3516.1_v1_prim_untrimmed.txt (RM8)</p> 	<p>Rfxdc2_3516.1_v2_prim_untrimmed.txt (RM8)</p> 	<p>Rfxdc2_3516.1_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Rfxdc2_3516.1_v1_sec_untrimmed.txt (RM8)</p> 	<p>Rfxdc2_3516.1_v2_sec_untrimmed.txt (RM8)  [Z-Beta = 0.456228]  [Pearson Full 0.499607834, Pearson Zeroed 0.499273]</p> 	<p>Rfxdc2_3516.1_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>




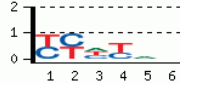



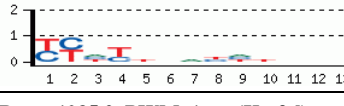






		
Rfxdc2_3516.1_v1_RM35_prim_trimmed.txt (RM35) [Z-Beta = 34.754] [Pearson Full 0.544938474, Pearson Zeroed 0.525309]	Rfxdc2_3516.1_v2_RM35_prim_trimmed.txt (RM35) [Z-Beta = 39.2707] [Pearson Full 0.499607834, Pearson Zeroed 0.501561]	Rfxdc2_3516.1_RM_combined_prim_trimmed.txt (RM35) [Z-Beta = 24.5889] [Pearson Full 0.595565612, Pearson Zeroed 0.593727]
		
Rfxdc2_3516.1_v1_RM35_sec_trimmed.txt (RM35)	Rfxdc2_3516.1_v2_RM35_sec_trimmed.txt (RM35)	Rfxdc2_3516.1_RM_combined_sec_trimmed.txt (RM35) [Z-Beta = 33.4821] [Pearson Full 0.595565612, Pearson Zeroed 0.569008]
		
Rfxdc2_3516.1_v1_RM35_prim_untrimmed.txt (RM35)	Rfxdc2_3516.1_v2_RM35_prim_untrimmed.txt (RM35)	Rfxdc2_3516.1_RM_combined_prim_untrimmed.txt (RM35)
		
Rfxdc2_3516.1_v1_RM35_sec_untrimmed.txt (RM35) [Z-Beta = 1.99186] [Pearson Full 0.544938474, Pearson Zeroed 0.543479]	Rfxdc2_3516.1_v2_RM35_sec_untrimmed.txt (RM35)	Rfxdc2_3516.1_RM_combined_sec_untrimmed.txt (RM35)
		
Rfxdc2_3516.1_PWM_1.txt (K-afal) [Z-Beta = 14.3724] [Pearson Full 0.544938474, Pearson Zeroed 0.527633]	Rfxdc2_3516.1_PWM_1.txt (K-afal) [Z-Beta = 56.0326] [Pearson Full 0.499607834, Pearson Zeroed 0.420724]	Rfxdc2_3516.1_combined_PWM_1.txt (K-afal) [Z-Beta = 32.3371] [Pearson Full 0.595565612, Pearson Zeroed 0.567549]
		
Rfxdc2_3516.1_PWM_2.txt (K-afal) [Z-Beta = 79.7639] [Pearson Full 0.544938474, Pearson Zeroed 0.51929]	Rfxdc2_3516.1_PWM_2.txt (K-afal) [Z-Beta = 56.2683] [Pearson Full 0.499607834, Pearson Zeroed 0.430921]	Rfxdc2_3516.1_combined_PWM_2.txt (K-afal) [Z-Beta = 26.0672] [Pearson Full 0.595565612, Pearson Zeroed 0.572458]
		
Rfxdc2_3516.1_PWM_3.txt (K-afal) [Z-Beta = 44.8301] [Pearson Full 0.544938474, Pearson Zeroed 0.538303]	Rfxdc2_3516.1_PWM_3.txt (K-afal) [Z-Beta = 14.0533] [Pearson Full 0.499607834, Pearson Zeroed 0.49537]	Rfxdc2_3516.1_combined_PWM_3.txt (K-afal) [Z-Beta = 55.6395] [Pearson Full 0.595565612, Pearson Zeroed 0.564087]
		
Rfxdc2_3516.1_PWM_4.txt (Kafal) No PWM Available	Rfxdc2_3516.1_PWM_4.txt (Kafal)	Rfxdc2_3516.1_combined_PWM_4.txt (Kafal)
		
Rfxdc2_3516.1_PWM_5.txt (Kafal) No PWM Available	Rfxdc2_3516.1_PWM_5.txt (Kafal)	Rfxdc2_3516.1_combined_PWM_5.txt (Kafal) [Z-Beta = 1.11727] [Pearson Full 0.595565612, Pearson Zeroed 0.594855]
		





# Rxra\_1035.2

Array Version 1	Array Version 2	Combined
<p>Rxra_1035.2_v1_prim_auc.txt (SW)</p>	<p>Rxra_1035.2_v2_prim_auc.txt (SW)  [Z-Beta = 8.89801]  [Pearson Full 0.437967332, Pearson Zeroed 0.438921]</p>	<p>Rxra_1035.2_combined_prim_auc.txt (SW)</p>
<p>Rxra_1035.2_v1_prim_ic.txt (SW)</p>	<p>Rxra_1035.2_v2_prim_ic.txt (SW)  [Z-Beta = 1.70526]  [Pearson Full 0.437967332, Pearson Zeroed 0.437923]</p>	<p>Rxra_1035.2_combined_prim_ic_trimmed.txt (SW)  [Z-Beta = 3.55751]  [Pearson Full 0.65222692, Pearson Zeroed 0.65211]</p>
<p>Rxra_1035.2_v1_prim_untrimmed.txt (SW)</p>	<p>Rxra_1035.2_v2_prim_untrimmed.txt (SW)</p>	<p>Rxra_1035.2_combined_prim_untrimmed.txt (SW)  [Z-Beta = 141.293]  [Pearson Full 0.65222692, Pearson Zeroed 0.583719]</p>
<p>Rxra_1035.2_v1_sec_auc.txt (SW)</p>	<p>Rxra_1035.2_v2_sec_auc.txt (SW)</p>	<p>Rxra_1035.2_combined_sec_auc.txt (SW)</p>
<p>Rxra_1035.2_v1_sec_ic.txt (SW)</p>	<p>Rxra_1035.2_v2_sec_ic.txt (SW)</p>	<p>Rxra_1035.2_combined_sec_ic_trimmed.txt (SW)</p>
<p>Rxra_1035.2_v1_sec_untrimmed.txt (SW)</p>	<p>Rxra_1035.2_v2_sec_untrimmed.txt (SW)  [Z-Beta = 1.8696]  [Pearson Full 0.437967332, Pearson Zeroed 0.436401]</p>	<p>Rxra_1035.2_combined_sec_untrimmed.txt (SW)</p>
<p>Rxra_1035.2_v1_prim_trimmed.txt (RM8)</p>	<p>Rxra_1035.2_v2_prim_trimmed.txt (RM8)</p>	<p>Rxra_1035.2_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Rxra_1035.2_v1_sec_trimmed.txt (RM8)</p>	<p>Rxra_1035.2_v2_sec_trimmed.txt (RM8)</p>	<p>Rxra_1035.2_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Rxra_1035.2_v1_prim_untrimmed.txt (RM8)</p>	<p>Rxra_1035.2_v2_prim_untrimmed.txt (RM8)</p>	<p>Rxra_1035.2_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Rxra_1035.2_v1_sec_untrimmed.txt (RM8)</p>	<p>Rxra_1035.2_v2_sec_untrimmed.txt (RM8)  [Z-Beta = 1.8696]  [Pearson Full 0.437967332, Pearson Zeroed 0.436401]</p>	<p>Rxra_1035.2_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>



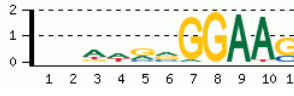






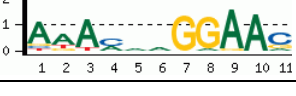




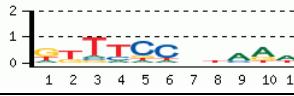

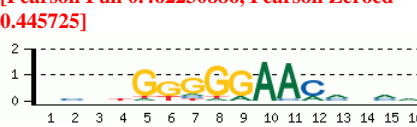


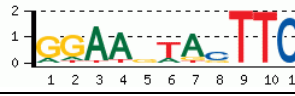


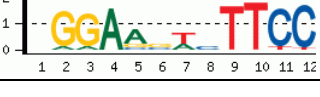


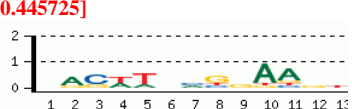
<p>Rxra_1035.2_v1_RM35_prim_trimmed.txt (RM35)</p> 	<p>Rxra_1035.2_v2_RM35_prim_trimmed.txt (RM35)</p> 	<p>Rxra_1035.2_RM_combined_prim_trimmed.txt (RM35)</p> 
<p>Rxra_1035.2_v1_RM35_sec_trimmed.txt (RM35)</p> 	<p>Rxra_1035.2_v2_RM35_sec_trimmed.txt (RM35)  [Z-Beta = 5.62735]  [Pearson Full 0.437967332, Pearson Zeroed 0.428348]</p> 	<p>Rxra_1035.2_RM_combined_sec_trimmed.txt (RM35)</p> 
<p>Rxra_1035.2_v1_RM35_prim_untrimmed.txt (RM35)  [Z-Beta = 162.394]  [Pearson Full 0.513342358, Pearson Zeroed NaN]</p> 	<p>Rxra_1035.2_v2_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Rxra_1035.2_RM_combined_prim_untrimmed.txt (RM35)  [Z-Beta = 169.288]  [Pearson Full 0.65222692, Pearson Zeroed 0.645108]</p> 
<p>Rxra_1035.2_v1_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Rxra_1035.2_v2_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Rxra_1035.2_RM_combined_sec_untrimmed.txt (RM35)</p> 
<p>Rxra_1035.2_PWM_1.txt (K-afal)</p> 	<p>Rxra_1035.2_PWM_1.txt (K-afal)  [Z-Beta = 28.3263]  [Pearson Full 0.437967332, Pearson Zeroed 0.437689]</p> 	<p>Rxra_1035.2_combined_PWM_1.txt (K-afal)</p> 
<p>Rxra_1035.2_PWM_2.txt (Kafal)  No PWM Available</p>	<p>Rxra_1035.2_PWM_2.txt (Kafal)  [Z-Beta = 12.7781]  [Pearson Full 0.437967332, Pearson Zeroed 0.423157]</p> 	<p>Rxra_1035.2_combined_PWM_2.txt (Kafal)</p> 
<p>Rxra_1035.2_PWM_3.txt (Kafal)  No PWM Available</p>	<p>Rxra_1035.2_PWM_3.txt (Kafal)  [Z-Beta = 57.9633]  [Pearson Full 0.437967332, Pearson Zeroed 0.449249]</p> 	<p>Rxra_1035.2_combined_PWM_3.txt (Kafal)  [Z-Beta = 18.3609]  [Pearson Full 0.65222692, Pearson Zeroed 0.638615]</p> 
<p>Rxra_1035.2_PWM_4.txt (Kafal)  No PWM Available</p>	<p>Rxra_1035.2_PWM_4.txt (Kafal)  [Z-Beta = 16.8134]  [Pearson Full 0.437967332, Pearson Zeroed 0.423043]</p> 	<p>Rxra_1035.2_combined_PWM_4.txt (Kafal)</p> 
<p>Rxra_1035.2_PWM_5.txt (Kafal)  No PWM Available</p>	<p>Rxra_1035.2_PWM_5.txt (Kafal)</p> 	<p>Rxra_1035.2_combined_PWM_5.txt (Kafal)</p> 


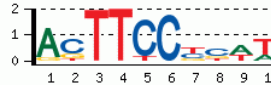

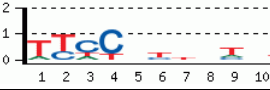
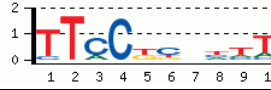
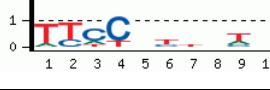



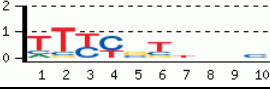

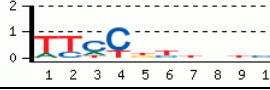

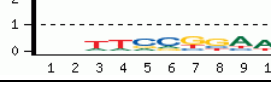
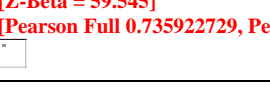
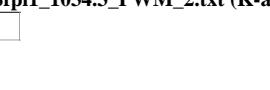









# Sfpi1\_1034.2

Array Version 1	Array Version 2	Combined
<p>Sfpi1_1034.2_v1_prim_auc.txt (SW)  [Z-Beta = 126.597]  [Pearson Full 0.787616377, Pearson Zeroed 0.76833]</p>	<p>Sfpi1_1034.2_v2_prim_auc.txt (SW)</p>	<p>Sfpi1_1034.2_combined_prim_auc.txt (SW)</p>
<p>Sfpi1_1034.2_v1_prim_ic.txt (SW)  [Z-Beta = 80.291]  [Pearson Full 0.787616377, Pearson Zeroed 0.776076]</p>	<p>Sfpi1_1034.2_v2_prim_ic.txt (SW)</p>	<p>Sfpi1_1034.2_combined_prim_ic_trimmed.txt (SW)</p>
<p>Sfpi1_1034.2_v1_prim_untrimmed.txt (SW)  [Z-Beta = 4.1562]  [Pearson Full 0.787616377, Pearson Zeroed 0.787449]</p>	<p>Sfpi1_1034.2_v2_prim_untrimmed.txt (SW)</p>	<p>Sfpi1_1034.2_combined_prim_untrimmed.txt (SW)</p>
<p>Sfpi1_1034.2_v1_sec_auc.txt (SW)</p>	<p>Sfpi1_1034.2_v2_sec_auc.txt (SW)</p>	<p>Sfpi1_1034.2_combined_sec_auc.txt (SW)</p>
<p>Sfpi1_1034.2_v1_sec_ic.txt (SW)</p>	<p>Sfpi1_1034.2_v2_sec_ic.txt (SW)  [Z-Beta = 0.693776]  [Pearson Full 0.728891234, Pearson Zeroed 0.728587]</p>	<p>Sfpi1_1034.2_combined_sec_ic_trimmed.txt (SW)  [Z-Beta = 51.0996]  [Pearson Full 0.764750644, Pearson Zeroed 0.720172]</p>
<p>Sfpi1_1034.2_v1_sec_untrimmed.txt (SW)</p>	<p>Sfpi1_1034.2_v2_sec_untrimmed.txt (SW)</p>	<p>Sfpi1_1034.2_combined_sec_untrimmed.txt (SW)</p>
<p>Sfpi1_1034.2_v1_prim_trimmed.txt (RM8)</p>	<p>Sfpi1_1034.2_v2_prim_trimmed.txt (RM8)</p>	<p>Sfpi1_1034.2_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Sfpi1_1034.2_v1_sec_trimmed.txt (RM8)</p>	<p>Sfpi1_1034.2_v2_sec_trimmed.txt (RM8)  [Z-Beta = 2.63542]  [Pearson Full 0.728891234, Pearson Zeroed 0.727387]</p>	<p>Sfpi1_1034.2_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Sfpi1_1034.2_v1_prim_untrimmed.txt (RM8)</p>	<p>Sfpi1_1034.2_v2_prim_untrimmed.txt (RM8)</p>	<p>Sfpi1_1034.2_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Sfpi1_1034.2_v1_sec_untrimmed.txt (RM8)</p>	<p>Sfpi1_1034.2_v2_sec_untrimmed.txt (RM8)</p>	<p>Sfpi1_1034.2_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>

SfpI1_1034.2_v1_RM35_prim_trimmed.txt (RM35) 	SfpI1_1034.2_v2_RM35_prim_trimmed.txt (RM35) 	SfpI1_1034.2_RM_combined_prim_trimmed.txt (RM35) 
SfpI1_1034.2_v1_RM35_sec_trimmed.txt (RM35) <input type="checkbox"/>	SfpI1_1034.2_v2_RM35_sec_trimmed.txt (RM35) [Z-Beta = 32.4079] [Pearson Full 0.728891234, Pearson Zeroed 0.71827] 	SfpI1_1034.2_RM_combined_sec_trimmed.txt (RM35) 
SfpI1_1034.2_v1_RM35_prim_untrimmed.txt (RM35) 	SfpI1_1034.2_v2_RM35_prim_untrimmed.txt (RM35) 	SfpI1_1034.2_RM_combined_prim_untrimmed.txt (RM35) 
SfpI1_1034.2_v1_RM35_sec_untrimmed.txt (RM35) [Z-Beta = 50.0115] [Pearson Full 0.787616377, Pearson Zeroed 0.775619] 	SfpI1_1034.2_v2_RM35_sec_untrimmed.txt (RM35) 	SfpI1_1034.2_RM_combined_sec_untrimmed.txt (RM35) 
SfpI1_1034.2_PWM_1.txt (K-afal) [Z-Beta = 263.602] [Pearson Full 0.787616377, Pearson Zeroed 0.722888] <input type="checkbox"/>	SfpI1_1034.2_PWM_1.txt (K-afal) [Z-Beta = 65.4316] [Pearson Full 0.728891234, Pearson Zeroed 0.710183] <input type="checkbox"/>	SfpI1_1034.2_combined_PWM_1.txt (K-afal) [Z-Beta = 335.48] [Pearson Full 0.764750644, Pearson Zeroed 0.63293] 
SfpI1_1034.2_PWM_2.txt (K-afal) [Z-Beta = 84.513] [Pearson Full 0.787616377, Pearson Zeroed 0.751759] 	SfpI1_1034.2_PWM_2.txt (K-afal) [Z-Beta = 128.987] [Pearson Full 0.728891234, Pearson Zeroed 0.713748] 	SfpI1_1034.2_combined_PWM_2.txt (K-afal) [Z-Beta = 20.9329] [Pearson Full 0.764750644, Pearson Zeroed 0.761421] 
SfpI1_1034.2_PWM_3.txt (K-afal) 	SfpI1_1034.2_PWM_3.txt (K-afal) 	SfpI1_1034.2_combined_PWM_3.txt (K-afal) 
SfpI1_1034.2_PWM_4.txt (K-afal) 	SfpI1_1034.2_PWM_4.txt (K-afal) [Z-Beta = 141.647] [Pearson Full 0.728891234, Pearson Zeroed 0.740184] 	SfpI1_1034.2_combined_PWM_4.txt (K-afal) No PWM Available

# Sfpi1\_1034.3

Array Version 1	Array Version 2	Combined
<p>Sfpi1_1034.3_v1_prim_auc.txt (SW)  [Z-Beta = 92.162]  [Pearson Full 0.735133593, Pearson Zeroed 0.697103]</p> 	<p>Sfpi1_1034.3_v2_prim_auc.txt (SW)</p> 	<p>Sfpi1_1034.3_combined_prim_auc.txt (SW)</p> 
<p>Sfpi1_1034.3_v1_prim_ic.txt (SW)</p> 	<p>Sfpi1_1034.3_v2_prim_ic.txt (SW)</p> 	<p>Sfpi1_1034.3_combined_prim_ic_trimmed.txt (SW)</p> 
<p>Sfpi1_1034.3_v1_prim_untrimmed.txt (SW)  [Z-Beta = 0.972451]  [Pearson Full 0.735133593, Pearson Zeroed 0.735036]</p> 	<p>Sfpi1_1034.3_v2_prim_untrimmed.txt (SW)</p> 	<p>Sfpi1_1034.3_combined_prim_untrimmed.txt (SW)  [Z-Beta = 86.0873]  [Pearson Full 0.735922729, Pearson Zeroed 0.727177]</p> 
<p>Sfpi1_1034.3_v1_sec_auc.txt (SW)</p> 	<p>Sfpi1_1034.3_v2_sec_auc.txt (SW)</p> 	<p>Sfpi1_1034.3_combined_sec_auc.txt (SW)</p> 
<p>Sfpi1_1034.3_v1_sec_ic.txt (SW)</p> 	<p>Sfpi1_1034.3_v2_sec_ic.txt (SW)</p> 	<p>Sfpi1_1034.3_combined_sec_ic_trimmed.txt (SW)  [Z-Beta = 8.1625]  [Pearson Full 0.735922729, Pearson Zeroed 0.728838]</p> 
<p>Sfpi1_1034.3_v1_sec_untrimmed.txt (SW)</p> 	<p>Sfpi1_1034.3_v2_sec_untrimmed.txt (SW)  [Z-Beta = 6.88258]  [Pearson Full 0.462230886, Pearson Zeroed 0.445725]</p> 	<p>Sfpi1_1034.3_combined_sec_untrimmed.txt (SW)</p> 
<p>Sfpi1_1034.3_v1_prim_trimmed.txt (RM8)</p> 	<p>Sfpi1_1034.3_v2_prim_trimmed.txt (RM8)</p> 	<p>Sfpi1_1034.3_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Sfpi1_1034.3_v1_sec_trimmed.txt (RM8)</p> 	<p>Sfpi1_1034.3_v2_sec_trimmed.txt (RM8)</p> 	<p>Sfpi1_1034.3_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Sfpi1_1034.3_v1_prim_untrimmed.txt (RM8)</p> 	<p>Sfpi1_1034.3_v2_prim_untrimmed.txt (RM8)</p> 	<p>Sfpi1_1034.3_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Sfpi1_1034.3_v1_sec_untrimmed.txt (RM8)</p> 	<p>Sfpi1_1034.3_v2_sec_untrimmed.txt (RM8)  [Z-Beta = 6.88258]  [Pearson Full 0.462230886, Pearson Zeroed 0.445725]</p> 	<p>Sfpi1_1034.3_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>

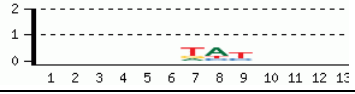





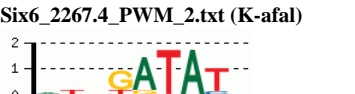
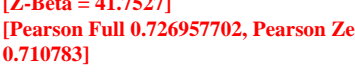

<p>Sfpi1_1034.3_v1_RM35_prim_trimmed.txt (RM35)</p> 	<p>Sfpi1_1034.3_v2_RM35_prim_trimmed.txt (RM35)</p> 	<p>Sfpi1_1034.3_RM_combined_prim_trimmed.txt (RM35)</p> <p>[Z-Beta = 37.1033] [Pearson Full 0.735922729, Pearson Zeroed 0.737466]</p> 
<p>Sfpi1_1034.3_v1_RM35_sec_trimmed.txt (RM35)</p> <p>[Z-Beta = 8.45905] [Pearson Full 0.735133593, Pearson Zeroed 0.730506]</p> 	<p>Sfpi1_1034.3_v2_RM35_sec_trimmed.txt (RM35)</p> <p>[Z-Beta = 26.9154] [Pearson Full 0.462230886, Pearson Zeroed 0.399122]</p> 	<p>Sfpi1_1034.3_RM_combined_sec_trimmed.txt (RM35)</p> <p>[Z-Beta = 70.3242] [Pearson Full 0.735922729, Pearson Zeroed 0.656684]</p> 
<p>Sfpi1_1034.3_v1_RM35_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = 20.3792] [Pearson Full 0.735133593, Pearson Zeroed 0.733127]</p> 	<p>Sfpi1_1034.3_v2_RM35_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = 21.052] [Pearson Full 0.462230886, Pearson Zeroed 0.456256]</p> 	<p>Sfpi1_1034.3_RM_combined_prim_untrimmed.txt (RM35)</p> 
<p>Sfpi1_1034.3_v1_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Sfpi1_1034.3_v2_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Sfpi1_1034.3_RM_combined_sec_untrimmed.txt (RM35)</p> 
<p>Sfpi1_1034.3_PWM_1.txt (K-afal)</p> 	<p>Sfpi1_1034.3_PWM_1.txt (K-afal)</p> 	<p>Sfpi1_1034.3_combined_PWM_1.txt (K-afal)</p> <p>[Z-Beta = 59.545] [Pearson Full 0.735922729, Pearson Zeroed 0.709649]</p> 
<p>Sfpi1_1034.3_PWM_2.txt (K-afal)</p> 	<p>Sfpi1_1034.3_PWM_2.txt (K-afal)</p> <p>No PWM Available</p>	<p>Sfpi1_1034.3_combined_PWM_2.txt (K-afal)</p> <p>[Z-Beta = 33.2138] [Pearson Full 0.735922729, Pearson Zeroed 0.732461]</p> 
<p>Sfpi1_1034.3_PWM_3.txt (K-afal)</p> 	<p>Sfpi1_1034.3_PWM_3.txt (K-afal)</p> <p>No PWM Available</p>	<p>Sfpi1_1034.3_combined_PWM_3.txt (K-afal)</p> 
<p>Sfpi1_1034.3_PWM_4.txt (K-afal)</p> <p>[Z-Beta = 219.46] [Pearson Full 0.735133593, Pearson Zeroed 0.603685]</p> 	<p>Sfpi1_1034.3_PWM_4.txt (K-afal)</p> <p>No PWM Available</p>	<p>Sfpi1_1034.3_combined_PWM_4.txt (K-afal)</p> 
<p>Sfpi1_1034.3_PWM_5.txt (K-afal)</p> 	<p>Sfpi1_1034.3_PWM_5.txt (K-afal)</p> <p>No PWM Available</p>	<p>Sfpi1_1034.3_combined_PWM_5.txt (K-afal)</p> 
<p>Sfpi1_1034.3_PWM_6.txt (K-afal)</p> 	<p>Sfpi1_1034.3_PWM_6.txt (K-afal)</p> <p>No PWM Available</p>	<p>Sfpi1_1034.3_combined_PWM_6.txt (K-afal)</p> 





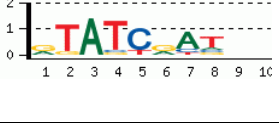

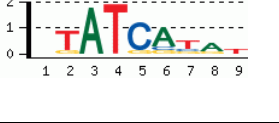







# Six6\_2267.4

Array Version 1	Array Version 2	Combined
<p>Six6_2267.4_v1_prim_auc.txt (SW)</p>	<p>Six6_2267.4_v2_prim_auc.txt (SW)</p>	<p>Six6_2267.4_combined_prim_auc.txt (SW)</p>
<p>Six6_2267.4_v1_prim_ic.txt (SW) [Z-Beta = -78.7257] [Pearson Full 0.726957702, Pearson Zeroed 0.682862]</p>	<p>Six6_2267.4_v2_prim_ic.txt (SW)</p>	<p>Six6_2267.4_combined_prim_ic_trimmed.txt (SW) [Z-Beta = -38.3115] [Pearson Full 0.525747314, Pearson Zeroed 0.511639]</p>
<p>Six6_2267.4_v1_prim_untrimmed.txt (SW) [Z-Beta = 87.9046] [Pearson Full 0.726957702, Pearson Zeroed 0.645282]</p>	<p>Six6_2267.4_v2_prim_untrimmed.txt (SW)</p>	<p>Six6_2267.4_combined_prim_untrimmed.txt (SW) [Z-Beta = 64.6749] [Pearson Full 0.525747314, Pearson Zeroed 0.46967]</p>
<p>Six6_2267.4_v1_sec_auc.txt (SW) [Z-Beta = 35.1851] [Pearson Full 0.726957702, Pearson Zeroed 0.71625]</p>	<p>Six6_2267.4_v2_sec_auc.txt (SW)</p>	<p>Six6_2267.4_combined_sec_auc.txt (SW)</p>
<p>Six6_2267.4_v1_sec_ic.txt (SW) [Z-Beta = 5.27327] [Pearson Full 0.726957702, Pearson Zeroed 0.726692]</p>	<p>Six6_2267.4_v2_sec_ic.txt (SW)</p>	<p>Six6_2267.4_combined_sec_ic_trimmed.txt (SW) [Z-Beta = 2412.14] [Pearson Full 0.525747314, Pearson Zeroed 0.0669924]</p>
<p>Six6_2267.4_v1_sec_untrimmed.txt (SW) [Z-Beta = -10.5492] [Pearson Full 0.726957702, Pearson Zeroed 0.726078]</p>	<p>Six6_2267.4_v2_sec_untrimmed.txt (SW) [Z-Beta = 143.299] [Pearson Full 0.671789583, Pearson Zeroed 0.459013]</p>	<p>Six6_2267.4_combined_sec_untrimmed.txt (SW) [Z-Beta = 33.6771] [Pearson Full 0.525747314, Pearson Zeroed 0.511345]</p>
<p>Six6_2267.4_v1_prim_trimmed.txt (RM8) [Z-Beta = 10.25] [Pearson Full 0.726957702, Pearson Zeroed 0.726006]</p>	<p>Six6_2267.4_v2_prim_trimmed.txt (RM8)</p>	<p>Six6_2267.4_RM8_combined_prim_trimmed.txt (RM8) No PWM Available</p>
<p>Six6_2267.4_v1_sec_trimmed.txt (RM8) [Z-Beta = 78.487] [Pearson Full 0.726957702, Pearson Zeroed 0.684171]</p>	<p>Six6_2267.4_v2_sec_trimmed.txt (RM8) [Z-Beta = 10.1143] [Pearson Full 0.671789583, Pearson Zeroed 0.670604]</p>	<p>Six6_2267.4_RM8_combined_sec_trimmed.txt (RM8) No PWM Available</p>
<p>Six6_2267.4_v1_prim_untrimmed.txt (RM8) [Z-Beta = -10.4594] [Pearson Full 0.726957702, Pearson Zeroed 0.684171]</p>	<p>Six6_2267.4_v2_prim_untrimmed.txt (RM8)</p>	<p>Six6_2267.4_RM8_combined_prim_untrimmed.txt (RM8) No PWM Available</p>

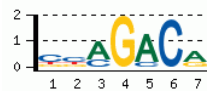
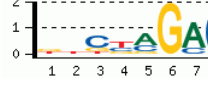










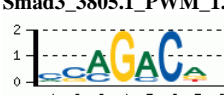
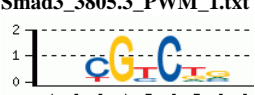
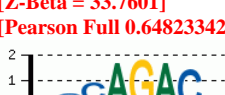


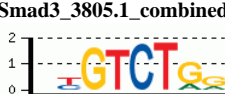
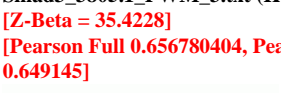
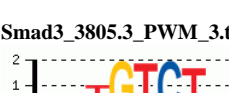
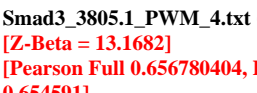
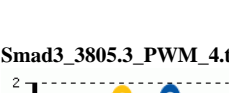
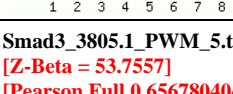


0.725989] 		
Six6_2267.4_v1_sec_untrimmed.txt (RM8) [Z-Beta = 13.7213] [Pearson Full 0.726957702, Pearson Zeroed 0.726329] 	Six6_2267.4_v2_sec_untrimmed.txt (RM8) [Z-Beta = 143.299] [Pearson Full 0.671789583, Pearson Zeroed 0.459013] 	Six6_2267.4_RM8_combined_sec_untrimmed.txt (RM8) No PWM Available
Six6_2267.4_v1_RM35_prim_trimmed.txt (RM35) [Z-Beta = 43.0425] [Pearson Full 0.726957702, Pearson Zeroed 0.715684] 	Six6_2267.4_v2_RM35_prim_trimmed.txt (RM35) 	Six6_2267.4_RM_combined_prim_trimmed.txt (RM35) [Z-Beta = -9.35271] [Pearson Full 0.525747314, Pearson Zeroed 0.525184] 
Six6_2267.4_v1_RM35_sec_trimmed.txt (RM35) [Z-Beta = 41.9139] [Pearson Full 0.726957702, Pearson Zeroed 0.714121] 	Six6_2267.4_v2_RM35_sec_trimmed.txt (RM35) 	Six6_2267.4_RM_combined_sec_trimmed.txt (RM35) [Z-Beta = 92.6175] [Pearson Full 0.525747314, Pearson Zeroed 0.420213] 
Six6_2267.4_v1_RM35_prim_untrimmed.txt (RM35) [Z-Beta = -8.31656] [Pearson Full 0.726957702, Pearson Zeroed 0.72642] 	Six6_2267.4_v2_RM35_prim_untrimmed.txt (RM35) 	Six6_2267.4_RM_combined_prim_untrimmed.txt (RM35) [Z-Beta = 25.4028] [Pearson Full 0.525747314, Pearson Zeroed 0.520617] 
Six6_2267.4_v1_RM35_sec_untrimmed.txt (RM35) [Z-Beta = 38.2909] [Pearson Full 0.726957702, Pearson Zeroed 0.713697] 	Six6_2267.4_v2_RM35_sec_untrimmed.txt (RM35) 	Six6_2267.4_RM_combined_sec_untrimmed.txt (RM35) [Z-Beta = 13.7974] [Pearson Full 0.525747314, Pearson Zeroed 0.523257] 
Six6_2267.4_PWM_1.txt (K-afal) [Z-Beta = 45.4851] [Pearson Full 0.726957702, Pearson Zeroed 0.714364] 	Six6_2267.4_PWM_1.txt (K-afal) 	Six6_2267.4_combined_PWM_1.txt (K-afal) [Z-Beta = 102.977] [Pearson Full 0.525747314, Pearson Zeroed 0.413221] 
Six6_2267.4_PWM_2.txt (K-afal) [Z-Beta = 11.4429] [Pearson Full 0.726957702, Pearson Zeroed 0.726064] 	Six6_2267.4_PWM_2.txt (K-afal) 	Six6_2267.4_combined_PWM_2.txt (K-afal) No PWM Available
Six6_2267.4_PWM_3.txt (K-afal) [Z-Beta = 41.7527] [Pearson Full 0.726957702, Pearson Zeroed 0.710783] 	Six6_2267.4_PWM_3.txt (K-afal) 	Six6_2267.4_combined_PWM_3.txt (K-afal) No PWM Available

		
<p>Six6_2267.4_PWM_4.txt (K-afal)  [Z-Beta = 53.0653]  [Pearson Full 0.726957702, Pearson Zeroed 0.705801]</p> 	<p>Six6_2267.4_PWM_4.txt (K-afal)  [Z-Beta = 19.7475]  [Pearson Full 0.671789583, Pearson Zeroed 0.646466]</p> 	<p>Six6_2267.4_combined_PWM_4.txt (K-afal)  No PWM Available</p>
<p>Six6_2267.4_PWM_5.txt (K-afal)  [Z-Beta = -27.6013]  [Pearson Full 0.726957702, Pearson Zeroed 0.721637]</p> 	<p>Six6_2267.4_PWM_5.txt (K-afal)</p> 	<p>Six6_2267.4_combined_PWM_5.txt (K-afal)  No PWM Available</p>
<p>Six6_2267.4_PWM_6.txt (K-afal)  [Z-Beta = 11.3166]  [Pearson Full 0.726957702, Pearson Zeroed 0.725873]</p> 	<p>Six6_2267.4_PWM_6.txt (K-afal)</p> 	<p>Six6_2267.4_combined_PWM_6.txt (K-afal)  No PWM Available</p>
<p>Six6_2267.4_PWM_7.txt (K-afal)  [Z-Beta = -17.0643]  [Pearson Full 0.726957702, Pearson Zeroed 0.724892]</p> 	<p>Six6_2267.4_PWM_7.txt (K-afal)</p> 	<p>Six6_2267.4_combined_PWM_7.txt (K-afal)  No PWM Available</p>
<p>Six6_2267.4_PWM_8.txt (K-afal)  [Z-Beta = 30.46]  [Pearson Full 0.726957702, Pearson Zeroed 0.718197]</p> 	<p>Six6_2267.4_PWM_8.txt (K-afal)</p> 	<p>Six6_2267.4_combined_PWM_8.txt (K-afal)  No PWM Available</p>
<p>Six6_2267.4_PWM_9.txt (K-afal)  [Z-Beta = 15.335]  [Pearson Full 0.726957702, Pearson Zeroed 0.72511]</p> 	<p>Six6_2267.4_PWM_9.txt (K-afal)  No PWM Available</p>	<p>Six6_2267.4_combined_PWM_9.txt (K-afal)  No PWM Available</p>

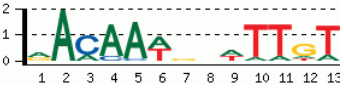

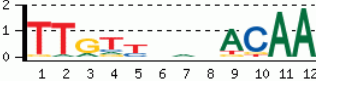
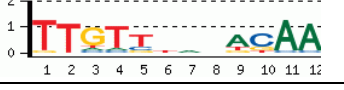
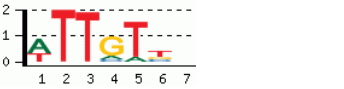

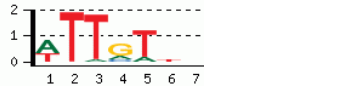
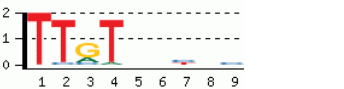


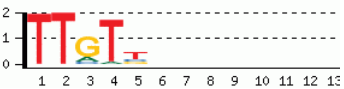
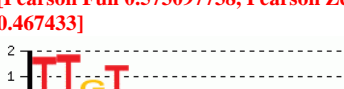

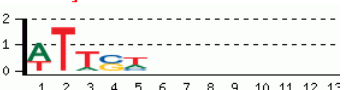
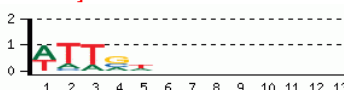
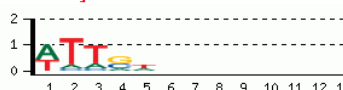



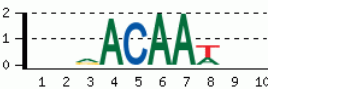


# Smad3\_3805.3

Array Version 1	Array Version 2	Combined
<p>Smad3_3805.1_v1_prim_auc.txt (SW)</p>	<p>Smad3_3805.3_v2_prim_auc.txt (SW)</p>	<p>Smad3_3805.3_combined_prim_auc.txt (SW)</p> <p>No PWM Available</p>
<p>Smad3_3805.1_v1_prim_ic.txt (SW)</p> <p>[Z-Beta = 3.84298] [Pearson Full 0.656780404, Pearson Zeroed 0.656801]</p>	<p>Smad3_3805.3_v2_prim_ic.txt (SW)</p>	<p>Smad3_3805.3_combined_prim_ic_trimmed.txt (SW)</p>
<p>Smad3_3805.1_v1_prim_untrimmed.txt (SW)</p> <p>[Z-Beta = 28.8674] [Pearson Full 0.656780404, Pearson Zeroed 0.652398]</p>	<p>Smad3_3805.3_v2_prim_untrimmed.txt (SW)</p>	<p>Smad3_3805.1_combined_prim_untrimmed.txt (SW)</p>
<p>Smad3_3805.1_v1_sec_auc.txt (SW)</p>	<p>Smad3_3805.3_v2_sec_auc.txt (SW)</p>	<p>Smad3_3805.1_combined_sec_auc.txt (SW)</p>
<p>Smad3_3805.1_v1_sec_ic.txt (SW)</p>	<p>Smad3_3805.3_v2_sec_ic.txt (SW)</p>	<p>Smad3_3805.1_combined_sec_ic_trimmed.txt (SW)</p> <p>[Z-Beta = 24.109] [Pearson Full 0.648233428, Pearson Zeroed 0.6317]</p>
<p>Smad3_3805.1_v1_sec_untrimmed.txt (SW)</p>	<p>Smad3_3805.3_v2_sec_untrimmed.txt (SW)</p>	<p>Smad3_3805.1_combined_sec_untrimmed.txt (SW)</p> <p>[Z-Beta = 16.5559] [Pearson Full 0.648233428, Pearson Zeroed 0.638008]</p>
<p>Smad3_3805.1_v1_prim_trimmed.txt (RM8)</p>	<p>Smad3_3805.3_v2_prim_trimmed.txt (RM8)</p>	<p>Smad3_3805.1_RM8_combined_prim_trimmed.txt (RM8)</p> <p>No PWM Available</p>
<p>Smad3_3805.1_v1_sec_trimmed.txt (RM8)</p>	<p>Smad3_3805.3_v2_sec_trimmed.txt (RM8)</p>	<p>Smad3_3805.1_RM8_combined_sec_trimmed.txt (RM8)</p> <p>No PWM Available</p>
<p>Smad3_3805.1_v1_prim_untrimmed.txt (RM8)</p>	<p>Smad3_3805.3_v2_prim_untrimmed.txt (RM8)</p>	<p>Smad3_3805.1_RM8_combined_prim_untrimmed.txt (RM8)</p> <p>No PWM Available</p>
<p>Smad3_3805.1_v1_sec_untrimmed.txt (RM8)</p>	<p>Smad3_3805.3_v2_sec_untrimmed.txt (RM8)</p>	<p>Smad3_3805.1_RM8_combined_sec_untrimmed.txt (RM8)</p> <p>No PWM Available</p>
<p>Smad3_3805.1_v1_RM35_prim_trimmed.txt (RM35)</p> <p>[Z-Beta = 64.01] [Pearson Full 0.656780404, Pearson Zeroed 0.656801]</p>	<p>Smad3_3805.3_v2_RM35_prim_trimmed.txt (RM35)</p>	<p>Smad3_3805.1_RM_combined_prim_trimmed.txt (RM35)</p> <p>[Z-Beta = 67.5237] [Pearson Full 0.648233428, Pearson Zeroed 0.639261]</p>

<p>0.664585]</p> 		
<p>Smad3_3805.1_v1_RM35_sec_trimmed.txt (RM35)</p> 	<p>Smad3_3805.3_v2_RM35_sec_trimmed.txt (RM35)</p> 	<p>Smad3_3805.1_RM_combined_sec_trimmed.txt (RM35)</p> <p>[Z-Beta = 155.626] [Pearson Full 0.648233428, Pearson Zeroed 0.60849]</p> 
<p>Smad3_3805.1_v1_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Smad3_3805.3_v2_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Smad3_3805.1_RM_combined_prim_untrimmed.txt (RM35)</p> 
<p>Smad3_3805.1_v1_RM35_sec_untrimmed.txt (RM35)</p> <p>[Z-Beta = 116.677] [Pearson Full 0.656780404, Pearson Zeroed 0.656648]</p> 	<p>Smad3_3805.3_v2_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Smad3_3805.1_RM_combined_sec_untrimmed.txt (RM35)</p> 
<p>Smad3_3805.1_PWM_1.txt (K-afal)</p> 	<p>Smad3_3805.3_PWM_1.txt (K-afal)</p> 	<p>Smad3_3805.1_combined_PWM_1.txt (K-afal)</p> <p>[Z-Beta = 33.7601] [Pearson Full 0.648233428, Pearson Zeroed 0.642673]</p> 
<p>Smad3_3805.1_PWM_2.txt (K-afal)</p> <p>[Z-Beta = 24.9298] [Pearson Full 0.656780404, Pearson Zeroed 0.644787]</p> 	<p>Smad3_3805.3_PWM_2.txt (K-afal)</p> 	<p>Smad3_3805.1_combined_PWM_2.txt (K-afal)</p> 
<p>Smad3_3805.1_PWM_3.txt (K-afal)</p> <p>[Z-Beta = 35.4228] [Pearson Full 0.656780404, Pearson Zeroed 0.649145]</p> 	<p>Smad3_3805.3_PWM_3.txt (K-afal)</p> 	<p>Smad3_3805.1_combined_PWM_3.txt (K-afal)</p> <p>No PWM Available</p>
<p>Smad3_3805.1_PWM_4.txt (K-afal)</p> <p>[Z-Beta = 13.1682] [Pearson Full 0.656780404, Pearson Zeroed 0.654591]</p> 	<p>Smad3_3805.3_PWM_4.txt (K-afal)</p> 	<p>Smad3_3805.1_combined_PWM_4.txt (K-afal)</p> <p>No PWM Available</p>
<p>Smad3_3805.1_PWM_5.txt (K-afal)</p> <p>[Z-Beta = 53.7557] [Pearson Full 0.656780404, Pearson Zeroed 0.609189]</p> 	<p>Smad3_3805.3_PWM_5.txt (K-afal)</p> <p>No PWM Available</p>	<p>Smad3_3805.1_combined_PWM_5.txt (K-afal)</p> <p>No PWM Available</p>

# Sox11\_2266.2

Array Version 1	Array Version 2	Combined
<p>Sox11_2266.2_v1_prim_auc.txt (SW)</p>	<p>Sox11_2266.2_v2_prim_auc.txt (SW)</p>	<p>Sox11_2266.2_combined_prim_auc.txt (SW)</p>
<p>Sox11_2266.2_v1_prim_ic.txt (SW)</p>	<p>Sox11_2266.2_v2_prim_ic.txt (SW)  [Z-Beta = -7.4642]  [Pearson Full 0.573097738, Pearson Zeroed 0.572303]</p>	<p>Sox11_2266.2_combined_prim_ic_trimmed.txt (SW)  [Z-Beta = -19.2475]  [Pearson Full 0.472809142, Pearson Zeroed 0.467013]</p>
<p>Sox11_2266.2_v1_prim_untrimmed.txt (SW)</p>	<p>Sox11_2266.2_v2_prim_untrimmed.txt (SW)  [Z-Beta = 57.3278]  [Pearson Full 0.573097738, Pearson Zeroed 0.542715]</p>	<p>Sox11_2266.2_combined_prim_untrimmed.txt (SW)  [Z-Beta = 530.074]  [Pearson Full 0.472809142, Pearson Zeroed 0.0046839]</p>
<p>Sox11_2266.2_v1_sec_auc.txt (SW)  [Z-Beta = 14.1506]  [Pearson Full 0.229129643, Pearson Zeroed 0.195823]</p>	<p>Sox11_2266.2_v2_sec_auc.txt (SW)  [Z-Beta = 24.4627]  [Pearson Full 0.573097738, Pearson Zeroed 0.569716]</p>	<p>Sox11_2266.2_combined_sec_auc.txt (SW)</p>
<p>Sox11_2266.2_v1_sec_ic.txt (SW)</p>	<p>Sox11_2266.2_v2_sec_ic.txt (SW)  [Z-Beta = 32.5011]  [Pearson Full 0.573097738, Pearson Zeroed 0.563019]</p>	<p>Sox11_2266.2_combined_sec_ic_trimmed.txt (SW)  [Z-Beta = -29.6608]  [Pearson Full 0.472809142, Pearson Zeroed 0.464351]</p>
<p>Sox11_2266.2_v1_sec_untrimmed.txt (SW)</p>	<p>Sox11_2266.2_v2_sec_untrimmed.txt (SW)  [Z-Beta = 11.4569]  [Pearson Full 0.573097738, Pearson Zeroed 0.572116]</p>	<p>Sox11_2266.2_combined_sec_untrimmed.txt (SW)  [Z-Beta = 37.4912]  [Pearson Full 0.472809142, Pearson Zeroed 0.452226]</p>
<p>Sox11_2266.2_v1_prim_trimmed.txt (RM8)</p>	<p>Sox11_2266.2_v2_prim_trimmed.txt (RM8)  [Z-Beta = -48.7363]  [Pearson Full 0.573097738, Pearson Zeroed 0.556016]</p>	<p>Sox11_2266.2_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Sox11_2266.2_v1_sec_trimmed.txt (RM8)</p>	<p>Sox11_2266.2_v2_sec_trimmed.txt (RM8)  [Z-Beta = 86.0401]  [Pearson Full 0.573097738, Pearson Zeroed 0.510006]</p>	<p>Sox11_2266.2_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Sox11_2266.2_v1_prim_untrimmed.txt (RM8)</p>	<p>Sox11_2266.2_v2_prim_untrimmed.txt (RM8)  [Z-Beta = 57.3278]  [Pearson Full 0.573097738, Pearson Zeroed 0.572303]</p>	<p>Sox11_2266.2_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>

	<p>0.542715]</p> 	
<p>Sox11_2266.2_v1_sec_untrimmed.txt (RM8)</p> 	<p>Sox11_2266.2_v2_sec_untrimmed.txt (RM8)  [Z-Beta = 11.4569]  [Pearson Full 0.573097738, Pearson Zeroed 0.572116]</p> 	<p>Sox11_2266.2_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>
<p>Sox11_2266.2_v1_RM35_prim_trimmed.txt (RM35)  [Z-Beta = 9.5465]  [Pearson Full 0.229129643, Pearson Zeroed 0.215284]</p> 	<p>Sox11_2266.2_v2_RM35_prim_trimmed.txt (RM35)  [Z-Beta = 16.5308]  [Pearson Full 0.573097738, Pearson Zeroed 0.572742]</p> 	<p>Sox11_2266.2_RM_combined_prim_trimmed.txt (RM35)  [Z-Beta = -8.61162]  [Pearson Full 0.472809142, Pearson Zeroed 0.472441]</p> 
<p>Sox11_2266.2_v1_RM35_sec_trimmed.txt (RM35)</p> 	<p>Sox11_2266.2_v2_RM35_sec_trimmed.txt (RM35)  [Z-Beta = 173.418]  [Pearson Full 0.573097738, Pearson Zeroed 0.26622]</p> 	<p>Sox11_2266.2_RM_combined_sec_trimmed.txt (RM35)  [Z-Beta = 20.1487]  [Pearson Full 0.472809142, Pearson Zeroed 0.467853]</p> 
<p>Sox11_2266.2_v1_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Sox11_2266.2_v2_RM35_prim_untrimmed.txt (RM35)  [Z-Beta = -188.881]  [Pearson Full 0.573097738, Pearson Zeroed 0.467433]</p> 	<p>Sox11_2266.2_RM_combined_prim_untrimmed.txt (RM35)  [Z-Beta = 13.6277]  [Pearson Full 0.472809142, Pearson Zeroed 0.47055]</p> 
<p>Sox11_2266.2_v1_RM35_sec_untrimmed.txt (RM35)  [Z-Beta = 11.7431]  [Pearson Full 0.229129643, Pearson Zeroed 0.191101]</p> 	<p>Sox11_2266.2_v2_RM35_sec_untrimmed.txt (RM35)  [Z-Beta = 125.969]  [Pearson Full 0.573097738, Pearson Zeroed 0.479447]</p> 	<p>Sox11_2266.2_RM_combined_sec_untrimmed.txt (RM35)  [Z-Beta = 108.572]  [Pearson Full 0.472809142, Pearson Zeroed 0.339043]</p> 
<p>Sox11_2266.2_PWM_1.txt (K-afal)</p> 	<p>Sox11_2266.2_PWM_1.txt (K-afal)</p> 	<p>Sox11_2266.2_combined_PWM_1.txt (K-afal)  [Z-Beta = 59.5175]  [Pearson Full 0.472809142, Pearson Zeroed 0.431827]</p> 
<p>Sox11_2266.2_PWM_2.txt (K-afal)</p> 	<p>Sox11_2266.2_PWM_2.txt (K-afal)  [Z-Beta = -26.0729]  [Pearson Full 0.573097738, Pearson Zeroed 0.566291]</p> 	<p>Sox11_2266.2_combined_PWM_2.txt (K-afal)  No PWM Available</p>
<p>Sox11_2266.2_PWM_3.txt (Kafal)  No PWM Available</p>	<p>Sox11_2266.2_PWM_3.txt (Kafal)</p> 	<p>Sox11_2266.2_combined_PWM_3.txt (Kafal)  No PWM Available</p>

Sox11\_2266.2\_PWM\_4.txt (Kafal)  
No PWM Available


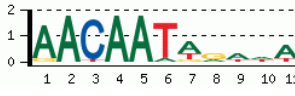







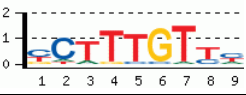


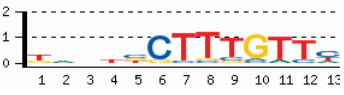
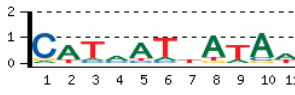


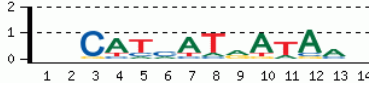
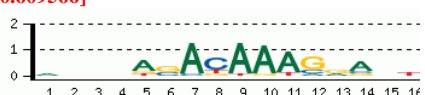
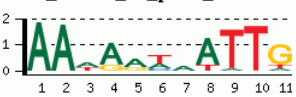
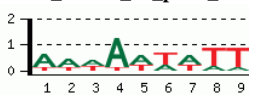
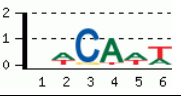
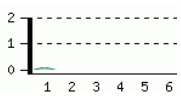
Sox11\_2266.2\_PWM\_4.txt (Kafal)

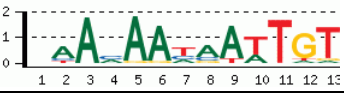
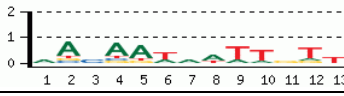
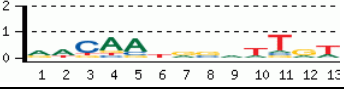
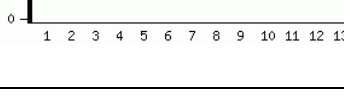
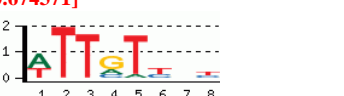

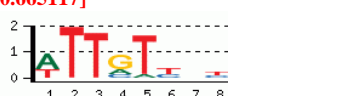



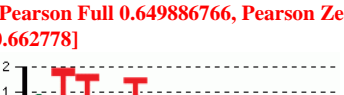


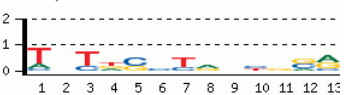
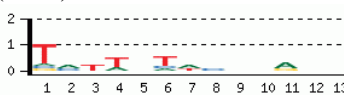
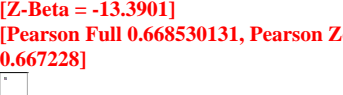








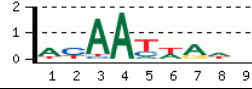



Sox11\_2266.2\_combined\_PWM\_4.txt (Kafal)  
No PWM Available



# Sox12\_3957.1



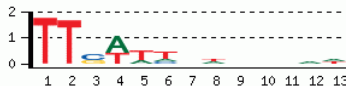
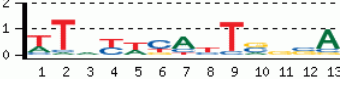
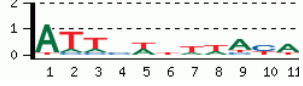
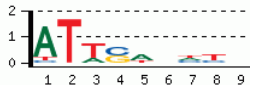
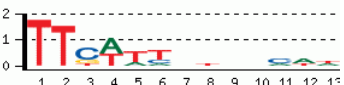

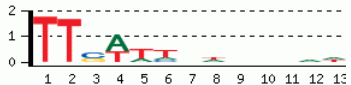


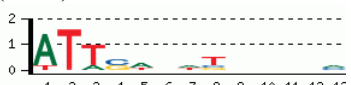


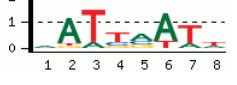







Array Version 1	Array Version 2	Combined
<p>Sox12_3957.1_v1_prim_auc.txt (SW)  [Z-Beta = 10.0146]  [Pearson Full 0.649886766, Pearson Zeroed 0.643992]</p> 	<p>Sox12_3957.1_v2_prim_auc.txt (SW)</p> 	<p>Sox12_3957.1_combined_prim_auc.txt (SW)</p> 
<p>Sox12_3957.1_v1_prim_ic.txt (SW)</p> 	<p>Sox12_3957.1_v2_prim_ic.txt (SW)</p> 	<p>Sox12_3957.1_combined_prim_ic_trimmed.txt (SW)  [Z-Beta = -50.1467]  [Pearson Full 0.668530131, Pearson Zeroed 0.661144]</p> 
<p>Sox12_3957.1_v1_prim_untrimmed.txt (SW)</p> 	<p>Sox12_3957.1_v2_prim_untrimmed.txt (SW)</p> 	<p>Sox12_3957.1_combined_prim_untrimmed.txt (SW)  [Z-Beta = -160.063]  [Pearson Full 0.668530131, Pearson Zeroed 0.621315]</p> 
<p>Sox12_3957.1_v1_sec_auc.txt (SW)  [Z-Beta = 30.5547]  [Pearson Full 0.649886766, Pearson Zeroed 0.613089]</p> 	<p>Sox12_3957.1_v2_sec_auc.txt (SW)</p> 	<p>Sox12_3957.1_combined_sec_auc.txt (SW)</p> 
<p>Sox12_3957.1_v1_sec_ic.txt (SW)</p> 	<p>Sox12_3957.1_v2_sec_ic.txt (SW)</p> 	<p>Sox12_3957.1_combined_sec_ic_trimmed.txt (SW)  [Z-Beta = -9.25184]  [Pearson Full 0.668530131, Pearson Zeroed 0.668047]</p> 
<p>Sox12_3957.1_v1_sec_untrimmed.txt (SW)</p> 	<p>Sox12_3957.1_v2_sec_untrimmed.txt (SW)</p> 	<p>Sox12_3957.1_combined_sec_untrimmed.txt (SW)  [Z-Beta = 91.5073]  [Pearson Full 0.668530131, Pearson Zeroed 0.609566]</p> 
<p>Sox12_3957.1_v1_prim_trimmed.txt (RM8)</p> 	<p>Sox12_3957.1_v2_prim_trimmed.txt (RM8)</p> 	<p>Sox12_3957.1_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Sox12_3957.1_v1_sec_trimmed.txt (RM8)  [Z-Beta = 45.1093]  [Pearson Full 0.649886766, Pearson Zeroed 0.624162]</p> 	<p>Sox12_3957.1_v2_sec_trimmed.txt (RM8)</p> 	<p>Sox12_3957.1_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Sox12_3957.1_v1_prim_untrimmed.txt (RM8)</p>	<p>Sox12_3957.1_v2_prim_untrimmed.txt (RM8)</p>	<p>Sox12_3957.1_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>

 <p>Sox12_3957.1_v1_sec_untrimmed.txt (RM8) [Z-Beta = 14.2857] [Pearson Full 0.649886766, Pearson Zeroed 0.645688]</p>	 <p>Sox12_3957.1_v2_sec_untrimmed.txt (RM8)</p>	<p>Sox12_3957.1_RM8_combined_sec_untrimmed.txt (RM8) No PWM Available</p>
 <p>Sox12_3957.1_v1_RM35_prim_trimmed.txt (RM35) [Z-Beta = 89.8931] [Pearson Full 0.649886766, Pearson Zeroed 0.674371]</p>	 <p>Sox12_3957.1_v2_RM35_prim_trimmed.txt (RM35)</p>	<p>Sox12_3957.1_RM_combined_prim_trimmed.txt (RM35) [Z-Beta = 45.922] [Pearson Full 0.668530131, Pearson Zeroed 0.665117]</p>
 <p>Sox12_3957.1_v1_RM35_sec_trimmed.txt (RM35)</p>	 <p>Sox12_3957.1_v2_RM35_sec_trimmed.txt (RM35) [Z-Beta = 20.3002] [Pearson Full 0.530661825, Pearson Zeroed 0.497306]</p>	 <p>Sox12_3957.1_RM_combined_sec_trimmed.txt (RM35) [Z-Beta = 26.2627] [Pearson Full 0.668530131, Pearson Zeroed 0.665292]</p>
 <p>Sox12_3957.1_v1_RM35_prim_untrimmed.txt (RM35) [Z-Beta = 41.5414] [Pearson Full 0.649886766, Pearson Zeroed 0.662778]</p>	 <p>Sox12_3957.1_v2_RM35_prim_untrimmed.txt (RM35)</p>	 <p>Sox12_3957.1_RM_combined_prim_untrimmed.txt (RM35) [Z-Beta = 96.9841] [Pearson Full 0.668530131, Pearson Zeroed 0.645748]</p>
 <p>Sox12_3957.1_v1_RM35_sec_untrimmed.txt (RM35)</p>	 <p>Sox12_3957.1_v2_RM35_sec_untrimmed.txt (RM35)</p>	 <p>Sox12_3957.1_RM_combined_sec_untrimmed.txt (RM35) [Z-Beta = -13.3901] [Pearson Full 0.668530131, Pearson Zeroed 0.667228]</p>
 <p>Sox12_3957.1_PWM_1.txt (K-afal) [Z-Beta = 2.46416] [Pearson Full 0.649886766, Pearson Zeroed 0.648262]</p>	 <p>Sox12_3957.1_PWM_1.txt (K-afal)</p>	 <p>Sox12_3957.1_combined_PWM_1.txt (K-afal) [Z-Beta = 167.243] [Pearson Full 0.668530131, Pearson Zeroed 0.531301]</p>
 <p>Sox12_3957.1_PWM_2.txt (K-afal)</p>	 <p>Sox12_3957.1_PWM_2.txt (K-afal)</p>	 <p>Sox12_3957.1_combined_PWM_2.txt (K-afal) [Z-Beta = -140.437] [Pearson Full 0.668530131, Pearson Zeroed 0.654396]</p>
 <p>Sox12_3957.1_PWM_3.txt (K-afal) [Z-Beta = 21.1389] [Pearson Full 0.649886766, Pearson Zeroed 0.639964]</p>	 <p>Sox12_3957.1_PWM_3.txt (K-afal) [Z-Beta = 128.743] [Pearson Full 0.530661825, Pearson Zeroed 0.340855]</p>	<p>Sox12_3957.1_combined_PWM_3.txt (K-afal) No PWM Available</p>

<p>Sox12_3957.1_PWM_4.txt (K-afal)</p> 	<p>Sox12_3957.1_PWM_4.txt (K-afal)  [Z-Beta = 1.75393]  [Pearson Full 0.530661825, Pearson Zeroed 0.528062]</p> 	<p>Sox12_3957.1_combined_PWM_4.txt (K-afal)  No PWM Available</p>
<p>Sox12_3957.1_PWM_5.txt (K-afal)</p> 	<p>Sox12_3957.1_PWM_5.txt (K-afal)  No PWM Available</p>	<p>Sox12_3957.1_combined_PWM_5.txt (K-afal)  No PWM Available</p>
<p>Sox12_3957.1_PWM_6.txt (K-afal)  [Z-Beta = 4.0335]  [Pearson Full 0.649886766, Pearson Zeroed 0.64688]</p> 	<p>Sox12_3957.1_PWM_6.txt (K-afal)  No PWM Available</p>	<p>Sox12_3957.1_combined_PWM_6.txt (K-afal)  No PWM Available</p>
<p>Sox12_3957.1_PWM_7.txt (K-afal)</p> 	<p>Sox12_3957.1_PWM_7.txt (K-afal)  No PWM Available</p>	<p>Sox12_3957.1_combined_PWM_7.txt (K-afal)  No PWM Available</p>

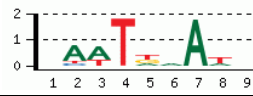
# Sox1\_2631.2

Array Version 1	Array Version 2	Combined
Sox1_2631.2_v1_prim_auc.txt (SW) 	Sox1_2631.2_v2_prim_auc.txt (SW) 	Sox1_2631.2_combined_prim_auc.txt (SW) 
Sox1_2631.2_v1_prim_ic.txt (SW) 	Sox1_2631.2_v2_prim_ic.txt (SW) 	Sox1_2631.2_combined_prim_ic_trimmed.txt (SW) 
Sox1_2631.2_v1_prim_untrimmed.txt (SW) 	Sox1_2631.2_v2_prim_untrimmed.txt (SW) 	Sox1_2631.2_combined_prim_untrimmed.txt (SW) 
Sox1_2631.2_v1_sec_auc.txt (SW) [Z-Beta = 52.1018] [Pearson Full 0.520336774, Pearson Zeroed 0.38506] 	Sox1_2631.2_v2_sec_auc.txt (SW) 	Sox1_2631.2_combined_sec_auc.txt (SW) 
Sox1_2631.2_v1_sec_ic.txt (SW) 	Sox1_2631.2_v2_sec_ic.txt (SW) 	Sox1_2631.2_combined_sec_ic_trimmed.txt (SW) 
Sox1_2631.2_v1_sec_untrimmed.txt (SW) 	Sox1_2631.2_v2_sec_untrimmed.txt (SW) [Z-Beta = 139.636] [Pearson Full 0.660985058, Pearson Zeroed 0.237793] 	Sox1_2631.2_combined_sec_untrimmed.txt (SW) 
Sox1_2631.2_v1_prim_trimmed.txt (RM8) 	Sox1_2631.2_v2_prim_trimmed.txt (RM8) 	Sox1_2631.2_RM8_combined_prim_trimmed.txt (RM8) No PWM Available
Sox1_2631.2_v1_sec_trimmed.txt (RM8) [Z-Beta = 30.1769] [Pearson Full 0.520336774, Pearson Zeroed 0.469886] 	Sox1_2631.2_v2_sec_trimmed.txt (RM8) 	Sox1_2631.2_RM8_combined_sec_trimmed.txt (RM8) No PWM Available
Sox1_2631.2_v1_prim_untrimmed.txt (RM8) 	Sox1_2631.2_v2_prim_untrimmed.txt (RM8) 	Sox1_2631.2_RM8_combined_prim_untrimmed.txt (RM8) No PWM Available
Sox1_2631.2_v1_sec_untrimmed.txt (RM8) [Z-Beta = 20.5201] [Pearson Full 0.520336774, Pearson Zeroed 0.495523] 	Sox1_2631.2_v2_sec_untrimmed.txt (RM8) [Z-Beta = 139.636] [Pearson Full 0.660985058, Pearson Zeroed 0.237793] 	Sox1_2631.2_RM8_combined_sec_untrimmed.txt (RM8) No PWM Available

<p>Sox1_2631.2_v1_RM35_prim_trimmed.txt (RM35)</p> 	<p>Sox1_2631.2_v2_RM35_prim_trimmed.txt (RM35)</p> 	<p>Sox1_2631.2_RM_combined_prim_trimmed.txt (RM35)</p> 
<p>Sox1_2631.2_v1_RM35_sec_trimmed.txt (RM35)</p> 	<p>Sox1_2631.2_v2_RM35_sec_trimmed.txt (RM35)</p> 	<p>Sox1_2631.2_RM_combined_sec_trimmed.txt (RM35)</p> <p>[Z-Beta = 19.1273] [Pearson Full 0.512630258, Pearson Zeroed 0.476927]</p> 
<p>Sox1_2631.2_v1_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Sox1_2631.2_v2_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Sox1_2631.2_RM_combined_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = 50.9753] [Pearson Full 0.512630258, Pearson Zeroed 0.396772]</p> 
<p>Sox1_2631.2_v1_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Sox1_2631.2_v2_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Sox1_2631.2_RM_combined_sec_untrimmed.txt (RM35)</p> 
<p>Sox1_2631.2_PWM_1.txt (K-afal)</p> 	<p>Sox1_2631.2_PWM_1.txt (K-afal)</p> <p>[Z-Beta = 21.143] [Pearson Full 0.660985058, Pearson Zeroed 0.620177]</p> 	<p>Sox1_2631.2_combined_PWM_1.txt (K-afal)</p> 
<p>Sox1_2631.2_PWM_2.txt (Kafal)</p> <p>No PWM Available</p>	<p>Sox1_2631.2_PWM_2.txt (Kafal)</p> 	<p>Sox1_2631.2_combined_PWM_2.txt (Kafal)</p> <p>No PWM Available</p>
<p>Sox1_2631.2_PWM_3.txt (Kafal)</p> <p>No PWM Available</p>	<p>Sox1_2631.2_PWM_3.txt (Kafal)</p> 	<p>Sox1_2631.2_combined_PWM_3.txt (Kafal)</p> <p>No PWM Available</p>
<p>Sox1_2631.2_PWM_4.txt (Kafal)</p> <p>No PWM Available</p>	<p>Sox1_2631.2_PWM_4.txt (Kafal)</p> 	<p>Sox1_2631.2_combined_PWM_4.txt (Kafal)</p> <p>No PWM Available</p>
<p>Sox1_2631.2_PWM_5.txt (Kafal)</p> <p>No PWM Available</p>	<p>Sox1_2631.2_PWM_5.txt (Kafal)</p> 	<p>Sox1_2631.2_combined_PWM_5.txt (Kafal)</p> <p>No PWM Available</p>
<p>Sox1_2631.2_PWM_6.txt (Kafal)</p> <p>No PWM Available</p>	<p>Sox1_2631.2_PWM_6.txt (Kafal)</p> 	<p>Sox1_2631.2_combined_PWM_6.txt (Kafal)</p> <p>No PWM Available</p>
<p>Sox1_2631.2_PWM_7.txt (Kafal)</p> <p>No PWM Available</p>	<p>Sox1_2631.2_PWM_7.txt (Kafal)</p> 	<p>Sox1_2631.2_combined_PWM_7.txt (Kafal)</p> <p>No PWM Available</p>
<p>Sox1_2631.2_PWM_8.txt (Kafal)</p> <p>No PWM Available</p>	<p>Sox1_2631.2_PWM_8.txt (Kafal)</p> 	<p>Sox1_2631.2_combined_PWM_8.txt (Kafal)</p> <p>No PWM Available</p>

Sox1\_2631.2\_PWM\_9.txt (Kafal)  
No PWM Available

Sox1\_2631.2\_PWM\_9.txt (Kafal)

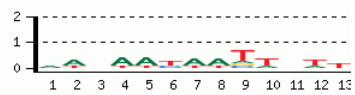
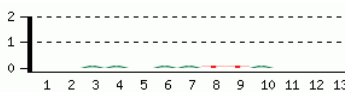
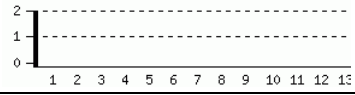
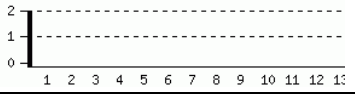
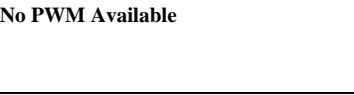

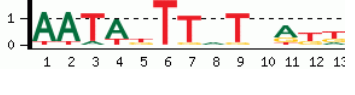


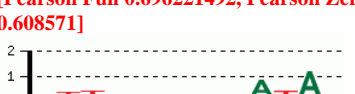
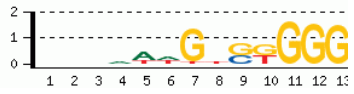
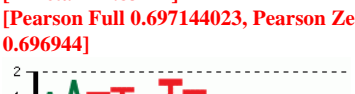


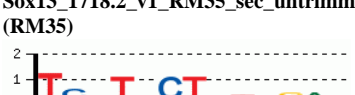
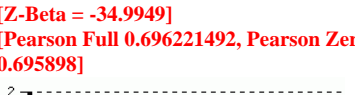

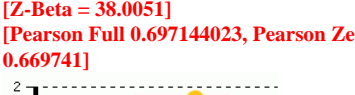
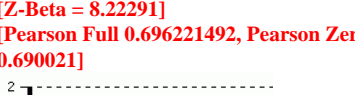






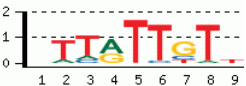

Sox1\_2631.2\_combined\_PWM\_9.txt (Kafal)  
No PWM Available

# Sox13\_1718.2

Array Version 1	Array Version 2	Combined
<p>Sox13_1718.2_v1_prim_auc.txt (SW)</p>	<p>Sox13_1718.2_v2_prim_auc.txt (SW)</p>	<p>Sox13_1718.2_combined_prim_auc.txt (SW)</p>
<p>Sox13_1718.2_v1_prim_ic.txt (SW)</p>	<p>Sox13_1718.2_v2_prim_ic.txt (SW)</p>	<p>Sox13_1718.2_combined_prim_ic_trimmed.txt (SW)</p>
<p>Sox13_1718.2_v1_prim_untrimmed.txt (SW) [Z-Beta = 4.58973] [Pearson Full 0.697144023, Pearson Zeroed 0.696772]</p>	<p>Sox13_1718.2_v2_prim_untrimmed.txt (SW) [Z-Beta = 3.1396] [Pearson Full 0.696221492, Pearson Zeroed 0.696675]</p>	<p>Sox13_1718.2_combined_prim_untrimmed.txt (SW)</p>
<p>Sox13_1718.2_v1_sec_auc.txt (SW) [Z-Beta = 7.64102] [Pearson Full 0.697144023, Pearson Zeroed 0.695969]</p>	<p>Sox13_1718.2_v2_sec_auc.txt (SW)</p>	<p>Sox13_1718.2_combined_sec_auc.txt (SW)</p>
<p>Sox13_1718.2_v1_sec_ic.txt (SW) [Z-Beta = 3.30734] [Pearson Full 0.697144023, Pearson Zeroed 0.696874]</p>	<p>Sox13_1718.2_v2_sec_ic.txt (SW)</p>	<p>Sox13_1718.2_combined_sec_ic_trimmed.txt (SW)</p>
<p>Sox13_1718.2_v1_sec_untrimmed.txt (SW) [Z-Beta = 5.30637] [Pearson Full 0.697144023, Pearson Zeroed 0.696491]</p>	<p>Sox13_1718.2_v2_sec_untrimmed.txt (SW) [Z-Beta = 74.6144] [Pearson Full 0.696221492, Pearson Zeroed 0.678829]</p>	<p>Sox13_1718.2_combined_sec_untrimmed.txt (SW)</p>
<p>Sox13_1718.2_v1_prim_trimmed.txt (RM8)</p>	<p>Sox13_1718.2_v2_prim_trimmed.txt (RM8) [Z-Beta = 40.1542] [Pearson Full 0.696221492, Pearson Zeroed 0.69342]</p>	<p>Sox13_1718.2_RM8_combined_prim_trimmed.txt (RM8) No PWM Available</p>
<p>Sox13_1718.2_v1_sec_trimmed.txt (RM8) [Z-Beta = -18.691] [Pearson Full 0.697144023, Pearson Zeroed 0.695751]</p>	<p>Sox13_1718.2_v2_sec_trimmed.txt (RM8)</p>	<p>Sox13_1718.2_RM8_combined_sec_trimmed.txt (RM8) No PWM Available</p>
<p>Sox13_1718.2_v1_prim_untrimmed.txt (RM8) [Z-Beta = 12.4373] [Pearson Full 0.697144023, Pearson Zeroed 0.695865]</p>	<p>Sox13_1718.2_v2_prim_untrimmed.txt (RM8) [Z-Beta = 3.1396] [Pearson Full 0.696221492, Pearson Zeroed 0.696675]</p>	<p>Sox13_1718.2_RM8_combined_prim_untrimmed.txt (RM8) No PWM Available</p>

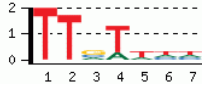

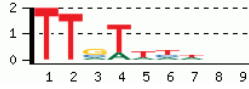

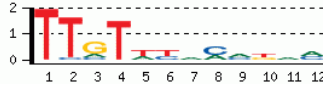

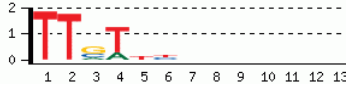
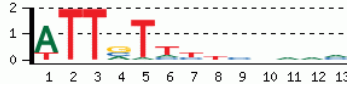
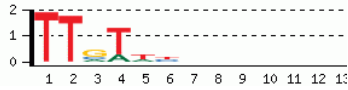
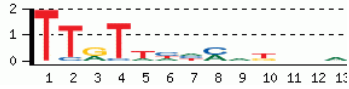
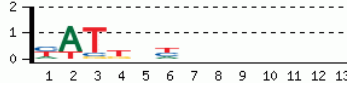




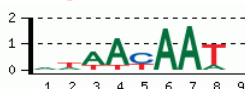
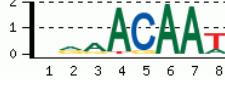


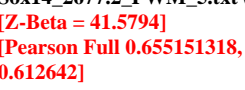





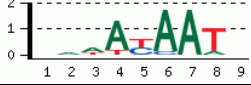
		
<p>Sox13_1718.2_v1_sec_untrimmed.txt (RM8)  [Z-Beta = 167.881]  [Pearson Full 0.697144023, Pearson Zeroed 0.116168]</p>	<p>Sox13_1718.2_v2_sec_untrimmed.txt (RM8)  [Z-Beta = 74.6144]  [Pearson Full 0.696221492, Pearson Zeroed 0.678829]</p>	<p>Sox13_1718.2_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>
		
<p>Sox13_1718.2_v1_RM35_prim_trimmed.txt (RM35)  [Z-Beta = -10.9063]  [Pearson Full 0.697144023, Pearson Zeroed 0.694994]</p>	<p>Sox13_1718.2_v2_RM35_prim_trimmed.txt (RM35)  </p>	<p>Sox13_1718.2_RM_combined_prim_trimmed.txt (RM35)  </p>
		
<p>Sox13_1718.2_v1_RM35_sec_trimmed.txt (RM35)  [Z-Beta = 13.268]  [Pearson Full 0.697144023, Pearson Zeroed 0.69399]</p>	<p>Sox13_1718.2_v2_RM35_sec_trimmed.txt (RM35)  [Z-Beta = 41.7163]  [Pearson Full 0.696221492, Pearson Zeroed 0.608571]</p>	<p>Sox13_1718.2_RM_combined_sec_trimmed.txt (RM35)  </p>
		
<p>Sox13_1718.2_v1_RM35_prim_untrimmed.txt (RM35)  [Z-Beta = -2.83422]  [Pearson Full 0.697144023, Pearson Zeroed 0.696944]</p>	<p>Sox13_1718.2_v2_RM35_prim_untrimmed.txt (RM35)  </p>	<p>Sox13_1718.2_RM_combined_prim_untrimmed.txt (RM35)  </p>
		
<p>Sox13_1718.2_v1_RM35_sec_untrimmed.txt (RM35)  </p>	<p>Sox13_1718.2_v2_RM35_sec_untrimmed.txt (RM35)  [Z-Beta = -34.9949]  [Pearson Full 0.696221492, Pearson Zeroed 0.695898]</p>	<p>Sox13_1718.2_RM_combined_sec_untrimmed.txt (RM35)  </p>
		
<p>Sox13_1718.2_PWM_1.txt (K-afal)  [Z-Beta = 38.0051]  [Pearson Full 0.697144023, Pearson Zeroed 0.669741]</p>	<p>Sox13_1718.2_PWM_1.txt (K-afal)  [Z-Beta = 8.22291]  [Pearson Full 0.696221492, Pearson Zeroed 0.690021]</p>	<p>Sox13_1718.2_combined_PWM_1.txt (K-afal)  </p>
		
<p>Sox13_1718.2_PWM_2.txt (K-afal)  [Z-Beta = 1.11936]  [Pearson Full 0.697144023, Pearson Zeroed 0.697124]</p>	<p>Sox13_1718.2_PWM_2.txt (K-afal)  [Z-Beta = 4.92387]  [Pearson Full 0.696221492, Pearson Zeroed 0.691686]</p>	<p>Sox13_1718.2_combined_PWM_2.txt (K-afal)  </p>
		
<p>Sox13_1718.2_PWM_3.txt (K-afal)  [Z-Beta = 13.9134]  [Pearson Full 0.697144023, Pearson Zeroed 0.693766]</p>	<p>Sox13_1718.2_PWM_3.txt (K-afal)  No PWM Available</p>	<p>Sox13_1718.2_combined_PWM_3.txt (K-afal)  </p>

		
<p>Sox13_1718.2_PWM_4.txt (K-afal)  [Z-Beta = 7.46644]  [Pearson Full 0.697144023, Pearson Zeroed 0.696261]</p> 	<p>Sox13_1718.2_PWM_4.txt (K-afal)  No PWM Available</p>	<p>Sox13_1718.2_combined_PWM_4.txt (K-afal)  No PWM Available</p>

# Sox14\_2677.2

Array Version 1	Array Version 2	Combined
<p>Sox14_2677.2_v1_prim_auc.txt (SW)</p>	<p>Sox14_2677.2_v2_prim_auc.txt (SW)</p>	<p>Sox14_2677.2_combined_prim_auc.txt (SW)</p>
<p>Sox14_2677.2_v1_prim_ic.txt (SW)  [Z-Beta = 11.4831]  [Pearson Full 0.655151318, Pearson Zeroed 0.657299]</p>	<p>Sox14_2677.2_v2_prim_ic.txt (SW)</p>	<p>Sox14_2677.2_combined_prim_ic_trimmed.txt (SW)</p>
<p>Sox14_2677.2_v1_prim_untrimmed.txt (SW)  [Z-Beta = 67.0169]  [Pearson Full 0.655151318, Pearson Zeroed 0.651426]</p>	<p>Sox14_2677.2_v2_prim_untrimmed.txt (SW)</p>	<p>Sox14_2677.2_combined_prim_untrimmed.txt (SW)</p>
<p>Sox14_2677.2_v1_sec_auc.txt (SW)</p> <input type="checkbox"/>	<p>Sox14_2677.2_v2_sec_auc.txt (SW)</p>	<p>Sox14_2677.2_combined_sec_auc.txt (SW)</p> <input type="checkbox"/>
<p>Sox14_2677.2_v1_sec_ic.txt (SW)</p>	<p>Sox14_2677.2_v2_sec_ic.txt (SW)</p>	<p>Sox14_2677.2_combined_sec_ic_trimmed.txt (SW)</p>
<p>Sox14_2677.2_v1_sec_untrimmed.txt (SW)</p>	<p>Sox14_2677.2_v2_sec_untrimmed.txt (SW)  [Z-Beta = 81.0657]  [Pearson Full 0.657572057, Pearson Zeroed 0.599681]</p>	<p>Sox14_2677.2_combined_sec_untrimmed.txt (SW)</p>
<p>Sox14_2677.2_v1_prim_trimmed.txt (RM8)</p>	<p>Sox14_2677.2_v2_prim_trimmed.txt (RM8)</p>	<p>Sox14_2677.2_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Sox14_2677.2_v1_sec_trimmed.txt (RM8)</p>	<p>Sox14_2677.2_v2_sec_trimmed.txt (RM8)</p>	<p>Sox14_2677.2_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Sox14_2677.2_v1_prim_untrimmed.txt (RM8)</p> <input type="checkbox"/>	<p>Sox14_2677.2_v2_prim_untrimmed.txt (RM8)</p>	<p>Sox14_2677.2_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Sox14_2677.2_v1_sec_untrimmed.txt (RM8)</p>	<p>Sox14_2677.2_v2_sec_untrimmed.txt (RM8)  [Z-Beta = 81.0657]  [Pearson Full 0.657572057, Pearson Zeroed 0.599681]</p>	<p>Sox14_2677.2_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>

<p>Sox14_2677.2_v1_RM35_prim_trimmed.txt (RM35)</p> 	<p>Sox14_2677.2_v2_RM35_prim_trimmed.txt (RM35)  [Z-Beta = 21.8698]  [Pearson Full 0.657572057, Pearson Zeroed 0.651553]</p> 	<p>Sox14_2677.2_RM_combined_prim_trimmed.txt (RM35)</p> 
<p>Sox14_2677.2_v1_RM35_sec_trimmed.txt (RM35)</p> 	<p>Sox14_2677.2_v2_RM35_sec_trimmed.txt (RM35)</p> 	<p>Sox14_2677.2_RM_combined_sec_trimmed.txt (RM35)  [Z-Beta = 52.9728]  [Pearson Full 0.674969069, Pearson Zeroed 0.621284]</p> 
<p>Sox14_2677.2_v1_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Sox14_2677.2_v2_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Sox14_2677.2_RM_combined_prim_untrimmed.txt (RM35)  [Z-Beta = 37.7258]  [Pearson Full 0.674969069, Pearson Zeroed 0.637904]</p> 
<p>Sox14_2677.2_v1_RM35_sec_untrimmed.txt (RM35)</p> <input type="checkbox"/>	<p>Sox14_2677.2_v2_RM35_sec_untrimmed.txt (RM35)  [Z-Beta = 8.95702]  [Pearson Full 0.657572057, Pearson Zeroed 0.649974]</p> 	<p>Sox14_2677.2_RM_combined_sec_untrimmed.txt (RM35)</p> 
<p>Sox14_2677.2_PWM_1.txt (K-afal)</p> 	<p>Sox14_2677.2_PWM_1.txt (K-afal)</p> 	<p>Sox14_2677.2_combined_PWM_1.txt (K-afal)</p> <input type="checkbox"/>
<p>Sox14_2677.2_PWM_2.txt (K-afal)  [Z-Beta = 58.3092]  [Pearson Full 0.655151318, Pearson Zeroed 0.618815]</p> 	<p>Sox14_2677.2_PWM_2.txt (K-afal)</p> 	<p>Sox14_2677.2_combined_PWM_2.txt (K-afal)</p> <input type="checkbox"/>
<p>Sox14_2677.2_PWM_3.txt (K-afal)  [Z-Beta = 52.7276]  [Pearson Full 0.655151318, Pearson Zeroed 0.63416]</p> 	<p>Sox14_2677.2_PWM_3.txt (K-afal)</p> 	<p>Sox14_2677.2_combined_PWM_3.txt (K-afal)  [Z-Beta = 126.539]  [Pearson Full 0.674969069, Pearson Zeroed 0.558468]</p> <input type="checkbox"/>
<p>Sox14_2677.2_PWM_4.txt (K-afal)  [Z-Beta = 5.7508]  [Pearson Full 0.655151318, Pearson Zeroed 0.65194]</p> 	<p>Sox14_2677.2_PWM_4.txt (K-afal)  [Z-Beta = 23.6894]  [Pearson Full 0.657572057, Pearson Zeroed 0.64191]</p> 	<p>Sox14_2677.2_combined_PWM_4.txt (K-afal)  No PWM Available</p>
<p>Sox14_2677.2_PWM_5.txt (K-afal)  [Z-Beta = 41.5794]  [Pearson Full 0.655151318, Pearson Zeroed 0.612642]</p> 	<p>Sox14_2677.2_PWM_5.txt (K-afal)</p> 	<p>Sox14_2677.2_combined_PWM_5.txt (K-afal)  No PWM Available</p>

 <p>Sequence logo showing peaks for TACAAT. The y-axis ranges from 0 to 2. The x-axis is labeled 1 through 9. The sequence TACAAT is highlighted in red and green.</p>		
<p>Sox14_2677.2_PWM_6.txt (K-afal)  [Z-Beta = 13.8893]  [Pearson Full 0.655151318, Pearson Zeroed 0.654057]</p>  <p>Sequence logo showing peaks for ATTGT. The y-axis ranges from 0 to 2. The x-axis is labeled 1 through 9. The sequence ATTGT is highlighted in red and green.</p>	<p>Sox14_2677.2_PWM_6.txt (K-afal)  [Z-Beta = 80.6167]  [Pearson Full 0.657572057, Pearson Zeroed 0.611241]</p>  <p>Sequence logo showing peaks for AATA. The y-axis ranges from 0 to 2. The x-axis is labeled 1 through 9. The sequence AATA is highlighted in red and green.</p>	<p>Sox14_2677.2_combined_PWM_6.txt (K-afal)  No PWM Available</p>







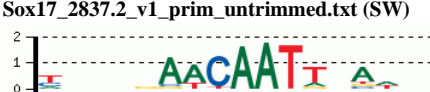
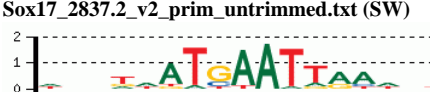


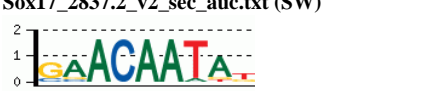
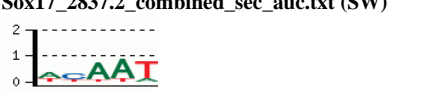


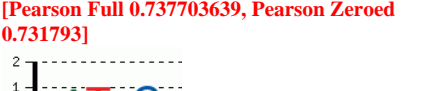
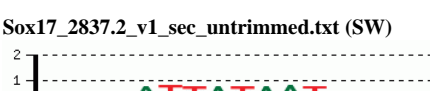
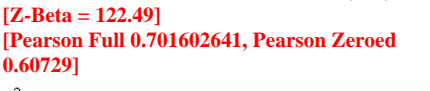
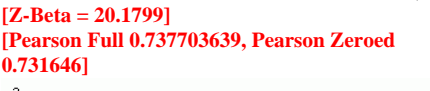

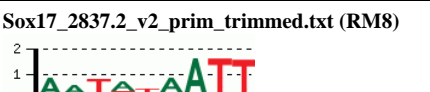

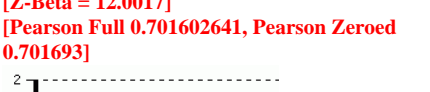
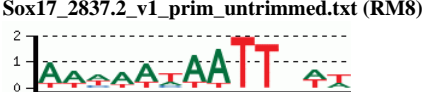
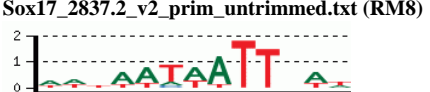
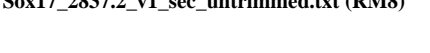
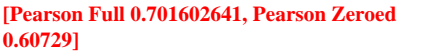
# Sox15\_3457.1

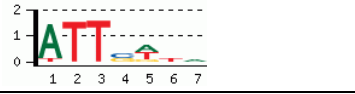
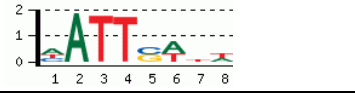
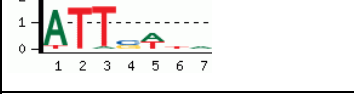
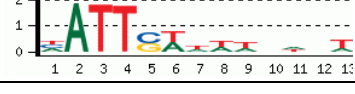
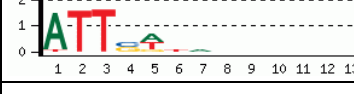
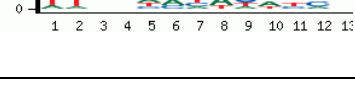
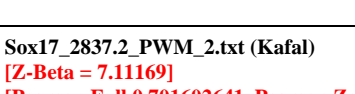
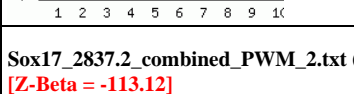
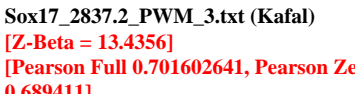

Array Version 1	Array Version 2	Combined
<p>Sox15_3457.1_v1_prim_auc.txt (SW)  [Z-Beta = 8.56136]  [Pearson Full 0.722003535, Pearson Zeroed 0.714835]</p>	<p>Sox15_3457.1_v2_prim_auc.txt (SW)  [Z-Beta = 8.78899]  [Pearson Full 0.324004564, Pearson Zeroed 0.318337]</p>	<p>Sox15_3457.1_combined_prim_auc.txt (SW)</p>
<p>Sox15_3457.1_v1_prim_ic.txt (SW)</p>	<p>Sox15_3457.1_v2_prim_ic.txt (SW)</p>	<p>Sox15_3457.1_combined_prim_ic_trimmed.txt (SW)  [Z-Beta = 14.3295]  [Pearson Full 0.681313189, Pearson Zeroed 0.679721]</p>
<p>Sox15_3457.1_v1_prim_untrimmed.txt (SW)</p>	<p>Sox15_3457.1_v2_prim_untrimmed.txt (SW)</p>	<p>Sox15_3457.1_combined_prim_untrimmed.txt (SW)  [Z-Beta = 38.3349]  [Pearson Full 0.681313189, Pearson Zeroed 0.668358]</p>
<p>Sox15_3457.1_v1_sec_auc.txt (SW)</p>	<p>Sox15_3457.1_v2_sec_auc.txt (SW)</p>	<p>Sox15_3457.1_combined_sec_auc.txt (SW)</p>
<p>Sox15_3457.1_v1_sec_ic.txt (SW)</p>	<p>Sox15_3457.1_v2_sec_ic.txt (SW)</p>	<p>Sox15_3457.1_combined_sec_ic_trimmed.txt (SW)  [Z-Beta = 11.8096]  [Pearson Full 0.681313189, Pearson Zeroed 0.680276]</p>
<p>Sox15_3457.1_v1_sec_untrimmed.txt (SW)</p>	<p>Sox15_3457.1_v2_sec_untrimmed.txt (SW)</p>	<p>Sox15_3457.1_combined_sec_untrimmed.txt (SW)  [Z-Beta = 64.3057]  [Pearson Full 0.681313189, Pearson Zeroed 0.634519]</p>
<p>Sox15_3457.1_v1_prim_trimmed.txt (RM8)</p>	<p>Sox15_3457.1_v2_prim_trimmed.txt (RM8)</p>	<p>Sox15_3457.1_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Sox15_3457.1_v1_sec_trimmed.txt (RM8)  [Z-Beta = 77.8321]  [Pearson Full 0.722003535, Pearson Zeroed 0.714981]</p>	<p>Sox15_3457.1_v2_sec_trimmed.txt (RM8)</p>	<p>Sox15_3457.1_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Sox15_3457.1_v1_prim_untrimmed.txt (RM8)</p>	<p>Sox15_3457.1_v2_prim_untrimmed.txt (RM8)</p>	<p>Sox15_3457.1_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Sox15_3457.1_v1_sec_untrimmed.txt (RM8)  [Z-Beta = 170.201]  [Pearson Full 0.722003535, Pearson Zeroed 0.709027]</p>	<p>Sox15_3457.1_v2_sec_untrimmed.txt (RM8)</p>	<p>Sox15_3457.1_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>





		
<p>Sox15_3457.1_v1_RM35_prim_trimmed.txt (RM35)</p> 	<p>Sox15_3457.1_v2_RM35_prim_trimmed.txt (RM35)</p> 	<p>Sox15_3457.1_RM_combined_prim_trimmed.txt (RM35)  [Z-Beta = -8.96319]  [Pearson Full 0.681313189, Pearson Zeroed 0.680948]</p> 
<p>Sox15_3457.1_v1_RM35_sec_trimmed.txt (RM35)  [Z-Beta = 11.7115]  [Pearson Full 0.722003535, Pearson Zeroed 0.71384]</p> 	<p>Sox15_3457.1_v2_RM35_sec_trimmed.txt (RM35)</p> 	<p>Sox15_3457.1_RM_combined_sec_trimmed.txt (RM35)  [Z-Beta = 72.7127]  [Pearson Full 0.681313189, Pearson Zeroed 0.64134]</p> 
<p>Sox15_3457.1_v1_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Sox15_3457.1_v2_RM35_prim_untrimmed.txt (RM35)  [Z-Beta = 17.2413]  [Pearson Full 0.324004564, Pearson Zeroed 0.293092]</p> 	<p>Sox15_3457.1_RM_combined_prim_untrimmed.txt (RM35)  [Z-Beta = 105.828]  [Pearson Full 0.681313189, Pearson Zeroed 0.586756]</p> 
<p>Sox15_3457.1_v1_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Sox15_3457.1_v2_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Sox15_3457.1_RM_combined_sec_untrimmed.txt (RM35)  [Z-Beta = -1.33561]  [Pearson Full 0.681313189, Pearson Zeroed 0.681267]</p> 
<p>Sox15_3457.1_PWM_1.txt (K-afal)  [Z-Beta = 14.8514]  [Pearson Full 0.722003535, Pearson Zeroed 0.718167]</p> 	<p>Sox15_3457.1_PWM_1.txt (K-afal)</p> 	<p>Sox15_3457.1_combined_PWM_1.txt (K-afal)  [Z-Beta = 82.5656]  [Pearson Full 0.681313189, Pearson Zeroed 0.631656]</p> 
<p>Sox15_3457.1_PWM_2.txt (Kafal)  No PWM Available</p>	<p>Sox15_3457.1_PWM_2.txt (Kafal)  [Z-Beta = 52.1657]  [Pearson Full 0.324004564, Pearson Zeroed 0.268251]</p> 	<p>Sox15_3457.1_combined_PWM_2.txt (Kafal)  No PWM Available</p>



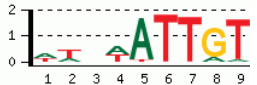









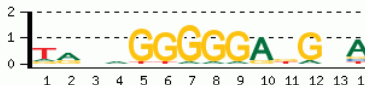




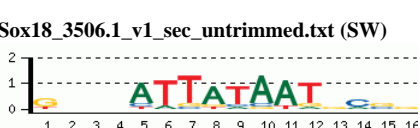
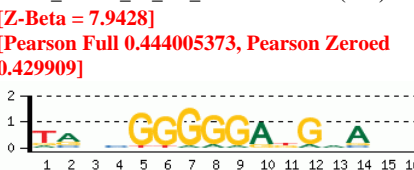
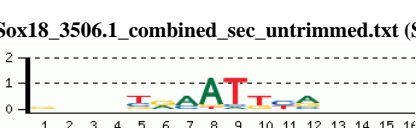


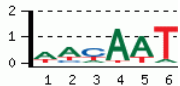

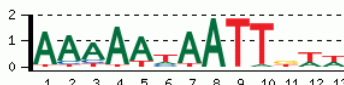
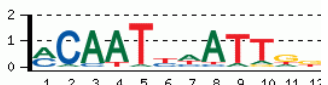
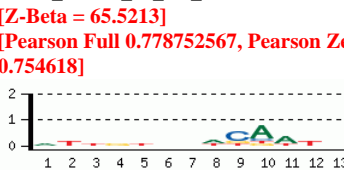

# Sox17\_2837.2


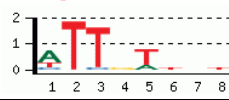
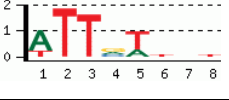
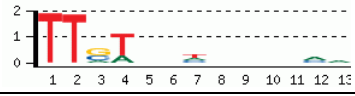
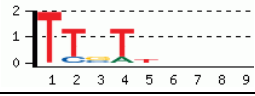
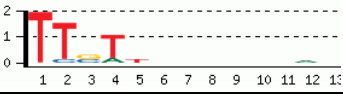
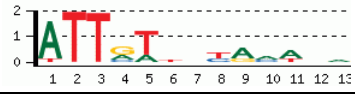
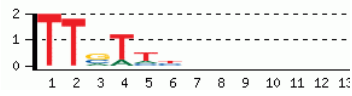
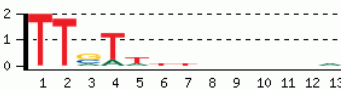
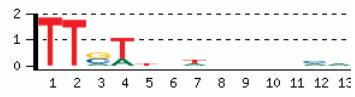

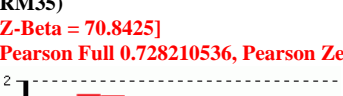
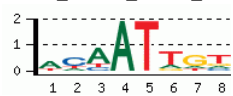
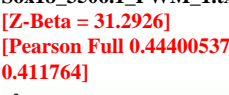
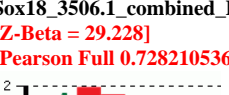



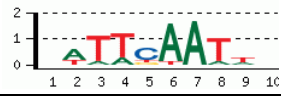


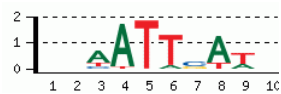
Array Version 1	Array Version 2	Combined
Sox17_2837.2_v1_prim_auc.txt (SW) 	Sox17_2837.2_v2_prim_auc.txt (SW) 	Sox17_2837.2_combined_prim_auc.txt (SW) 
Sox17_2837.2_v1_prim_ic.txt (SW) 	Sox17_2837.2_v2_prim_ic.txt (SW) 	Sox17_2837.2_combined_prim_ic_trimmed.txt (SW) [Z-Beta = 16.4974] [Pearson Full 0.737703639, Pearson Zeroed 0.734526] 
Sox17_2837.2_v1_prim_untrimmed.txt (SW) 	Sox17_2837.2_v2_prim_untrimmed.txt (SW) 	Sox17_2837.2_combined_prim_untrimmed.txt (SW) [Z-Beta = 65.9046] [Pearson Full 0.737703639, Pearson Zeroed 0.674769] 
Sox17_2837.2_v1_sec_auc.txt (SW) 	Sox17_2837.2_v2_sec_auc.txt (SW) 	Sox17_2837.2_combined_sec_auc.txt (SW) 
Sox17_2837.2_v1_sec_ic.txt (SW) 	Sox17_2837.2_v2_sec_ic.txt (SW) 	Sox17_2837.2_combined_sec_ic_trimmed.txt (SW) [Z-Beta = 20.2071] [Pearson Full 0.737703639, Pearson Zeroed 0.731793] 
Sox17_2837.2_v1_sec_untrimmed.txt (SW) 	Sox17_2837.2_v2_sec_untrimmed.txt (SW) [Z-Beta = 122.49] [Pearson Full 0.701602641, Pearson Zeroed 0.60729] 	Sox17_2837.2_combined_sec_untrimmed.txt (SW) [Z-Beta = 20.1799] [Pearson Full 0.737703639, Pearson Zeroed 0.731646] 
Sox17_2837.2_v1_prim_trimmed.txt (RM8) 	Sox17_2837.2_v2_prim_trimmed.txt (RM8) 	Sox17_2837.2_RM8_combined_prim_trimmed.txt (RM8) No PWM Available
Sox17_2837.2_v1_sec_trimmed.txt (RM8) 	Sox17_2837.2_v2_sec_trimmed.txt (RM8) [Z-Beta = 12.0017] [Pearson Full 0.701602641, Pearson Zeroed 0.701693] 	Sox17_2837.2_RM8_combined_sec_trimmed.txt (RM8) No PWM Available
Sox17_2837.2_v1_prim_untrimmed.txt (RM8) 	Sox17_2837.2_v2_prim_untrimmed.txt (RM8) 	Sox17_2837.2_RM8_combined_prim_untrimmed.txt (RM8) No PWM Available
Sox17_2837.2_v1_sec_untrimmed.txt (RM8) 	Sox17_2837.2_v2_sec_untrimmed.txt (RM8) [Z-Beta = 122.49] [Pearson Full 0.701602641, Pearson Zeroed 0.60729] 	Sox17_2837.2_RM8_combined_sec_untrimmed.txt (RM8) No PWM Available

		
<b>Sox17_2837.2_v1_RM35_prim_trimmed.txt (RM35)</b> [Z-Beta = 43.0848] [Pearson Full 0.513648353, Pearson Zeroed 0.475863]	<b>Sox17_2837.2_v2_RM35_prim_trimmed.txt (RM35)</b> [Z-Beta = 36.2578] [Pearson Full 0.701602641, Pearson Zeroed 0.675424]	<b>Sox17_2837.2_RM_combined_prim_trimmed.txt (RM35)</b> [Z-Beta = -36.6144] [Pearson Full 0.737703639, Pearson Zeroed 0.73256]
		
<b>Sox17_2837.2_v1_RM35_sec_trimmed.txt (RM35)</b>	<b>Sox17_2837.2_v2_RM35_sec_trimmed.txt (RM35)</b>	<b>Sox17_2837.2_RM_combined_sec_trimmed.txt (RM35)</b> [Z-Beta = 114.114] [Pearson Full 0.737703639, Pearson Zeroed 0.527194]
		
<b>Sox17_2837.2_v1_RM35_prim_untrimmed.txt (RM35)</b> [Z-Beta = 19.8568] [Pearson Full 0.513648353, Pearson Zeroed 0.479827]	<b>Sox17_2837.2_v2_RM35_prim_untrimmed.txt (RM35)</b> [Z-Beta = 1.61997] [Pearson Full 0.701602641, Pearson Zeroed 0.70128]	<b>Sox17_2837.2_RM_combined_prim_untrimmed.txt (RM35)</b> [Z-Beta = 49.4851] [Pearson Full 0.737703639, Pearson Zeroed 0.721158]
		
<b>Sox17_2837.2_v1_RM35_sec_untrimmed.txt (RM35)</b>	<b>Sox17_2837.2_v2_RM35_sec_untrimmed.txt (RM35)</b>	<b>Sox17_2837.2_RM_combined_sec_untrimmed.txt (RM35)</b> [Z-Beta = -79.9503] [Pearson Full 0.737703639, Pearson Zeroed 0.696249]
		
<b>Sox17_2837.2_PWM_1.txt (K-afal)</b> [Z-Beta = 57.4845] [Pearson Full 0.513648353, Pearson Zeroed 0.427694]	<b>Sox17_2837.2_PWM_1.txt (K-afal)</b>	<b>Sox17_2837.2_combined_PWM_1.txt (K-afal)</b> [Z-Beta = 145.379] [Pearson Full 0.737703639, Pearson Zeroed 0.397348]
<b>Sox17_2837.2_PWM_2.txt (Kafal)</b> No PWM Available		
	<b>Sox17_2837.2_PWM_2.txt (Kafal)</b> [Z-Beta = 7.11169] [Pearson Full 0.701602641, Pearson Zeroed 0.696127]	<b>Sox17_2837.2_combined_PWM_2.txt (Kafal)</b> [Z-Beta = -113.12] [Pearson Full 0.737703639, Pearson Zeroed 0.68662]
<b>Sox17_2837.2_PWM_3.txt (Kafal)</b> No PWM Available		<b>Sox17_2837.2_combined_PWM_3.txt (Kafal)</b> No PWM Available
	<b>Sox17_2837.2_PWM_3.txt (Kafal)</b> [Z-Beta = 13.4356] [Pearson Full 0.701602641, Pearson Zeroed 0.689411]	
<b>Sox17_2837.2_PWM_4.txt (Kafal)</b> No PWM Available		<b>Sox17_2837.2_combined_PWM_4.txt (Kafal)</b> No PWM Available
	<b>Sox17_2837.2_PWM_4.txt (Kafal)</b>	

<p>Sox17_2837.2_PWM_5.txt (Kafal) No PWM Available</p>	<p>Sox17_2837.2_PWM_5.txt (Kafal)</p> 	<p>Sox17_2837.2_combined_PWM_5.txt (Kafal) No PWM Available</p>
<p>Sox17_2837.2_PWM_6.txt (Kafal) No PWM Available</p>	<p>Sox17_2837.2_PWM_6.txt (Kafal)</p> 	<p>Sox17_2837.2_combined_PWM_6.txt (Kafal) No PWM Available</p>
<p>Sox17_2837.2_PWM_7.txt (Kafal) No PWM Available</p>	<p>Sox17_2837.2_PWM_7.txt (Kafal)</p> 	<p>Sox17_2837.2_combined_PWM_7.txt (Kafal) No PWM Available</p>
<p>Sox17_2837.2_PWM_8.txt (Kafal) No PWM Available</p>	<p>Sox17_2837.2_PWM_8.txt (Kafal)</p> 	<p>Sox17_2837.2_combined_PWM_8.txt (Kafal) No PWM Available</p>


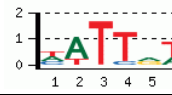

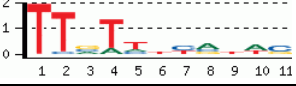

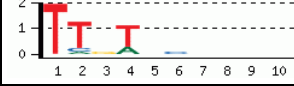
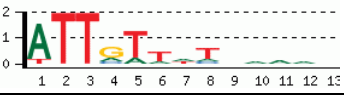
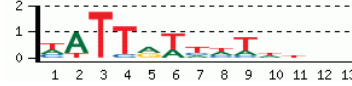
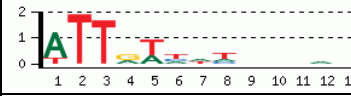
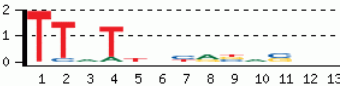
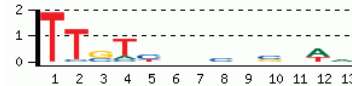
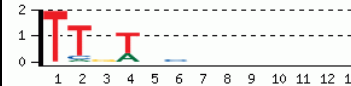






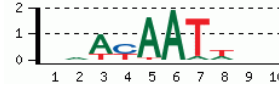
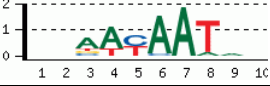
# Sox18\_3506.1

Array Version 1	Array Version 2	Combined
<p>Sox18_3506.1_v1_prim_auc.txt (SW)  [Z-Beta = 44.8726]  [Pearson Full 0.778752567, Pearson Zeroed 0.756135]</p> 	<p>Sox18_3506.1_v2_prim_auc.txt (SW)</p> 	<p>Sox18_3506.1_combined_prim_auc.txt (SW)</p> 
<p>Sox18_3506.1_v1_prim_ic.txt (SW)</p> 	<p>Sox18_3506.1_v2_prim_ic.txt (SW)</p> 	<p>Sox18_3506.1_combined_prim_ic_trimmed.txt (SW)</p> 
<p>Sox18_3506.1_v1_prim_untrimmed.txt (SW)</p> 	<p>Sox18_3506.1_v2_prim_untrimmed.txt (SW)</p> 	<p>Sox18_3506.1_combined_prim_untrimmed.txt (SW)</p> 
<p>Sox18_3506.1_v1_sec_auc.txt (SW)</p> 	<p>Sox18_3506.1_v2_sec_auc.txt (SW)</p> 	<p>Sox18_3506.1_combined_sec_auc.txt (SW)</p> 
<p>Sox18_3506.1_v1_sec_ic.txt (SW)</p> 	<p>Sox18_3506.1_v2_sec_ic.txt (SW)</p> 	<p>Sox18_3506.1_combined_sec_ic_trimmed.txt (SW)</p> 
<p>Sox18_3506.1_v1_sec_untrimmed.txt (SW)</p> 	<p>Sox18_3506.1_v2_sec_untrimmed.txt (SW)  [Z-Beta = 7.9428]  [Pearson Full 0.444005373, Pearson Zeroed 0.429909]</p> 	<p>Sox18_3506.1_combined_sec_untrimmed.txt (SW)</p> 
<p>Sox18_3506.1_v1_prim_trimmed.txt (RM8)</p> 	<p>Sox18_3506.1_v2_prim_trimmed.txt (RM8)</p> 	<p>Sox18_3506.1_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Sox18_3506.1_v1_sec_trimmed.txt (RM8)  [Z-Beta = 79.1758]  [Pearson Full 0.778752567, Pearson Zeroed 0.781701]</p> 	<p>Sox18_3506.1_v2_sec_trimmed.txt (RM8)</p> 	<p>Sox18_3506.1_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Sox18_3506.1_v1_prim_untrimmed.txt (RM8)</p> 	<p>Sox18_3506.1_v2_prim_untrimmed.txt (RM8)</p> 	<p>Sox18_3506.1_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Sox18_3506.1_v1_sec_untrimmed.txt (RM8)  [Z-Beta = 65.5213]  [Pearson Full 0.778752567, Pearson Zeroed 0.754618]</p> 	<p>Sox18_3506.1_v2_sec_untrimmed.txt (RM8)  [Z-Beta = 7.9428]  [Pearson Full 0.444005373, Pearson Zeroed 0.429909]</p> 	<p>Sox18_3506.1_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>

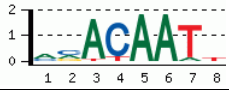
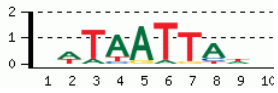

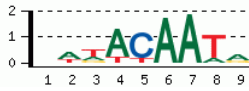


<p>Sox18_3506.1_v1_RM35_prim_trimmed.txt (RM35)  [Z-Beta = 43.2983]  [Pearson Full 0.778752567, Pearson Zeroed 0.782381]</p> 	<p>Sox18_3506.1_v2_RM35_prim_trimmed.txt (RM35)  [Z-Beta = 42.3611]  [Pearson Full 0.444005373, Pearson Zeroed 0.4213]</p> 	<p>Sox18_3506.1_RM_combined_prim_trimmed.txt (RM35)  [Z-Beta = 107.133]  [Pearson Full 0.728210536, Pearson Zeroed 0.669624]</p> 
<p>Sox18_3506.1_v1_RM35_sec_trimmed.txt (RM35)</p> 	<p>Sox18_3506.1_v2_RM35_sec_trimmed.txt (RM35)</p> 	<p>Sox18_3506.1_RM_combined_sec_trimmed.txt (RM35)</p> 
<p>Sox18_3506.1_v1_RM35_prim_untrimmed.txt (RM35)  [Z-Beta = 53.8362]  [Pearson Full 0.778752567, Pearson Zeroed 0.771796]</p> 	<p>Sox18_3506.1_v2_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Sox18_3506.1_RM_combined_prim_untrimmed.txt (RM35)</p> 
<p>Sox18_3506.1_v1_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Sox18_3506.1_v2_RM35_sec_untrimmed.txt (RM35)  [Z-Beta = 27.5897]  [Pearson Full 0.444005373, Pearson Zeroed 0.435787]</p> 	<p>Sox18_3506.1_RM_combined_sec_untrimmed.txt (RM35)  [Z-Beta = 70.8425]  [Pearson Full 0.728210536, Pearson Zeroed 0.697319]</p> 
<p>Sox18_3506.1_PWM_1.txt (K-afal)</p> 	<p>Sox18_3506.1_PWM_1.txt (K-afal)  [Z-Beta = 31.2926]  [Pearson Full 0.444005373, Pearson Zeroed 0.411764]</p> 	<p>Sox18_3506.1_combined_PWM_1.txt (K-afal)  [Z-Beta = 29.228]  [Pearson Full 0.728210536, Pearson Zeroed 0.710352]</p> 
<p>Sox18_3506.1_PWM_2.txt (K-afal)</p> 	<p>Sox18_3506.1_PWM_2.txt (K-afal)</p> 	<p>Sox18_3506.1_combined_PWM_2.txt (K-afal)</p> 
<p>Sox18_3506.1_PWM_3.txt (K-afal)</p> 	<p>Sox18_3506.1_PWM_3.txt (K-afal)  No PWM Available</p>	<p>Sox18_3506.1_combined_PWM_3.txt (K-afal)  No PWM Available</p>
<p>Sox18_3506.1_PWM_4.txt (K-afal)</p> 	<p>Sox18_3506.1_PWM_4.txt (K-afal)  No PWM Available</p>	<p>Sox18_3506.1_combined_PWM_4.txt (K-afal)  No PWM Available</p>
<p>Sox18_3506.1_PWM_5.txt (K-afal)</p> 	<p>Sox18_3506.1_PWM_5.txt (K-afal)  No PWM Available</p>	<p>Sox18_3506.1_combined_PWM_5.txt (K-afal)  No PWM Available</p>
<p>Sox18_3506.1_PWM_6.txt (K-afal)  [Z-Beta = 39.7257]  [Pearson Full 0.778752567, Pearson Zeroed 0.74624]</p> 	<p>Sox18_3506.1_PWM_6.txt (K-afal)  No PWM Available</p>	<p>Sox18_3506.1_combined_PWM_6.txt (K-afal)  No PWM Available</p>

# Sox21\_3417.1

Array Version 1	Array Version 2	Combined
<p>Sox21_3417.1_v1_prim_auc.txt (SW)</p>	<p>Sox21_3417.1_v2_prim_auc.txt (SW)</p>	<p>Sox21_3417.1_combined_prim_auc.txt (SW)</p>
<p>Sox21_3417.1_v1_prim_ic.txt (SW)</p>	<p>Sox21_3417.1_v2_prim_ic.txt (SW)</p>	<p>Sox21_3417.1_combined_prim_ic_trimmed.txt (SW)</p>
<p>Sox21_3417.1_v1_prim_untrimmed.txt (SW)</p>	<p>Sox21_3417.1_v2_prim_untrimmed.txt (SW)</p>	<p>Sox21_3417.1_combined_prim_untrimmed.txt (SW)</p>
<p>Sox21_3417.1_v1_sec_auc.txt (SW)  [Z-Beta = 15.5782]  [Pearson Full 0.695681683, Pearson Zeroed 0.680345]</p>	<p>Sox21_3417.1_v2_sec_auc.txt (SW)</p>	<p>Sox21_3417.1_combined_sec_auc.txt (SW)</p>
<p>Sox21_3417.1_v1_sec_ic.txt (SW)</p>	<p>Sox21_3417.1_v2_sec_ic.txt (SW)</p>	<p>Sox21_3417.1_combined_sec_ic_trimmed.txt (SW)</p>
<p>Sox21_3417.1_v1_sec_untrimmed.txt (SW)</p>	<p>Sox21_3417.1_v2_sec_untrimmed.txt (SW)  [Z-Beta = 123.185]  [Pearson Full 0.696017159, Pearson Zeroed 0.52803]</p>	<p>Sox21_3417.1_combined_sec_untrimmed.txt (SW)</p>
<p>Sox21_3417.1_v1_prim_trimmed.txt (RM8)</p>	<p>Sox21_3417.1_v2_prim_trimmed.txt (RM8)</p>	<p>Sox21_3417.1_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Sox21_3417.1_v1_sec_trimmed.txt (RM8)  [Z-Beta = 11.5284]  [Pearson Full 0.695681683, Pearson Zeroed 0.697044]</p>	<p>Sox21_3417.1_v2_sec_trimmed.txt (RM8)</p>	<p>Sox21_3417.1_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Sox21_3417.1_v1_prim_untrimmed.txt (RM8)</p>	<p>Sox21_3417.1_v2_prim_untrimmed.txt (RM8)</p>	<p>Sox21_3417.1_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Sox21_3417.1_v1_sec_untrimmed.txt (RM8)  [Z-Beta = 77.0547]  [Pearson Full 0.695681683, Pearson Zeroed 0.646432]</p>	<p>Sox21_3417.1_v2_sec_untrimmed.txt (RM8)  [Z-Beta = 123.185]  [Pearson Full 0.696017159, Pearson Zeroed 0.52803]</p>	<p>Sox21_3417.1_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>



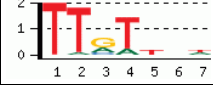
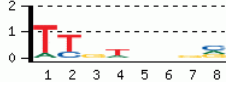
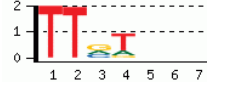

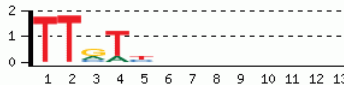
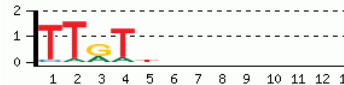
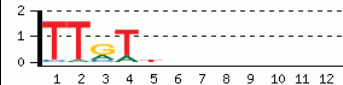
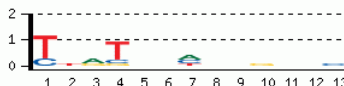
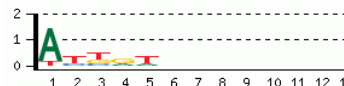
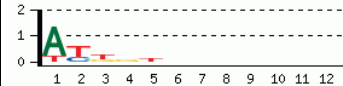

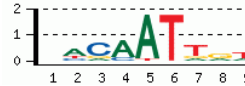




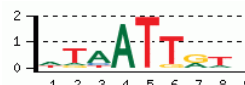


<p>Sox21_3417.1_v1_RM35_prim_trimmed.txt (RM35)  [Z-Beta = 44.6639]  [Pearson Full 0.695681683, Pearson Zeroed 0.702958]</p> 	<p>Sox21_3417.1_v2_RM35_prim_trimmed.txt (RM35)  [Z-Beta = 10.2975]  [Pearson Full 0.696017159, Pearson Zeroed 0.689587]</p> 	<p>Sox21_3417.1_RM_combined_prim_trimmed.txt (RM35)  [Z-Beta = 21.3078]  [Pearson Full 0.703256872, Pearson Zeroed 0.70833]</p> 
<p>Sox21_3417.1_v1_RM35_sec_trimmed.txt (RM35)  [Z-Beta = 16.3427]  [Pearson Full 0.695681683, Pearson Zeroed 0.68193]</p> 	<p>Sox21_3417.1_v2_RM35_sec_trimmed.txt (RM35)</p> 	<p>Sox21_3417.1_RM_combined_sec_trimmed.txt (RM35)  [Z-Beta = 35.1128]  [Pearson Full 0.703256872, Pearson Zeroed 0.657122]</p> 
<p>Sox21_3417.1_v1_RM35_prim_untrimmed.txt (RM35)  [Z-Beta = 12.6766]  [Pearson Full 0.695681683, Pearson Zeroed 0.697078]</p> 	<p>Sox21_3417.1_v2_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Sox21_3417.1_RM_combined_prim_untrimmed.txt (RM35)  [Z-Beta = 91.714]  [Pearson Full 0.703256872, Pearson Zeroed 0.706922]</p> 
<p>Sox21_3417.1_v1_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Sox21_3417.1_v2_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Sox21_3417.1_RM_combined_sec_untrimmed.txt (RM35)</p> 
<p>Sox21_3417.1_PWM_10.txt (K-afal)</p> 	<p>Sox21_3417.1_PWM_10.txt (K-afal)  No PWM Available</p>	<p>Sox21_3417.1_combined_PWM_10.txt (K-afal)  No PWM Available</p>
<p>Sox21_3417.1_PWM_11.txt (K-afal)  [Z-Beta = 10.6518]  [Pearson Full 0.695681683, Pearson Zeroed 0.684365]</p> 	<p>Sox21_3417.1_PWM_11.txt (K-afal)  No PWM Available</p>	<p>Sox21_3417.1_combined_PWM_11.txt (K-afal)  No PWM Available</p>
<p>Sox21_3417.1_PWM_1.txt (K-afal)  [Z-Beta = 19.7394]  [Pearson Full 0.695681683, Pearson Zeroed 0.681267]</p> 	<p>Sox21_3417.1_PWM_1.txt (K-afal)  [Z-Beta = 27.3831]  [Pearson Full 0.696017159, Pearson Zeroed 0.666566]</p> 	<p>Sox21_3417.1_combined_PWM_1.txt (K-afal)  [Z-Beta = 25.2209]  [Pearson Full 0.703256872, Pearson Zeroed 0.674907]</p> 
<p>Sox21_3417.1_PWM_2.txt (K-afal)</p> 	<p>Sox21_3417.1_PWM_2.txt (K-afal)</p> 	<p>Sox21_3417.1_combined_PWM_2.txt (K-afal)  No PWM Available</p>
<p>Sox21_3417.1_PWM_3.txt (K-afal)  [Z-Beta = 45.8548]  [Pearson Full 0.695681683, Pearson Zeroed 0.689575]</p> 	<p>Sox21_3417.1_PWM_3.txt (K-afal)  No PWM Available</p>	<p>Sox21_3417.1_combined_PWM_3.txt (K-afal)  No PWM Available</p>
<p>Sox21_3417.1_PWM_4.txt (K-afal)</p>	<p>Sox21_3417.1_PWM_4.txt (K-afal)  No PWM Available</p>	<p>Sox21_3417.1_combined_PWM_4.txt (K-afal)  No PWM Available</p>





















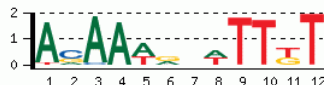
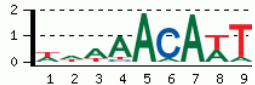

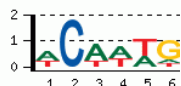
		
<p>Sox21_3417.1_PWM_5.txt (K-afal)</p> 	<p>Sox21_3417.1_PWM_5.txt (K-afal) No PWM Available</p>	<p>Sox21_3417.1_combined_PWM_5.txt (K-afal) No PWM Available</p>
<p>Sox21_3417.1_PWM_6.txt (K-afal)</p> 	<p>Sox21_3417.1_PWM_6.txt (K-afal) No PWM Available</p>	<p>Sox21_3417.1_combined_PWM_6.txt (K-afal) No PWM Available</p>
<p>Sox21_3417.1_PWM_7.txt (K-afal) [Z-Beta = 8.43899] [Pearson Full 0.695681683, Pearson Zeroed 0.692039]</p> 	<p>Sox21_3417.1_PWM_7.txt (K-afal) No PWM Available</p>	<p>Sox21_3417.1_combined_PWM_7.txt (K-afal) No PWM Available</p>
<p>Sox21_3417.1_PWM_8.txt (K-afal)</p> 	<p>Sox21_3417.1_PWM_8.txt (K-afal) No PWM Available</p>	<p>Sox21_3417.1_combined_PWM_8.txt (K-afal) No PWM Available</p>
<p>Sox21_3417.1_PWM_9.txt (K-afal)</p> 	<p>Sox21_3417.1_PWM_9.txt (K-afal) No PWM Available</p>	<p>Sox21_3417.1_combined_PWM_9.txt (K-afal) No PWM Available</p>







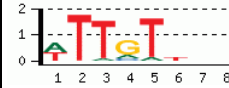

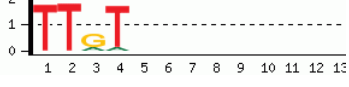
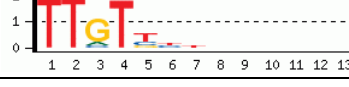
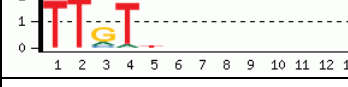
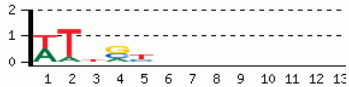
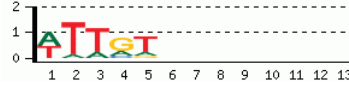
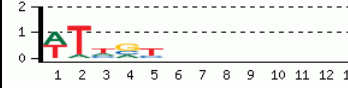


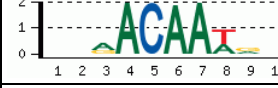


# Sox30\_2781.2

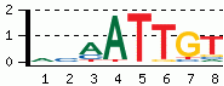





Array Version 1	Array Version 2	Combined
<p>Sox30_2781.2_v1_prim_auc.txt (SW)  [Z-Beta = 37.6782]  [Pearson Full 0.634936119, Pearson Zeroed 0.634285]</p>	<p>Sox30_2781.2_v2_prim_auc.txt (SW)</p>	<p>Sox30_2781.2_combined_prim_auc.txt (SW)</p>
<p>Sox30_2781.2_v1_prim_ic.txt (SW)  [Z-Beta = 53.1345]  [Pearson Full 0.634936119, Pearson Zeroed 0.609426]</p>	<p>Sox30_2781.2_v2_prim_ic.txt (SW)  [Z-Beta = 22.228]  [Pearson Full 0.788538582, Pearson Zeroed 0.78011]</p>	<p>Sox30_2781.2_combined_prim_ic_trimmed.txt (SW)  [Z-Beta = 52.0713]  [Pearson Full 0.796151502, Pearson Zeroed 0.792177]</p>
<p>Sox30_2781.2_v1_prim_untrimmed.txt (SW)</p>	<p>Sox30_2781.2_v2_prim_untrimmed.txt (SW)</p>	<p>Sox30_2781.2_combined_prim_untrimmed.txt (SW)</p>
<p>Sox30_2781.2_v1_sec_auc.txt (SW)</p>	<p>Sox30_2781.2_v2_sec_auc.txt (SW)</p>	<p>Sox30_2781.2_combined_sec_auc.txt (SW)</p>
<p>Sox30_2781.2_v1_sec_ic.txt (SW)</p>	<p>Sox30_2781.2_v2_sec_ic.txt (SW)</p>	<p>Sox30_2781.2_combined_sec_ic_trimmed.txt (SW)  [Z-Beta = 152.291]  [Pearson Full 0.796151502, Pearson Zeroed 0.695231]</p>
<p>Sox30_2781.2_v1_sec_untrimmed.txt (SW)</p>	<p>Sox30_2781.2_v2_sec_untrimmed.txt (SW)</p>	<p>Sox30_2781.2_combined_sec_untrimmed.txt (SW)  [Z-Beta = -42.1348]  [Pearson Full 0.796151502, Pearson Zeroed 0.788528]</p>
<p>Sox30_2781.2_v1_prim_trimmed.txt (RM8)</p>	<p>Sox30_2781.2_v2_prim_trimmed.txt (RM8)</p>	<p>Sox30_2781.2_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Sox30_2781.2_v1_sec_trimmed.txt (RM8)</p>	<p>Sox30_2781.2_v2_sec_trimmed.txt (RM8)</p>	<p>Sox30_2781.2_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Sox30_2781.2_v1_prim_untrimmed.txt (RM8)</p>	<p>Sox30_2781.2_v2_prim_untrimmed.txt (RM8)</p>	<p>Sox30_2781.2_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Sox30_2781.2_v1_sec_untrimmed.txt (RM8)</p>	<p>Sox30_2781.2_v2_sec_untrimmed.txt (RM8)</p>	<p>Sox30_2781.2_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>

<p>Sox30_2781.2_v1_RM35_prim_trimmed.txt (RM35)</p> 	<p>Sox30_2781.2_v2_RM35_prim_trimmed.txt (RM35)</p> <p>[Z-Beta = 315.549] [Pearson Full 0.788538582, Pearson Zeroed 0.55854]</p> 	<p>Sox30_2781.2_RM_combined_prim_trimmed.txt (RM35)</p> <p>[Z-Beta = 67.1082] [Pearson Full 0.796151502, Pearson Zeroed 0.782074]</p> 
<p>Sox30_2781.2_v1_RM35_sec_trimmed.txt (RM35)</p> 	<p>Sox30_2781.2_v2_RM35_sec_trimmed.txt (RM35)</p> 	<p>Sox30_2781.2_RM_combined_sec_trimmed.txt (RM35)</p> <p>[Z-Beta = 136.647] [Pearson Full 0.796151502, Pearson Zeroed 0.753231]</p> 
<p>Sox30_2781.2_v1_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Sox30_2781.2_v2_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Sox30_2781.2_RM_combined_prim_untrimmed.txt (RM35)</p> 
<p>Sox30_2781.2_v1_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Sox30_2781.2_v2_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Sox30_2781.2_RM_combined_sec_untrimmed.txt (RM35)</p> <p>[Z-Beta = -12.3462] [Pearson Full 0.796151502, Pearson Zeroed 0.795536]</p> 
<p>Sox30_2781.2_PWM_1.txt (K-afal)</p> <p>[Z-Beta = 51.1154] [Pearson Full 0.634936119, Pearson Zeroed 0.56195]</p> 	<p>Sox30_2781.2_PWM_1.txt (K-afal)</p> 	<p>Sox30_2781.2_combined_PWM_1.txt (K-afal)</p> <p>[Z-Beta = 152.069] [Pearson Full 0.796151502, Pearson Zeroed 0.764686]</p> 
<p>Sox30_2781.2_PWM_2.txt (K-afal)</p> <p>[Z-Beta = 57.7378] [Pearson Full 0.634936119, Pearson Zeroed 0.639328]</p> 	<p>Sox30_2781.2_PWM_2.txt (K-afal)</p> 	<p>Sox30_2781.2_combined_PWM_2.txt (K-afal)</p> <p>[Z-Beta = 42.5568] [Pearson Full 0.796151502, Pearson Zeroed 0.791242]</p> 
<p>Sox30_2781.2_PWM_3.txt (Kafal)</p> <p>No PWM Available</p>	<p>Sox30_2781.2_PWM_3.txt (Kafal)</p> <p>[Z-Beta = 15.4903] [Pearson Full 0.788538582, Pearson Zeroed 0.780613]</p> 	<p>Sox30_2781.2_combined_PWM_3.txt (Kafal)</p> <p>[Z-Beta = 20.3634] [Pearson Full 0.796151502, Pearson Zeroed 0.795378]</p> 
<p>Sox30_2781.2_PWM_4.txt (Kafal)</p> <p>No PWM Available</p>	<p>Sox30_2781.2_PWM_4.txt (Kafal)</p> 	<p>Sox30_2781.2_combined_PWM_4.txt (Kafal)</p> <p>No PWM Available</p>

# Sox4\_2941.2

Array Version 1	Array Version 2	Combined
<p>Sox4_2941.2_v1_prim_auc.txt (SW)  [Z-Beta = 50.5672]  [Pearson Full 0.636686822, Pearson Zeroed 0.581702]</p> 	<p>Sox4_2941.2_v2_prim_auc.txt (SW)  [Z-Beta = -141.283]  [Pearson Full 0.797176103, Pearson Zeroed 0.765506]</p> 	<p>Sox4_2941.2_combined_prim_auc.txt (SW)</p> 
<p>Sox4_2941.2_v1_prim_ic.txt (SW)</p> 	<p>Sox4_2941.2_v2_prim_ic.txt (SW)  [Z-Beta = 26.5101]  [Pearson Full 0.797176103, Pearson Zeroed 0.795467]</p> 	<p>Sox4_2941.2_combined_prim_ic_trimmed.txt (SW)  [Z-Beta = 58.5792]  [Pearson Full 0.782339286, Pearson Zeroed 0.771478]</p> 
<p>Sox4_2941.2_v1_prim_untrimmed.txt (SW)</p> 	<p>Sox4_2941.2_v2_prim_untrimmed.txt (SW)  [Z-Beta = 2.43031]  [Pearson Full 0.797176103, Pearson Zeroed 0.797157]</p> 	<p>Sox4_2941.2_combined_prim_untrimmed.txt (SW)  [Z-Beta = 61.384]  [Pearson Full 0.782339286, Pearson Zeroed 0.770256]</p> 
<p>Sox4_2941.2_v1_sec_auc.txt (SW)  [Z-Beta = 10.0743]  [Pearson Full 0.636686822, Pearson Zeroed 0.626035]</p> 	<p>Sox4_2941.2_v2_sec_auc.txt (SW)  [Z-Beta = 28.5155]  [Pearson Full 0.797176103, Pearson Zeroed 0.794928]</p> 	<p>Sox4_2941.2_combined_sec_auc.txt (SW)</p> 
<p>Sox4_2941.2_v1_sec_ic.txt (SW)</p> 	<p>Sox4_2941.2_v2_sec_ic.txt (SW)  [Z-Beta = 5.72577]  [Pearson Full 0.797176103, Pearson Zeroed 0.797058]</p> 	<p>Sox4_2941.2_combined_sec_ic_trimmed.txt (SW)  [Z-Beta = 420.586]  [Pearson Full 0.782339286, Pearson Zeroed 0.18201]</p> 
<p>Sox4_2941.2_v1_sec_untrimmed.txt (SW)</p> 	<p>Sox4_2941.2_v2_sec_untrimmed.txt (SW)  [Z-Beta = 29.717]  [Pearson Full 0.797176103, Pearson Zeroed 0.794713]</p> 	<p>Sox4_2941.2_combined_sec_untrimmed.txt (SW)  [Z-Beta = 23.9672]  [Pearson Full 0.782339286, Pearson Zeroed 0.780206]</p> 
<p>Sox4_2941.2_v1_prim_trimmed.txt (RM8)</p> 	<p>Sox4_2941.2_v2_prim_trimmed.txt (RM8)  [Z-Beta = 6.98882]  [Pearson Full 0.797176103, Pearson Zeroed 0.797033]</p> 	<p>Sox4_2941.2_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Sox4_2941.2_v1_sec_trimmed.txt (RM8)</p> 	<p>Sox4_2941.2_v2_sec_trimmed.txt (RM8)  [Z-Beta = 28.8596]  [Pearson Full 0.797176103, Pearson Zeroed 0.794901]</p> 	<p>Sox4_2941.2_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>

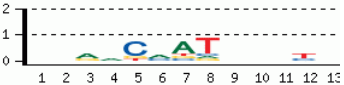
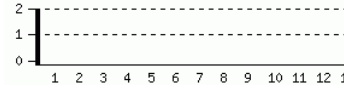
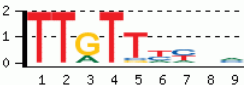



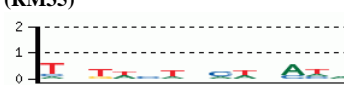
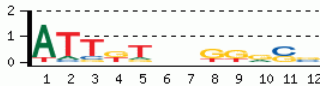
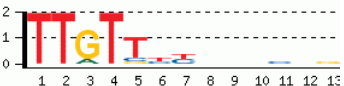
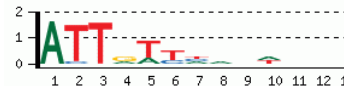
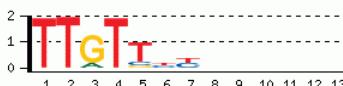

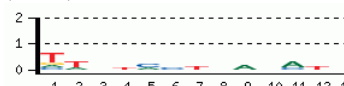





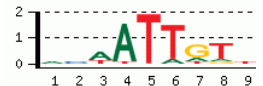




<p>Sox4_2941.2_v1_prim_untrimmed.txt (RM8)</p> 	<p>Sox4_2941.2_v2_prim_untrimmed.txt (RM8)</p> <p>[Z-Beta = 2.43031] [Pearson Full 0.797176103, Pearson Zeroed 0.797157]</p> 	<p>Sox4_2941.2_RM8_combined_prim_untrimmed.txt (RM8)</p> <p>No PWM Available</p>
<p>Sox4_2941.2_v1_sec_untrimmed.txt (RM8)</p> 	<p>Sox4_2941.2_v2_sec_untrimmed.txt (RM8)</p> <p>[Z-Beta = 29.717] [Pearson Full 0.797176103, Pearson Zeroed 0.794713]</p> 	<p>Sox4_2941.2_RM8_combined_sec_untrimmed.txt (RM8)</p> <p>No PWM Available</p>
<p>Sox4_2941.2_v1_RM35_prim_trimmed.txt (RM35)</p> <p>[Z-Beta = 199.287] [Pearson Full 0.636686822, Pearson Zeroed 0.398076]</p> 	<p>Sox4_2941.2_v2_RM35_prim_trimmed.txt (RM35)</p> <p>[Z-Beta = 14.3363] [Pearson Full 0.797176103, Pearson Zeroed 0.796994]</p> 	<p>Sox4_2941.2_RM_combined_prim_trimmed.txt (RM35)</p> <p>[Z-Beta = 79.4261] [Pearson Full 0.782339286, Pearson Zeroed 0.776374]</p> 
<p>Sox4_2941.2_v1_RM35_sec_trimmed.txt (RM35)</p> 	<p>Sox4_2941.2_v2_RM35_sec_trimmed.txt (RM35)</p> <p>[Z-Beta = 31.8995] [Pearson Full 0.797176103, Pearson Zeroed 0.795109]</p> 	<p>Sox4_2941.2_RM_combined_sec_trimmed.txt (RM35)</p> <p>[Z-Beta = 74.7109] [Pearson Full 0.782339286, Pearson Zeroed 0.766732]</p> 
<p>Sox4_2941.2_v1_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Sox4_2941.2_v2_RM35_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = 3.92197] [Pearson Full 0.797176103, Pearson Zeroed 0.797147]</p> 	<p>Sox4_2941.2_RM_combined_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = -1.20906] [Pearson Full 0.782339286, Pearson Zeroed 0.782336]</p> 
<p>Sox4_2941.2_v1_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Sox4_2941.2_v2_RM35_sec_untrimmed.txt (RM35)</p> <p>[Z-Beta = 120.106] [Pearson Full 0.797176103, Pearson Zeroed 0.773305]</p> 	<p>Sox4_2941.2_RM_combined_sec_untrimmed.txt (RM35)</p> <p>[Z-Beta = 92.9945] [Pearson Full 0.782339286, Pearson Zeroed 0.756561]</p> 
<p>Sox4_2941.2_PWM_1.txt (K-afal)</p> 	<p>Sox4_2941.2_PWM_1.txt (K-afal)</p> <p>[Z-Beta = 20.5925] [Pearson Full 0.797176103, Pearson Zeroed 0.796319]</p> 	<p>Sox4_2941.2_combined_PWM_1.txt (K-afal)</p> <p>[Z-Beta = 653.741] [Pearson Full 0.782339286, Pearson Zeroed 0.196573]</p> 
<p>Sox4_2941.2_PWM_2.txt (Kafal)</p> <p>No PWM Available</p>	<p>Sox4_2941.2_PWM_2.txt (Kafal)</p> <p>[Z-Beta = 91.4866] [Pearson Full 0.797176103, Pearson Zeroed 0.772955]</p> 	<p>Sox4_2941.2_combined_PWM_2.txt (Kafal)</p> <p>[Z-Beta = -513.846] [Pearson Full 0.782339286, Pearson Zeroed 0.729995]</p> 

<p>Sox4_2941.2_PWM_3.txt (Kafal) No PWM Available</p>	<p>Sox4_2941.2_PWM_3.txt (Kafal)</p> 	<p>Sox4_2941.2_combined_PWM_3.txt (Kafal) No PWM Available</p>
<p>Sox4_2941.2_PWM_4.txt (Kafal) No PWM Available</p>	<p>Sox4_2941.2_PWM_4.txt (Kafal) [Z-Beta = 23.2878] [Pearson Full 0.797176103, Pearson Zeroed 0.795952]</p> 	<p>Sox4_2941.2_combined_PWM_4.txt (Kafal) No PWM Available</p>
<p>Sox4_2941.2_PWM_5.txt (Kafal) No PWM Available</p>	<p>Sox4_2941.2_PWM_5.txt (Kafal) [Z-Beta = 10.8743] [Pearson Full 0.797176103, Pearson Zeroed 0.796904]</p> 	<p>Sox4_2941.2_combined_PWM_5.txt (Kafal) No PWM Available</p>
<p>Sox4_2941.2_PWM_6.txt (Kafal) No PWM Available</p>	<p>Sox4_2941.2_PWM_6.txt (Kafal) [Z-Beta = 19.48] [Pearson Full 0.797176103, Pearson Zeroed 0.796712]</p> 	<p>Sox4_2941.2_combined_PWM_6.txt (Kafal) No PWM Available</p>
<p>Sox4_2941.2_PWM_7.txt (Kafal) No PWM Available</p>	<p>Sox4_2941.2_PWM_7.txt (Kafal) [Z-Beta = 41.6362] [Pearson Full 0.797176103, Pearson Zeroed 0.794152]</p> 	<p>Sox4_2941.2_combined_PWM_7.txt (Kafal) No PWM Available</p>
<p>Sox4_2941.2_PWM_8.txt (Kafal) No PWM Available</p>	<p>Sox4_2941.2_PWM_8.txt (Kafal) [Z-Beta = 98.9595] [Pearson Full 0.797176103, Pearson Zeroed 0.781132]</p> 	<p>Sox4_2941.2_combined_PWM_8.txt (Kafal) No PWM Available</p>

# Sox5\_3459.1




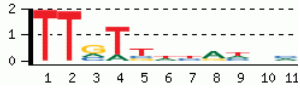
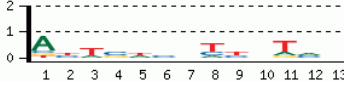
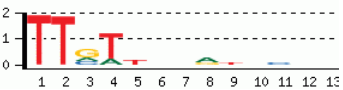
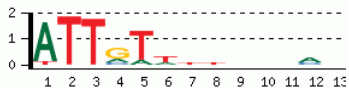

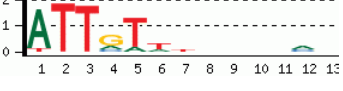
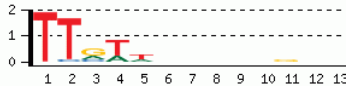
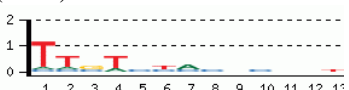
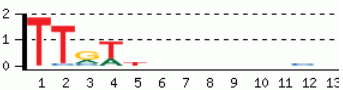








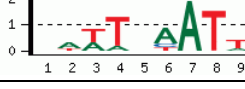



Array Version 1	Array Version 2	Combined
<p>Sox5_3459.1_v1_prim_auc.txt (SW)</p>	<p>Sox5_3459.1_v2_prim_auc.txt (SW)</p>	<p>Sox5_3459.1_combined_prim_auc.txt (SW)</p>
<p>Sox5_3459.1_v1_prim_ic.txt (SW)</p>	<p>Sox5_3459.1_v2_prim_ic.txt (SW)</p>	<p>Sox5_3459.1_combined_prim_ic_trimmed.txt (SW)  [Z-Beta = -26.101]  [Pearson Full 0.575008591, Pearson Zeroed 0.569088]</p>
<p>Sox5_3459.1_v1_prim_untrimmed.txt (SW)  [Z-Beta = 28.2148]  [Pearson Full 0.54441246, Pearson Zeroed 0.533864]</p>	<p>Sox5_3459.1_v2_prim_untrimmed.txt (SW)  [Z-Beta = 95.4126]  [Pearson Full 0.755575291, Pearson Zeroed 0.738581]</p>	<p>Sox5_3459.1_combined_prim_untrimmed.txt (SW)</p>
<p>Sox5_3459.1_v1_sec_auc.txt (SW)</p>	<p>Sox5_3459.1_v2_sec_auc.txt (SW)</p>	<p>Sox5_3459.1_combined_sec_auc.txt (SW)</p>
<p>Sox5_3459.1_v1_sec_ic.txt (SW)</p>	<p>Sox5_3459.1_v2_sec_ic.txt (SW)</p>	<p>Sox5_3459.1_combined_sec_ic_trimmed.txt (SW)  [Z-Beta = -159.969]  [Pearson Full 0.575008591, Pearson Zeroed 0.474746]</p>
<p>Sox5_3459.1_v1_sec_untrimmed.txt (SW)</p>	<p>Sox5_3459.1_v2_sec_untrimmed.txt (SW)  [Z-Beta = 54.7906]  [Pearson Full 0.755575291, Pearson Zeroed 0.73613]</p>	<p>Sox5_3459.1_combined_sec_untrimmed.txt (SW)  [Z-Beta = 12.0148]  [Pearson Full 0.575008591, Pearson Zeroed 0.573261]</p>
<p>Sox5_3459.1_v1_prim_trimmed.txt (RM8)</p>	<p>Sox5_3459.1_v2_prim_trimmed.txt (RM8)  [Z-Beta = 11.1893]  [Pearson Full 0.755575291, Pearson Zeroed 0.750299]</p>	<p>Sox5_3459.1_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Sox5_3459.1_v1_sec_trimmed.txt (RM8)  [Z-Beta = 30.2496]  [Pearson Full 0.54441246, Pearson Zeroed 0.509818]</p>	<p>Sox5_3459.1_v2_sec_trimmed.txt (RM8)</p>	<p>Sox5_3459.1_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Sox5_3459.1_v1_prim_untrimmed.txt (RM8)</p>	<p>Sox5_3459.1_v2_prim_untrimmed.txt (RM8)  [Z-Beta = 95.4126]  [Pearson Full 0.755575291, Pearson Zeroed 0.738581]</p>	<p>Sox5_3459.1_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>



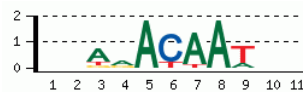
<p>Sox5_3459.1_v1_sec_untrimmed.txt (RM8)  [Z-Beta = 10.4438]  [Pearson Full 0.54441246, Pearson Zeroed 0.53502]</p> 	<p>Sox5_3459.1_v2_sec_untrimmed.txt (RM8)  [Z-Beta = 54.7906]  [Pearson Full 0.755575291, Pearson Zeroed 0.73613]</p> 	<p>Sox5_3459.1_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>
<p>Sox5_3459.1_v1_RM35_prim_trimmed.txt (RM35)</p> 	<p>Sox5_3459.1_v2_RM35_prim_trimmed.txt (RM35)</p> 	<p>Sox5_3459.1_RM_combined_prim_trimmed.txt (RM35)  [Z-Beta = -1.62135]  [Pearson Full 0.575008591, Pearson Zeroed 0.574975]</p> 
<p>Sox5_3459.1_v1_RM35_sec_trimmed.txt (RM35)</p> 	<p>Sox5_3459.1_v2_RM35_sec_trimmed.txt (RM35)</p> 	<p>Sox5_3459.1_RM_combined_sec_trimmed.txt (RM35)  [Z-Beta = 0.212195]  [Pearson Full 0.575008591, Pearson Zeroed 0.575008]</p> 
<p>Sox5_3459.1_v1_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Sox5_3459.1_v2_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Sox5_3459.1_RM_combined_prim_untrimmed.txt (RM35)  [Z-Beta = 44.5962]  [Pearson Full 0.575008591, Pearson Zeroed 0.546325]</p> 
<p>Sox5_3459.1_v1_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Sox5_3459.1_v2_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Sox5_3459.1_RM_combined_sec_untrimmed.txt (RM35)  [Z-Beta = 25.1318]  [Pearson Full 0.575008591, Pearson Zeroed 0.565155]</p> 
<p>Sox5_3459.1_PWM_1.txt (K-afal)</p> 	<p>Sox5_3459.1_PWM_1.txt (K-afal)</p> 	<p>Sox5_3459.1_combined_PWM_1.txt (K-afal)  [Z-Beta = 32.301]  [Pearson Full 0.575008591, Pearson Zeroed 0.564895]</p> 
<p>Sox5_3459.1_PWM_2.txt (K-afal)  [Z-Beta = 49.3084]  [Pearson Full 0.54441246, Pearson Zeroed 0.532068]</p> 	<p>Sox5_3459.1_PWM_2.txt (K-afal)</p> 	<p>Sox5_3459.1_combined_PWM_2.txt (K-afal)  [Z-Beta = 37.5163]  [Pearson Full 0.575008591, Pearson Zeroed 0.556059]</p> 
<p>Sox5_3459.1_PWM_3.txt (K-afal)  [Z-Beta = 15.6658]  [Pearson Full 0.54441246, Pearson Zeroed 0.516331]</p> 	<p>Sox5_3459.1_PWM_3.txt (K-afal)  No PWM Available</p>	<p>Sox5_3459.1_combined_PWM_3.txt (K-afal)  [Z-Beta = 60.0024]  [Pearson Full 0.575008591, Pearson Zeroed 0.537137]</p> 
<p>Sox5_3459.1_PWM_4.txt (K-afal)</p> 	<p>Sox5_3459.1_PWM_4.txt (K-afal)  No PWM Available</p>	<p>Sox5_3459.1_combined_PWM_4.txt (K-afal)  No PWM Available</p>

# Sox7\_3460.2

Array Version 1	Array Version 2	Combined
<p>Sox7_3460.1_v1_prim_auc.txt (SW)</p>	<p>Sox7_3460.2_v2_prim_auc.txt (SW)</p>	<p>Sox7_3460.2_combined_prim_auc.txt (SW)</p>
<p>Sox7_3460.1_v1_prim_ic.txt (SW)</p>	<p>Sox7_3460.2_v2_prim_ic.txt (SW)</p>	<p>Sox7_3460.2_combined_prim_ic_trimmed.txt (SW)  [Z-Beta = -56.1925]  [Pearson Full 0.703871496, Pearson Zeroed 0.691744]</p>
<p>Sox7_3460.1_v1_prim_untrimmed.txt (SW)</p>	<p>Sox7_3460.2_v2_prim_untrimmed.txt (SW)</p>	<p>Sox7_3460.1_combined_prim_untrimmed.txt (SW)  [Z-Beta = 63.477]  [Pearson Full 0.703871496, Pearson Zeroed 0.679007]</p>
<p>Sox7_3460.1_v1_sec_auc.txt (SW)</p>	<p>Sox7_3460.2_v2_sec_auc.txt (SW)</p>	<p>Sox7_3460.1_combined_sec_auc.txt (SW)</p>
<p>Sox7_3460.1_v1_sec_ic.txt (SW)</p>	<p>Sox7_3460.2_v2_sec_ic.txt (SW)</p>	<p>Sox7_3460.1_combined_sec_ic_trimmed.txt (SW)  [Z-Beta = 78.5617]  [Pearson Full 0.703871496, Pearson Zeroed 0.655055]</p>
<p>Sox7_3460.1_v1_sec_untrimmed.txt (SW)</p>	<p>Sox7_3460.2_v2_sec_untrimmed.txt (SW)</p>	<p>Sox7_3460.1_combined_sec_untrimmed.txt (SW)  [Z-Beta = -40.014]  [Pearson Full 0.703871496, Pearson Zeroed 0.69382]</p>
<p>Sox7_3460.1_v1_prim_trimmed.txt (RM8)</p>	<p>Sox7_3460.2_v2_prim_trimmed.txt (RM8)</p>	<p>Sox7_3460.1_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Sox7_3460.1_v1_sec_trimmed.txt (RM8)</p>	<p>Sox7_3460.2_v2_sec_trimmed.txt (RM8)</p>	<p>Sox7_3460.1_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Sox7_3460.1_v1_prim_untrimmed.txt (RM8)</p>	<p>Sox7_3460.2_v2_prim_untrimmed.txt (RM8)</p>	<p>Sox7_3460.1_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Sox7_3460.1_v1_sec_untrimmed.txt (RM8)  [Z-Beta = 31.6065]  [Pearson Full 0.754555519, Pearson Zeroed 0.744433]</p>	<p>Sox7_3460.2_v2_sec_untrimmed.txt (RM8)</p>	<p>Sox7_3460.1_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>

<p>Sox7_3460.1_v1_RM35_prim_trimmed.txt (RM35)  [Z-Beta = 75.3449]  [Pearson Full 0.75455519, Pearson Zeroed 0.768382]</p> 	<p>Sox7_3460.2_v2_RM35_prim_trimmed.txt (RM35)</p> 	<p>Sox7_3460.1_RM_combined_prim_trimmed.txt (RM35)  [Z-Beta = 124.805]  [Pearson Full 0.703871496, Pearson Zeroed 0.658968]</p> 
<p>Sox7_3460.1_v1_RM35_sec_trimmed.txt (RM35)</p> 	<p>Sox7_3460.2_v2_RM35_sec_trimmed.txt (RM35)</p> 	<p>Sox7_3460.1_RM_combined_sec_trimmed.txt (RM35)  [Z-Beta = 60.2686]  [Pearson Full 0.703871496, Pearson Zeroed 0.683319]</p> 
<p>Sox7_3460.1_v1_RM35_prim_untrimmed.txt (RM35)  [Z-Beta = 82.8937]  [Pearson Full 0.75455519, Pearson Zeroed 0.763648]</p> 	<p>Sox7_3460.2_v2_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Sox7_3460.1_RM_combined_prim_untrimmed.txt (RM35)  [Z-Beta = 32.9074]  [Pearson Full 0.703871496, Pearson Zeroed 0.702348]</p> 
<p>Sox7_3460.1_v1_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Sox7_3460.2_v2_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Sox7_3460.1_RM_combined_sec_untrimmed.txt (RM35)  [Z-Beta = 12.6807]  [Pearson Full 0.703871496, Pearson Zeroed 0.703143]</p> 
<p>Sox7_3460.1_PWM_1.txt (K-afal)  [Z-Beta = 37.3807]  [Pearson Full 0.75455519, Pearson Zeroed 0.733736]</p> 	<p>Sox7_3460.2_PWM_1.txt (K-afal)</p> 	<p>Sox7_3460.1_combined_PWM_1.txt (K-afal)  [Z-Beta = 1.76028]  [Pearson Full 0.703871496, Pearson Zeroed 0.703864]</p> 
<p>Sox7_3460.1_PWM_2.txt (K-afal)  [Z-Beta = 36.6708]  [Pearson Full 0.75455519, Pearson Zeroed 0.758311]</p> 	<p>Sox7_3460.2_PWM_2.txt (K-afal)</p> 	<p>Sox7_3460.1_combined_PWM_2.txt (K-afal)  [Z-Beta = 81.1643]  [Pearson Full 0.703871496, Pearson Zeroed 0.678888]</p> 
<p>Sox7_3460.1_PWM_3.txt (K-afal)  [Z-Beta = 44.3644]  [Pearson Full 0.75455519, Pearson Zeroed 0.746487]</p> 	<p>Sox7_3460.2_PWM_3.txt (K-afal)</p> 	<p>Sox7_3460.1_combined_PWM_3.txt (K-afal)  No PWM Available</p>
<p>Sox7_3460.1_PWM_4.txt (K-afal)  [Z-Beta = 49.1823]  [Pearson Full 0.75455519, Pearson Zeroed 0.706527]</p> 	<p>Sox7_3460.2_PWM_4.txt (K-afal)</p> 	<p>Sox7_3460.1_combined_PWM_4.txt (K-afal)  No PWM Available</p>
<p>Sox7_3460.1_PWM_5.txt (K-afal)</p> 	<p>Sox7_3460.2_PWM_5.txt (K-afal)</p> 	<p>Sox7_3460.1_combined_PWM_5.txt (K-afal)  No PWM Available</p>

Sox7\_3460.1\_PWM\_6.txt (K-afal)



Sox7\_3460.2\_PWM\_6.txt (K-afal)



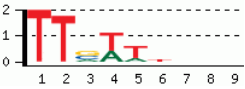
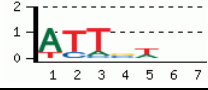
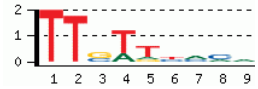


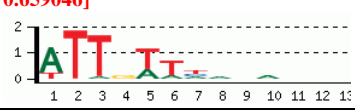
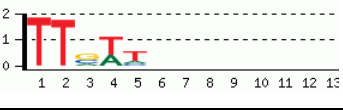


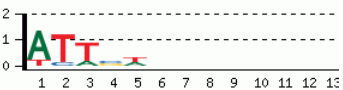



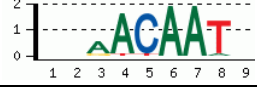
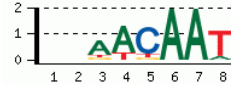


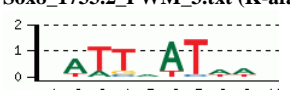


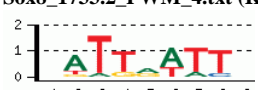



Sox7\_3460.1\_combined\_PWM\_6.txt (K-afal)

No PWM Available

# Sox8\_1733.2

Array Version 1	Array Version 2	Combined
<p>Sox8_1733.2_v1_prim_auc.txt (SW)  [Z-Beta = 55.6534]  [Pearson Full 0.647944975, Pearson Zeroed 0.621551]</p>	<p>Sox8_1733.2_v2_prim_auc.txt (SW)</p>	<p>Sox8_1733.2_combined_prim_auc.txt (SW)</p>
<p>Sox8_1733.2_v1_prim_ic.txt (SW)</p>	<p>Sox8_1733.2_v2_prim_ic.txt (SW)</p>	<p>Sox8_1733.2_combined_prim_ic_trimmed.txt (SW)</p>
<p>Sox8_1733.2_v1_prim_untrimmed.txt (SW)</p>	<p>Sox8_1733.2_v2_prim_untrimmed.txt (SW)</p>	<p>Sox8_1733.2_combined_prim_untrimmed.txt (SW)  [Z-Beta = 52.0423]  [Pearson Full 0.706509729, Pearson Zeroed 0.664182]</p>
<p>Sox8_1733.2_v1_sec_auc.txt (SW)</p>	<p>Sox8_1733.2_v2_sec_auc.txt (SW)</p>	<p>Sox8_1733.2_combined_sec_auc.txt (SW)</p>
<p>Sox8_1733.2_v1_sec_ic.txt (SW)</p>	<p>Sox8_1733.2_v2_sec_ic.txt (SW)</p>	<p>Sox8_1733.2_combined_sec_ic_trimmed.txt (SW)</p>
<p>Sox8_1733.2_v1_sec_untrimmed.txt (SW)</p>	<p>Sox8_1733.2_v2_sec_untrimmed.txt (SW)  [Z-Beta = 43.0454]  [Pearson Full 0.665410912, Pearson Zeroed 0.626407]</p>	<p>Sox8_1733.2_combined_sec_untrimmed.txt (SW)</p>
<p>Sox8_1733.2_v1_prim_trimmed.txt (RM8)</p>	<p>Sox8_1733.2_v2_prim_trimmed.txt (RM8)</p>	<p>Sox8_1733.2_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Sox8_1733.2_v1_sec_trimmed.txt (RM8)</p>	<p>Sox8_1733.2_v2_sec_trimmed.txt (RM8)</p>	<p>Sox8_1733.2_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Sox8_1733.2_v1_prim_untrimmed.txt (RM8)  [Z-Beta = 2.27525]  [Pearson Full 0.647944975, Pearson Zeroed 0.647214]</p>	<p>Sox8_1733.2_v2_prim_untrimmed.txt (RM8)</p>	<p>Sox8_1733.2_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Sox8_1733.2_v1_sec_untrimmed.txt (RM8)</p>	<p>Sox8_1733.2_v2_sec_untrimmed.txt (RM8)  [Z-Beta = 43.0454]  [Pearson Full 0.665410912, Pearson Zeroed 0.626407]</p>	<p>Sox8_1733.2_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>

<p>Sox8_1733.2_v1_RM35_prim_trimmed.txt (RM35)</p> 	<p>Sox8_1733.2_v2_RM35_prim_trimmed.txt (RM35)</p> <p>[Z-Beta = 173.048] [Pearson Full 0.665410912, Pearson Zeroed 0.594857]</p> 	<p>Sox8_1733.2_RM_combined_prim_trimmed.txt (RM35)</p> 
<p>Sox8_1733.2_v1_RM35_sec_trimmed.txt (RM35)</p> <p>[Z-Beta = 75.3615] [Pearson Full 0.647944975, Pearson Zeroed 0.609116]</p> 	<p>Sox8_1733.2_v2_RM35_sec_trimmed.txt (RM35)</p> 	<p>Sox8_1733.2_RM_combined_sec_trimmed.txt (RM35)</p> <p>[Z-Beta = 101.873] [Pearson Full 0.706509729, Pearson Zeroed 0.610256]</p> 
<p>Sox8_1733.2_v1_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Sox8_1733.2_v2_RM35_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = 36.4525] [Pearson Full 0.665410912, Pearson Zeroed 0.659046]</p> 	<p>Sox8_1733.2_RM_combined_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = 7.287] [Pearson Full 0.706509729, Pearson Zeroed 0.700416]</p> 
<p>Sox8_1733.2_v1_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Sox8_1733.2_v2_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Sox8_1733.2_RM_combined_sec_untrimmed.txt (RM35)</p> 
<p>Sox8_1733.2_PWM_1.txt (K-afal)</p> <p>[Z-Beta = 5.18092] [Pearson Full 0.647944975, Pearson Zeroed 0.644945]</p> 	<p>Sox8_1733.2_PWM_1.txt (K-afal)</p> 	<p>Sox8_1733.2_combined_PWM_1.txt (K-afal)</p> <p>[Z-Beta = 5.3294] [Pearson Full 0.706509729, Pearson Zeroed 0.705367]</p> 
<p>Sox8_1733.2_PWM_2.txt (K-afal)</p> <p>[Z-Beta = 74.6608] [Pearson Full 0.647944975, Pearson Zeroed 0.593819]</p> 	<p>Sox8_1733.2_PWM_2.txt (K-afal)</p> 	<p>Sox8_1733.2_combined_PWM_2.txt (K-afal)</p> 
<p>Sox8_1733.2_PWM_3.txt (K-afal)</p> <p>[Z-Beta = 2.72409] [Pearson Full 0.647944975, Pearson Zeroed 0.645551]</p> 	<p>Sox8_1733.2_PWM_3.txt (K-afal)</p> 	<p>Sox8_1733.2_combined_PWM_3.txt (K-afal)</p> <p>[Z-Beta = 68.22] [Pearson Full 0.706509729, Pearson Zeroed 0.672932]</p> 
<p>Sox8_1733.2_PWM_4.txt (K-afal)</p> 	<p>Sox8_1733.2_PWM_4.txt (K-afal)</p> 	<p>Sox8_1733.2_combined_PWM_4.txt (K-afal)</p> <p>No PWM Available</p>
<p>Sox8_1733.2_PWM_5.txt (K-afal)</p> <p>[Z-Beta = 77.5106] [Pearson Full 0.647944975, Pearson Zeroed 0.545058]</p> 	<p>Sox8_1733.2_PWM_5.txt (K-afal)</p> <p>No PWM Available</p>	<p>Sox8_1733.2_combined_PWM_5.txt (K-afal)</p> <p>No PWM Available</p>

Sox8\_1733.2\_PWM\_6.txt (K-afal)



Sox8\_1733.2\_PWM\_6.txt (K-afal)

No PWM Available

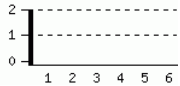
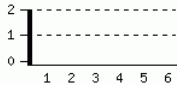

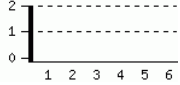
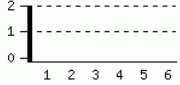

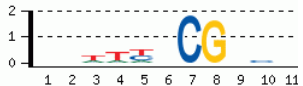



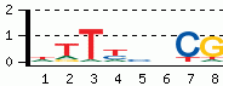

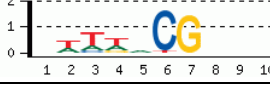
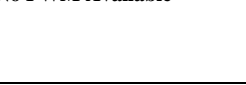

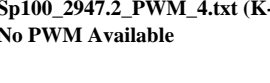
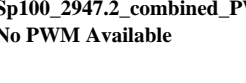
Sox8\_1733.2\_combined\_PWM\_6.txt (K-afal)

No PWM Available





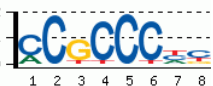

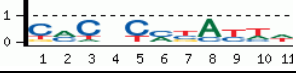
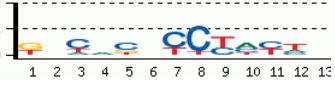
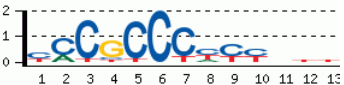


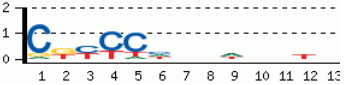

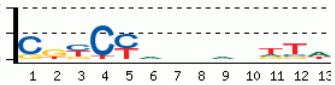
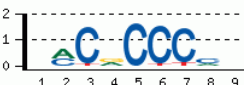









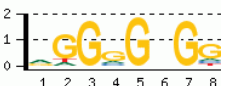









# Sp100\_2947.2

Array Version 1	Array Version 2	Combined
<p>Sp100_2947.2_v1_prim_auc.txt (SW)</p>	<p>Sp100_2947.2_v2_prim_auc.txt (SW)</p>	<p>Sp100_2947.2_combined_prim_auc.txt (SW)</p> <p>No PWM Available</p>
<p>Sp100_2947.2_v1_prim_ic.txt (SW)</p>	<p>Sp100_2947.2_v2_prim_ic.txt (SW)</p>	<p>Sp100_2947.2_combined_prim_ic_trimmed.txt (SW)</p>
<p>Sp100_2947.2_v1_prim_untrimmed.txt (SW)</p>	<p>Sp100_2947.2_v2_prim_untrimmed.txt (SW)</p>	<p>Sp100_2947.2_combined_prim_untrimmed.txt (SW)</p>
<p>Sp100_2947.2_v1_sec_auc.txt (SW)</p>	<p>Sp100_2947.2_v2_sec_auc.txt (SW)</p>	<p>Sp100_2947.2_combined_sec_auc.txt (SW)</p>
<p>Sp100_2947.2_v1_sec_ic.txt (SW)</p> <p>[Z-Beta = 8.87537] [Pearson Full 0.541657862, Pearson Zeroed 0.533028]</p>	<p>Sp100_2947.2_v2_sec_ic.txt (SW)</p>	<p>Sp100_2947.2_combined_sec_ic_trimmed.txt (SW)</p> <p>[Z-Beta = 20.0101] [Pearson Full 0.51377298, Pearson Zeroed 0.46199]</p>
<p>Sp100_2947.2_v1_sec_untrimmed.txt (SW)</p>	<p>Sp100_2947.2_v2_sec_untrimmed.txt (SW)</p>	<p>Sp100_2947.2_combined_sec_untrimmed.txt (SW)</p>
<p>Sp100_2947.2_v1_prim_trimmed.txt (RM8)</p>	<p>Sp100_2947.2_v2_prim_trimmed.txt (RM8)</p>	<p>Sp100_2947.2_RM8_combined_prim_trimmed.txt (RM8)</p> <p>No PWM Available</p>
<p>Sp100_2947.2_v1_sec_trimmed.txt (RM8)</p>	<p>Sp100_2947.2_v2_sec_trimmed.txt (RM8)</p>	<p>Sp100_2947.2_RM8_combined_sec_trimmed.txt (RM8)</p> <p>No PWM Available</p>
<p>Sp100_2947.2_v1_prim_untrimmed.txt (RM8)</p>	<p>Sp100_2947.2_v2_prim_untrimmed.txt (RM8)</p>	<p>Sp100_2947.2_RM8_combined_prim_untrimmed.txt (RM8)</p> <p>No PWM Available</p>
<p>Sp100_2947.2_v1_sec_untrimmed.txt (RM8)</p>	<p>Sp100_2947.2_v2_sec_untrimmed.txt (RM8)</p>	<p>Sp100_2947.2_RM8_combined_sec_untrimmed.txt (RM8)</p> <p>No PWM Available</p>
<p>Sp100_2947.2_v1_RM35_prim_trimmed.txt (RM35)</p>	<p>Sp100_2947.2_v2_RM35_prim_trimmed.txt (RM35)</p>	<p>Sp100_2947.2_RM_combined_prim_trimmed.txt (RM35)</p>
<p>Sp100_2947.2_v1_RM35_sec_trimmed.txt (RM35)</p>	<p>Sp100_2947.2_v2_RM35_sec_trimmed.txt (RM35)</p> <p>[Z-Beta = 41.6605] [Pearson Full 0.595181432, Pearson Zeroed 0.573137]</p>	<p>Sp100_2947.2_RM_combined_sec_trimmed.txt (RM35)</p>

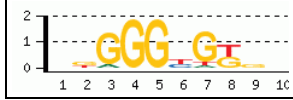
		
Sp100_2947.2_v1_RM35_prim_untrimmed.txt (RM35)	Sp100_2947.2_v2_RM35_prim_untrimmed.txt (RM35)	Sp100_2947.2_RM_combined_prim_untrimmed.txt (RM35)
		
Sp100_2947.2_v1_RM35_sec_untrimmed.txt (RM35) [Z-Beta = 36.9224] [Pearson Full 0.541657862, Pearson Zeroed 0.518822]	Sp100_2947.2_v2_RM35_sec_untrimmed.txt (RM35)	Sp100_2947.2_RM_combined_sec_untrimmed.txt (RM35) [Z-Beta = 4.21861] [Pearson Full 0.51377298, Pearson Zeroed 0.508771]
		
Sp100_2947.2_PWM_1.txt (K-afal) [Z-Beta = 109.777] [Pearson Full 0.541657862, Pearson Zeroed 0.419421]	Sp100_2947.2_PWM_1.txt (K-afal) [Z-Beta = 23.2986] [Pearson Full 0.595181432, Pearson Zeroed 0.587706]	Sp100_2947.2_combined_PWM_1.txt (K-afal) [Z-Beta = 86.6323] [Pearson Full 0.51377298, Pearson Zeroed 0.423811]
		
Sp100_2947.2_PWM_2.txt (K-afal)	Sp100_2947.2_PWM_2.txt (K-afal) [Z-Beta = 48.5153] [Pearson Full 0.595181432, Pearson Zeroed 0.55935]	Sp100_2947.2_combined_PWM_2.txt (K-afal)
		
Sp100_2947.2_PWM_3.txt (K-afal) [Z-Beta = 10.3831] [Pearson Full 0.541657862, Pearson Zeroed 0.532916]	Sp100_2947.2_PWM_3.txt (K-afal) [Z-Beta = 80.4211] [Pearson Full 0.595181432, Pearson Zeroed 0.566954]	Sp100_2947.2_combined_PWM_3.txt (K-afal) No PWM Available
		
Sp100_2947.2_PWM_4.txt (K-afal)	Sp100_2947.2_PWM_4.txt (K-afal) No PWM Available	Sp100_2947.2_combined_PWM_4.txt (K-afal) No PWM Available

# Sp4\_1011.2

Array Version 1	Array Version 2	Combined
<p>Sp4_1011.2_v1_prim_auc.txt (SW) [Z-Beta = 201.469] [Pearson Full 0.706212808, Pearson Zeroed 0.587326]</p>	<p>Sp4_1011.2_v2_prim_auc.txt (SW) [Z-Beta = 37.9446] [Pearson Full 0.550435344, Pearson Zeroed 0.560687]</p>	<p>Sp4_1011.2_combined_prim_auc.txt (SW)</p>
<p>Sp4_1011.2_v1_prim_ic.txt (SW)</p>	<p>Sp4_1011.2_v2_prim_ic.txt (SW)</p>	<p>Sp4_1011.2_combined_prim_ic_trimmed.txt (SW) [Z-Beta = 63.0337] [Pearson Full 0.68004694, Pearson Zeroed 0.604353]</p>
<p>Sp4_1011.2_v1_prim_untrimmed.txt (SW)</p>	<p>Sp4_1011.2_v2_prim_untrimmed.txt (SW)</p>	<p>Sp4_1011.2_combined_prim_untrimmed.txt (SW)</p>
<p>Sp4_1011.2_v1_sec_auc.txt (SW)</p>	<p>Sp4_1011.2_v2_sec_auc.txt (SW)</p>	<p>Sp4_1011.2_combined_sec_auc.txt (SW)</p>
<p>Sp4_1011.2_v1_sec_ic.txt (SW)</p>	<p>Sp4_1011.2_v2_sec_ic.txt (SW)</p>	<p>Sp4_1011.2_combined_sec_ic_trimmed.txt (SW)</p>
<p>Sp4_1011.2_v1_sec_untrimmed.txt (SW)</p>	<p>Sp4_1011.2_v2_sec_untrimmed.txt (SW) [Z-Beta = -56.3627] [Pearson Full 0.550435344, Pearson Zeroed 0.375883]</p>	<p>Sp4_1011.2_combined_sec_untrimmed.txt (SW)</p>
<p>Sp4_1011.2_v1_prim_trimmed.txt (RM8)</p>	<p>Sp4_1011.2_v2_prim_trimmed.txt (RM8)</p>	<p>Sp4_1011.2_RM8_combined_prim_trimmed.txt (RM8) No PWM Available</p>
<p>Sp4_1011.2_v1_sec_trimmed.txt (RM8)</p>	<p>Sp4_1011.2_v2_sec_trimmed.txt (RM8)</p>	<p>Sp4_1011.2_RM8_combined_sec_trimmed.txt (RM8) No PWM Available</p>
<p>Sp4_1011.2_v1_prim_untrimmed.txt (RM8)</p>	<p>Sp4_1011.2_v2_prim_untrimmed.txt (RM8)</p>	<p>Sp4_1011.2_RM8_combined_prim_untrimmed.txt (RM8) No PWM Available</p>
<p>Sp4_1011.2_v1_sec_untrimmed.txt (RM8)</p>	<p>Sp4_1011.2_v2_sec_untrimmed.txt (RM8) [Z-Beta = -56.3627] [Pearson Full 0.550435344, Pearson Zeroed 0.375883]</p>	<p>Sp4_1011.2_RM8_combined_sec_untrimmed.txt (RM8) No PWM Available</p>

<p>Sp4_1011.2_v1_RM35_prim_trimmed.txt (RM35)</p> 	<p>Sp4_1011.2_v2_RM35_prim_trimmed.txt (RM35)</p> 	<p>Sp4_1011.2_RM_combined_prim_trimmed.txt (RM35)</p> 
<p>Sp4_1011.2_v1_RM35_sec_trimmed.txt (RM35)</p> 	<p>Sp4_1011.2_v2_RM35_sec_trimmed.txt (RM35)  [Z-Beta = 5.71207]  [Pearson Full 0.550435344, Pearson Zeroed 0.544575]</p> 	<p>Sp4_1011.2_RM_combined_sec_trimmed.txt (RM35)</p> 
<p>Sp4_1011.2_v1_RM35_prim_untrimmed.txt (RM35)  [Z-Beta = 29.9277]  [Pearson Full 0.706212808, Pearson Zeroed 0.695005]</p> 	<p>Sp4_1011.2_v2_RM35_prim_untrimmed.txt (RM35)  [Z-Beta = 0.0335185]  [Pearson Full 0.550435344, Pearson Zeroed 0.550453]</p> 	<p>Sp4_1011.2_RM_combined_prim_untrimmed.txt (RM35)  [Z-Beta = 30.6885]  [Pearson Full 0.68004694, Pearson Zeroed 0.643813]</p> 
<p>Sp4_1011.2_v1_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Sp4_1011.2_v2_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Sp4_1011.2_RM_combined_sec_untrimmed.txt (RM35)</p> 
<p>Sp4_1011.2_PWM_1.txt (K-afal)  [Z-Beta = 170.418]  [Pearson Full 0.706212808, Pearson Zeroed 0.59552]</p> 	<p>Sp4_1011.2_PWM_1.txt (K-afal)  [Z-Beta = 115.494]  [Pearson Full 0.550435344, Pearson Zeroed 0.19153]</p> 	<p>Sp4_1011.2_combined_PWM_1.txt (K-afal)  [Z-Beta = 52.5368]  [Pearson Full 0.68004694, Pearson Zeroed 0.63424]</p> 
<p>Sp4_1011.2_PWM_2.txt (K-afal)</p> 	<p>Sp4_1011.2_PWM_2.txt (K-afal)</p> 	<p>Sp4_1011.2_combined_PWM_2.txt (K-afal)  [Z-Beta = 6.49047]  [Pearson Full 0.68004694, Pearson Zeroed 0.676161]</p> 
<p>Sp4_1011.2_PWM_3.txt (K-afal)</p> 	<p>Sp4_1011.2_PWM_3.txt (K-afal)</p> 	<p>Sp4_1011.2_combined_PWM_3.txt (K-afal)</p> 
<p>Sp4_1011.2_PWM_4.txt (K-afal)</p> 	<p>Sp4_1011.2_PWM_4.txt (K-afal)</p> 	<p>Sp4_1011.2_combined_PWM_4.txt (K-afal)</p> 
<p>Sp4_1011.2_PWM_5.txt (K-afal)</p> 	<p>Sp4_1011.2_PWM_5.txt (K-afal)</p> 	<p>Sp4_1011.2_combined_PWM_5.txt (K-afal)</p> 
<p>Sp4_1011.2_PWM_6.txt (K-afal)</p> 	<p>Sp4_1011.2_PWM_6.txt (K-afal)</p> 	<p>Sp4_1011.2_combined_PWM_6.txt (K-afal)</p> 
<p>Sp4_1011.2_PWM_7.txt (K-afal)</p> 	<p>Sp4_1011.2_PWM_7.txt (K-afal)  No PWM Available</p>	<p>Sp4_1011.2_combined_PWM_7.txt (K-afal)</p> 



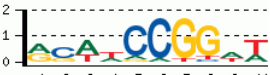

















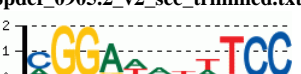


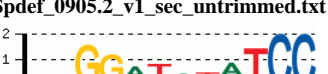
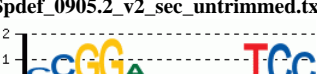



Sp4\_1011.2\_PWM\_8.txt (K-afal)

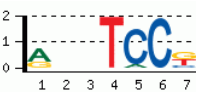
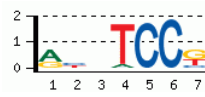
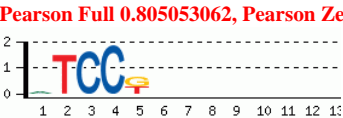
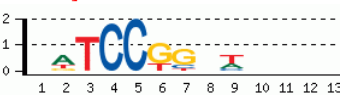
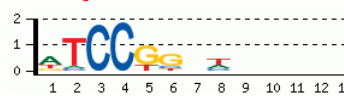
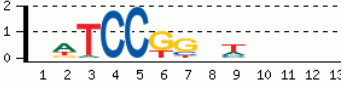

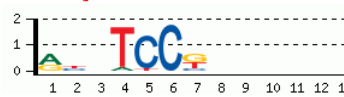

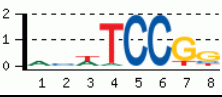
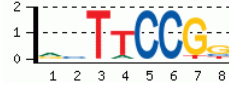


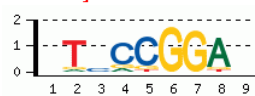

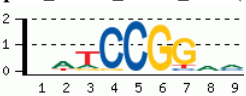
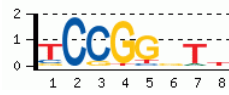

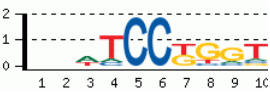



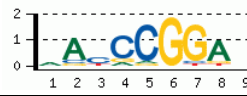
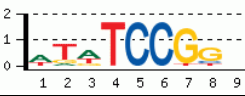





Sp4\_1011.2\_PWM\_8.txt (K-afal)  
No PWM Available








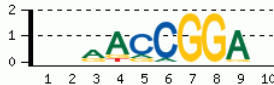


Sp4\_1011.2\_combined\_PWM\_8.txt (K-afal)  
No PWM Available

# Spdef\_0905.2

Array Version 1	Array Version 2	Combined
<p>Spdef_0905.2_v1_prim_auc.txt (SW)  [Z-Beta = 81.9646]  [Pearson Full 0.788305553, Pearson Zeroed 0.779576]</p> 	<p>Spdef_0905.2_v2_prim_auc.txt (SW)  [Z-Beta = 50.5729]  [Pearson Full 0.733841341, Pearson Zeroed 0.732025]</p> 	<p>Spdef_0905.2_combined_prim_auc.txt (SW)  No PWM Available</p>
<p>Spdef_0905.2_v1_prim_ic.txt (SW)</p> 	<p>Spdef_0905.2_v2_prim_ic.txt (SW)</p> 	<p>Spdef_0905.2_combined_prim_ic_trimmed.txt (SW)</p> 
<p>Spdef_0905.2_v1_prim_untrimmed.txt (SW)</p> 	<p>Spdef_0905.2_v2_prim_untrimmed.txt (SW)</p> 	<p>Spdef_0905.2_combined_prim_untrimmed.txt (SW)</p> 
<p>Spdef_0905.2_v1_sec_auc.txt (SW)</p> 	<p>Spdef_0905.2_v2_sec_auc.txt (SW)</p> 	<p>Spdef_0905.2_combined_sec_auc.txt (SW)</p> 
<p>Spdef_0905.2_v1_sec_ic.txt (SW)</p> 	<p>Spdef_0905.2_v2_sec_ic.txt (SW)</p> 	<p>Spdef_0905.2_combined_sec_ic_trimmed.txt (SW)</p> 
<p>Spdef_0905.2_v1_sec_untrimmed.txt (SW)</p> 	<p>Spdef_0905.2_v2_sec_untrimmed.txt (SW)</p> 	<p>Spdef_0905.2_combined_sec_untrimmed.txt (SW)</p> 
<p>Spdef_0905.2_v1_prim_trimmed.txt (RM8)  [Z-Beta = 25.1455]  [Pearson Full 0.788305553, Pearson Zeroed 0.788269]</p> 	<p>Spdef_0905.2_v2_prim_trimmed.txt (RM8)</p> 	<p>Spdef_0905.2_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Spdef_0905.2_v1_sec_trimmed.txt (RM8)</p> 	<p>Spdef_0905.2_v2_sec_trimmed.txt (RM8)</p> 	<p>Spdef_0905.2_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Spdef_0905.2_v1_prim_untrimmed.txt (RM8)</p> 	<p>Spdef_0905.2_v2_prim_untrimmed.txt (RM8)</p> 	<p>Spdef_0905.2_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Spdef_0905.2_v1_sec_untrimmed.txt (RM8)</p> 	<p>Spdef_0905.2_v2_sec_untrimmed.txt (RM8)</p> 	<p>Spdef_0905.2_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>
<p>Spdef_0905.2_v1_RM35_prim_trimmed.txt (RM35)</p> 	<p>Spdef_0905.2_v2_RM35_prim_trimmed.txt (RM35)</p> 	<p>Spdef_0905.2_RM_combined_prim_trimmed.txt (RM35)  [Z-Beta = 212.78]  [Pearson Full 0.805053062, Pearson Zeroed 0.758339]</p> 

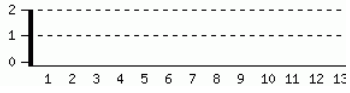
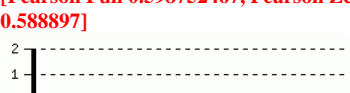
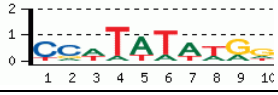


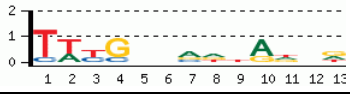
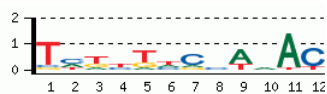
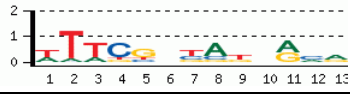


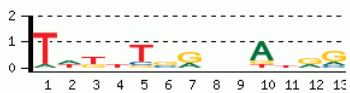
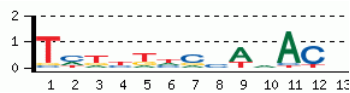
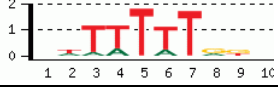


<p>Spdef_0905.2_v1_RM35_sec_trimmed.txt (RM35)</p> 	<p>Spdef_0905.2_v2_RM35_sec_trimmed.txt (RM35)</p> 	<p>Spdef_0905.2_RM_combined_sec_trimmed.txt (RM35)</p> <p>[Z-Beta = 51.5067] [Pearson Full 0.805053062, Pearson Zeroed 0.787415]</p> 
<p>Spdef_0905.2_v1_RM35_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = 199.201] [Pearson Full 0.788305553, Pearson Zeroed 0.762551]</p> 	<p>Spdef_0905.2_v2_RM35_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = 125.904] [Pearson Full 0.733841341, Pearson Zeroed 0.691338]</p> 	<p>Spdef_0905.2_RM_combined_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = 19.2105] [Pearson Full 0.805053062, Pearson Zeroed 0.806259]</p> 
<p>Spdef_0905.2_v1_RM35_sec_untrimmed.txt (RM35)</p> <p>[Z-Beta = 95.7181] [Pearson Full 0.788305553, Pearson Zeroed 0.754318]</p> 	<p>Spdef_0905.2_v2_RM35_sec_untrimmed.txt (RM35)</p> <p>[Z-Beta = 111.963] [Pearson Full 0.733841341, Pearson Zeroed 0.658164]</p> 	<p>Spdef_0905.2_RM_combined_sec_untrimmed.txt (RM35)</p> <p>[Z-Beta = 52.6775] [Pearson Full 0.805053062, Pearson Zeroed 0.78681]</p> 
<p>Spdef_0905.2_PWM_1.txt (K-afal)</p> <p>[Z-Beta = 155.451] [Pearson Full 0.788305553, Pearson Zeroed 0.767051]</p> 	<p>Spdef_0905.2_PWM_1.txt (K-afal)</p> 	<p>Spdef_0905.2_combined_PWM_1.txt (K-afal)</p> <p>[Z-Beta = 49.3095] [Pearson Full 0.805053062, Pearson Zeroed 0.80242]</p> 
<p>Spdef_0905.2_PWM_2.txt (K-afal)</p> <p>[Z-Beta = 3.43583] [Pearson Full 0.788305553, Pearson Zeroed 0.787949]</p> 	<p>Spdef_0905.2_PWM_2.txt (K-afal)</p> <p>[Z-Beta = 11.022] [Pearson Full 0.733841341, Pearson Zeroed 0.731164]</p> 	<p>Spdef_0905.2_combined_PWM_2.txt (K-afal)</p> 
<p>Spdef_0905.2_PWM_3.txt (K-afal)</p> 	<p>Spdef_0905.2_PWM_3.txt (K-afal)</p> <p>[Z-Beta = 24.1154] [Pearson Full 0.733841341, Pearson Zeroed 0.728511]</p> 	<p>Spdef_0905.2_combined_PWM_3.txt (K-afal)</p> 
<p>Spdef_0905.2_PWM_4.txt (K-afal)</p> 	<p>Spdef_0905.2_PWM_4.txt (K-afal)</p> <p>[Z-Beta = 5.83634] [Pearson Full 0.733841341, Pearson Zeroed 0.732138]</p> 	<p>Spdef_0905.2_combined_PWM_4.txt (K-afal)</p> <p>[Z-Beta = 66.7291] [Pearson Full 0.805053062, Pearson Zeroed 0.780899]</p> 
<p>Spdef_0905.2_PWM_5.txt (K-afal)</p> 	<p>Spdef_0905.2_PWM_5.txt (K-afal)</p> 	<p>Spdef_0905.2_combined_PWM_5.txt (K-afal)</p> 
<p>Spdef_0905.2_PWM_6.txt (K-afal)</p> 	<p>Spdef_0905.2_PWM_6.txt (K-afal)</p> <p>[Z-Beta = 8.05191] [Pearson Full 0.733841341, Pearson Zeroed 0.733036]</p> 	<p>Spdef_0905.2_combined_PWM_6.txt (K-afal)</p> 



<p>Spdef_0905.2_PWM_7.txt (K-afal)  [Z-Beta = 10.4854]  [Pearson Full 0.788305553, Pearson Zeroed 0.785949]</p> 	<p>Spdef_0905.2_PWM_7.txt (K-afal)</p> 	<p>Spdef_0905.2_combined_PWM_7.txt (K-afal)</p> 
<p>Spdef_0905.2_PWM_8.txt (K-afal)  [Z-Beta = 38.419]  [Pearson Full 0.788305553, Pearson Zeroed 0.782093]</p> 	<p>Spdef_0905.2_PWM_8.txt (K-afal)  [Z-Beta = 35.3497]  [Pearson Full 0.733841341, Pearson Zeroed 0.729886]</p> 	<p>Spdef_0905.2_combined_PWM_8.txt (K-afal)  No PWM Available</p>
<p>Spdef_0905.2_PWM_9.txt (K-afal)</p> 	<p>Spdef_0905.2_PWM_9.txt (K-afal)</p> 	<p>Spdef_0905.2_combined_PWM_9.txt (K-afal)  No PWM Available</p>
<p>Spdef_0905.2_PWM_10.txt (Kafal)  No PWM Available</p>	<p>Spdef_0905.2_PWM_10.txt (Kafal)</p> 	<p>Spdef_0905.2_combined_PWM_10.txt (Kafal)  No PWM Available</p>
<p>Spdef_0905.2_PWM_11.txt (Kafal)  No PWM Available</p>	<p>Spdef_0905.2_PWM_11.txt (Kafal)  [Z-Beta = 12.929]  [Pearson Full 0.733841341, Pearson Zeroed 0.730402]</p> 	<p>Spdef_0905.2_combined_PWM_11.txt (Kafal)  No PWM Available</p>
<p>Spdef_0905.2_PWM_12.txt (Kafal)  No PWM Available</p>	<p>Spdef_0905.2_PWM_12.txt (Kafal)</p> 	<p>Spdef_0905.2_combined_PWM_12.txt (Kafal)  No PWM Available</p>

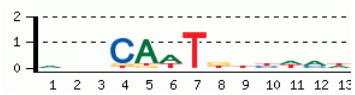
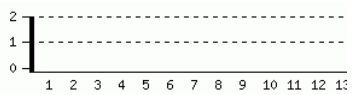

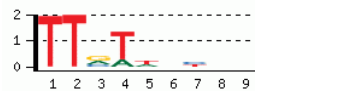
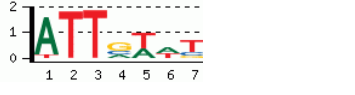
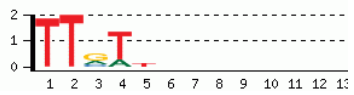
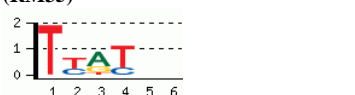
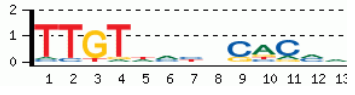

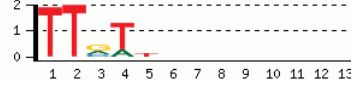
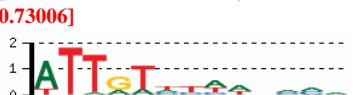
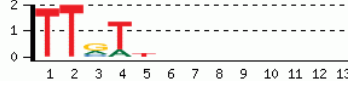

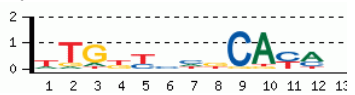



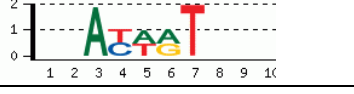
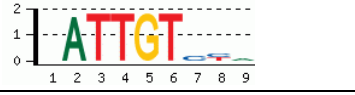
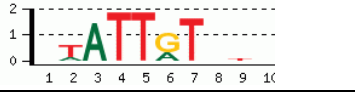

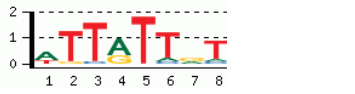

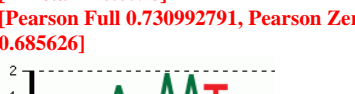
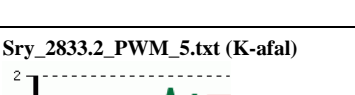
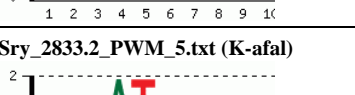
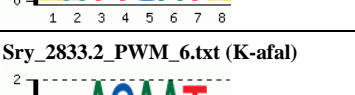
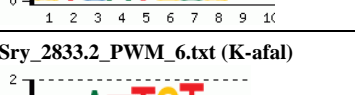
# Srf\_3509.1

Array Version 1	Array Version 2	Combined
<p>Srf_3509.1_v1_prim_auc.txt (SW)</p>	<p>Srf_3509.1_v2_prim_auc.txt (SW)</p>	<p>Srf_3509.1_combined_prim_auc.txt (SW)</p>
<p>Srf_3509.1_v1_prim_ic.txt (SW)  [Z-Beta = 19.372]  [Pearson Full 0.670303215, Pearson Zeroed 0.660454]</p>	<p>Srf_3509.1_v2_prim_ic.txt (SW)</p>	<p>Srf_3509.1_combined_prim_ic_trimmed.txt (SW)  [Z-Beta = -68.5253]  [Pearson Full 0.530509697, Pearson Zeroed 0.447412]</p>
<p>Srf_3509.1_v1_prim_untrimmed.txt (SW)</p>	<p>Srf_3509.1_v2_prim_untrimmed.txt (SW)</p>	<p>Srf_3509.1_combined_prim_untrimmed.txt (SW)</p>
<p>Srf_3509.1_v1_sec_auc.txt (SW)  [Z-Beta = -8.40158]  [Pearson Full 0.670303215, Pearson Zeroed 0.668469]</p>	<p>Srf_3509.1_v2_sec_auc.txt (SW)</p>	<p>Srf_3509.1_combined_sec_auc.txt (SW)</p>
<p>Srf_3509.1_v1_sec_ic.txt (SW)  [Z-Beta = 0.404436]  [Pearson Full 0.670303215, Pearson Zeroed 0.670286]</p>	<p>Srf_3509.1_v2_sec_ic.txt (SW)</p>	<p>Srf_3509.1_combined_sec_ic_trimmed.txt (SW)  [Z-Beta = 35.0131]  [Pearson Full 0.530509697, Pearson Zeroed 0.503098]</p>
<p>Srf_3509.1_v1_sec_untrimmed.txt (SW)</p>	<p>Srf_3509.1_v2_sec_untrimmed.txt (SW)  [Z-Beta = 106.765]  [Pearson Full 0.598752467, Pearson Zeroed 0.588897]</p>	<p>Srf_3509.1_combined_sec_untrimmed.txt (SW)</p>
<p>Srf_3509.1_v1_prim_trimmed.txt (RM8)  [Z-Beta = 29.2264]  [Pearson Full 0.670303215, Pearson Zeroed 0.660921]</p>	<p>Srf_3509.1_v2_prim_trimmed.txt (RM8)</p>	<p>Srf_3509.1_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Srf_3509.1_v1_sec_trimmed.txt (RM8)  [Z-Beta = 126.53]  [Pearson Full 0.670303215, Pearson Zeroed 0.487524]</p>	<p>Srf_3509.1_v2_sec_trimmed.txt (RM8)  [Z-Beta = 31.7729]  [Pearson Full 0.598752467, Pearson Zeroed 0.597289]</p>	<p>Srf_3509.1_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Srf_3509.1_v1_prim_untrimmed.txt (RM8)</p>	<p>Srf_3509.1_v2_prim_untrimmed.txt (RM8)</p>	<p>Srf_3509.1_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>

<p>Srf_3509.1_v1_sec_untrimmed.txt (RM8)</p> 	<p>Srf_3509.1_v2_sec_untrimmed.txt (RM8)  [Z-Beta = 106.765]  [Pearson Full 0.598752467, Pearson Zeroed 0.588897]</p> 	<p>Srf_3509.1_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>
<p>Srf_3509.1_v1_RM35_prim_trimmed.txt (RM35)  [Z-Beta = -2.44499]  [Pearson Full 0.670303215, Pearson Zeroed 0.670013]</p> 	<p>Srf_3509.1_v2_RM35_prim_trimmed.txt (RM35)</p> 	<p>Srf_3509.1_RM_combined_prim_trimmed.txt (RM35)</p> 
<p>Srf_3509.1_v1_RM35_sec_trimmed.txt (RM35)  [Z-Beta = 9.58473]  [Pearson Full 0.670303215, Pearson Zeroed 0.668843]</p> 	<p>Srf_3509.1_v2_RM35_sec_trimmed.txt (RM35)</p> 	<p>Srf_3509.1_RM_combined_sec_trimmed.txt (RM35)  [Z-Beta = 36.9022]  [Pearson Full 0.530509697, Pearson Zeroed 0.496418]</p> 
<p>Srf_3509.1_v1_RM35_prim_untrimmed.txt (RM35)  [Z-Beta = 1.77408]  [Pearson Full 0.670303215, Pearson Zeroed 0.670184]</p> 	<p>Srf_3509.1_v2_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Srf_3509.1_RM_combined_prim_untrimmed.txt (RM35)  [Z-Beta = 49.7408]  [Pearson Full 0.530509697, Pearson Zeroed 0.472387]</p> 
<p>Srf_3509.1_v1_RM35_sec_untrimmed.txt (RM35)  [Z-Beta = 6.04122]  [Pearson Full 0.670303215, Pearson Zeroed 0.669501]</p> 	<p>Srf_3509.1_v2_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Srf_3509.1_RM_combined_sec_untrimmed.txt (RM35)  [Z-Beta = 17.1722]  [Pearson Full 0.530509697, Pearson Zeroed 0.522823]</p> 
<p>Srf_3509.1_PWM_1.txt (K-afal)  [Z-Beta = 4.4857]  [Pearson Full 0.670303215, Pearson Zeroed 0.66986]</p> 	<p>Srf_3509.1_PWM_1.txt (K-afal)</p> 	<p>Srf_3509.1_combined_PWM_1.txt (K-afal)  [Z-Beta = -62.0229]  [Pearson Full 0.530509697, Pearson Zeroed 0.488237]</p> 
<p>Srf_3509.1_PWM_2.txt (K-afal)</p> 	<p>Srf_3509.1_PWM_2.txt (K-afal)</p> 	<p>Srf_3509.1_combined_PWM_2.txt (K-afal)  [Z-Beta = 94.0829]  [Pearson Full 0.530509697, Pearson Zeroed 0.298757]</p> 

# Sry\_2833.2

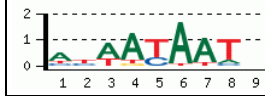
Array Version 1	Array Version 2	Combined
<p>Sry_2833.2_v1_prim_auc.txt (SW)  [Z-Beta = 53.8795]  [Pearson Full 0.585447023, Pearson Zeroed 0.583187]</p>	<p>Sry_2833.2_v2_prim_auc.txt (SW)</p>	<p>Sry_2833.2_combined_prim_auc.txt (SW)</p>
<p>Sry_2833.2_v1_prim_ic.txt (SW)</p>	<p>Sry_2833.2_v2_prim_ic.txt (SW)</p>	<p>Sry_2833.2_combined_prim_ic_trimmed.txt (SW)</p>
<p>Sry_2833.2_v1_prim_untrimmed.txt (SW)</p>	<p>Sry_2833.2_v2_prim_untrimmed.txt (SW)</p>	<p>Sry_2833.2_combined_prim_untrimmed.txt (SW)</p>
<p>Sry_2833.2_v1_sec_auc.txt (SW)</p>	<p>Sry_2833.2_v2_sec_auc.txt (SW)</p>	<p>Sry_2833.2_combined_sec_auc.txt (SW)</p>
<p>Sry_2833.2_v1_sec_ic.txt (SW)</p>	<p>Sry_2833.2_v2_sec_ic.txt (SW)</p>	<p>Sry_2833.2_combined_sec_ic_trimmed.txt (SW)  [Z-Beta = 26.5987]  [Pearson Full 0.595242233, Pearson Zeroed 0.524288]</p>
<p>Sry_2833.2_v1_sec_untrimmed.txt (SW)</p>	<p>Sry_2833.2_v2_sec_untrimmed.txt (SW)  [Z-Beta = 90.9938]  [Pearson Full 0.730992791, Pearson Zeroed 0.592494]</p>	<p>Sry_2833.2_combined_sec_untrimmed.txt (SW)</p>
<p>Sry_2833.2_v1_prim_trimmed.txt (RM8)</p>	<p>Sry_2833.2_v2_prim_trimmed.txt (RM8)</p>	<p>Sry_2833.2_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Sry_2833.2_v1_sec_trimmed.txt (RM8)</p>	<p>Sry_2833.2_v2_sec_trimmed.txt (RM8)  [Z-Beta = 53.3927]  [Pearson Full 0.730992791, Pearson Zeroed 0.710848]</p>	<p>Sry_2833.2_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Sry_2833.2_v1_prim_untrimmed.txt (RM8)</p>	<p>Sry_2833.2_v2_prim_untrimmed.txt (RM8)</p>	<p>Sry_2833.2_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Sry_2833.2_v1_sec_untrimmed.txt (RM8)  [Z-Beta = 34.8073]  [Pearson Full 0.585447023, Pearson Zeroed 0.550877]</p>	<p>Sry_2833.2_v2_sec_untrimmed.txt (RM8)  [Z-Beta = 90.9938]  [Pearson Full 0.730992791, Pearson Zeroed 0.592494]</p>	<p>Sry_2833.2_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>

		
<p>Sry_2833.2_v1_RM35_prim_trimmed.txt (RM35)  [Z-Beta = 23.5551]  [Pearson Full 0.585447023, Pearson Zeroed 0.55771]</p> 	<p>Sry_2833.2_v2_RM35_prim_trimmed.txt (RM35)</p> 	<p>Sry_2833.2_RM_combined_prim_trimmed.txt (RM35)  [Z-Beta = 7.59895]  [Pearson Full 0.595242233, Pearson Zeroed 0.555423]</p> 
<p>Sry_2833.2_v1_RM35_sec_trimmed.txt (RM35)</p> 	<p>Sry_2833.2_v2_RM35_sec_trimmed.txt (RM35)</p> 	<p>Sry_2833.2_RM_combined_sec_trimmed.txt (RM35)</p> 
<p>Sry_2833.2_v1_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Sry_2833.2_v2_RM35_prim_untrimmed.txt (RM35)  [Z-Beta = 2.18988]  [Pearson Full 0.730992791, Pearson Zeroed 0.73006]</p> 	<p>Sry_2833.2_RM_combined_prim_untrimmed.txt (RM35)</p> 
<p>Sry_2833.2_v1_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Sry_2833.2_v2_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Sry_2833.2_RM_combined_sec_untrimmed.txt (RM35)</p> 
<p>Sry_2833.2_PWM_1.txt (K-afal)  [Z-Beta = 44.7458]  [Pearson Full 0.585447023, Pearson Zeroed 0.577593]</p> 	<p>Sry_2833.2_PWM_1.txt (K-afal)</p> 	<p>Sry_2833.2_combined_PWM_1.txt (K-afal)  [Z-Beta = 23.305]  [Pearson Full 0.595242233, Pearson Zeroed 0.488743]</p> 
<p>Sry_2833.2_PWM_2.txt (K-afal)</p> 	<p>Sry_2833.2_PWM_2.txt (K-afal)</p> 	<p>Sry_2833.2_combined_PWM_2.txt (K-afal)  No PWM Available</p>
<p>Sry_2833.2_PWM_3.txt (K-afal)</p> 	<p>Sry_2833.2_PWM_3.txt (K-afal)</p> 	<p>Sry_2833.2_combined_PWM_3.txt (K-afal)  No PWM Available</p>
<p>Sry_2833.2_PWM_4.txt (K-afal)</p> 	<p>Sry_2833.2_PWM_4.txt (K-afal)  [Z-Beta = 40.0596]  [Pearson Full 0.730992791, Pearson Zeroed 0.685626]</p> 	<p>Sry_2833.2_combined_PWM_4.txt (K-afal)  No PWM Available</p>
<p>Sry_2833.2_PWM_5.txt (K-afal)</p> 	<p>Sry_2833.2_PWM_5.txt (K-afal)</p> 	<p>Sry_2833.2_combined_PWM_5.txt (K-afal)  No PWM Available</p>
<p>Sry_2833.2_PWM_6.txt (K-afal)</p> 	<p>Sry_2833.2_PWM_6.txt (K-afal)</p> 	<p>Sry_2833.2_combined_PWM_6.txt (K-afal)  No PWM Available</p>

Sry\_2833.2\_PWM\_7.txt (K-afal)

[Z-Beta = 13.8391]

[Pearson Full 0.585447023, Pearson Zeroed  
0.572821]



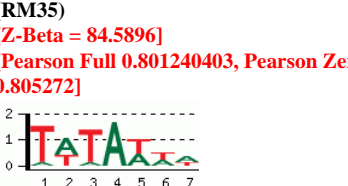

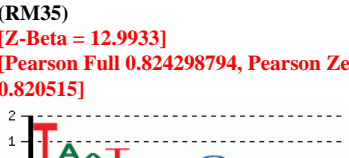
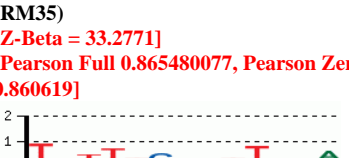
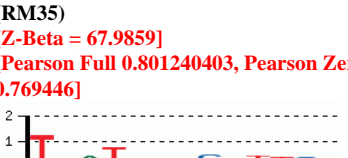
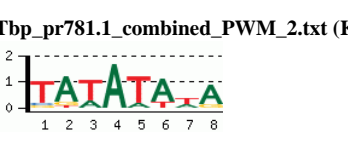
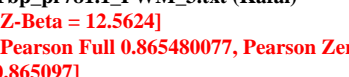
Sry\_2833.2\_PWM\_7.txt (K-afal)  
No PWM Available

Sry\_2833.2\_combined\_PWM\_7.txt (K-afal)  
No PWM Available

# Tbp\_pr781.1

Array Version 1	Array Version 2	Combined
<p>Tbp_pr781.1_v1_prim_auc.txt (SW)</p>	<p>Tbp_pr781.1_v2_prim_auc.txt (SW)  [Z-Beta = 25.8561]  [Pearson Full 0.865480077, Pearson Zeroed 0.863559]</p>	<p>Tbp_pr781.1_combined_prim_auc.txt (SW)  No PWM Available</p>
<p>Tbp_pr781.1_v1_prim_ic.txt (SW)</p>	<p>Tbp_pr781.1_v2_prim_ic.txt (SW)</p>	<p>Tbp_pr781.1_combined_prim_ic_trimmed.txt (SW)</p>
<p>Tbp_pr781.1_v1_prim_untrimmed.txt (SW)</p>	<p>Tbp_pr781.1_v2_prim_untrimmed.txt (SW)  [Z-Beta = -141.032]  [Pearson Full 0.865480077, Pearson Zeroed 0.830337]</p>	<p>Tbp_pr781.1_combined_prim_untrimmed.txt (SW)</p>
<p>Tbp_pr781.1_v1_sec_auc.txt (SW)  [Z-Beta = 9.69149]  [Pearson Full 0.824298794, Pearson Zeroed 0.819851]</p>	<p>Tbp_pr781.1_v2_sec_auc.txt (SW)</p>	<p>Tbp_pr781.1_combined_sec_auc.txt (SW)</p>
<p>Tbp_pr781.1_v1_sec_ic.txt (SW)</p>	<p>Tbp_pr781.1_v2_sec_ic.txt (SW)  [Z-Beta = 26.3553]  [Pearson Full 0.865480077, Pearson Zeroed 0.862399]</p>	<p>Tbp_pr781.1_combined_sec_ic_trimmed.txt (SW)</p>
<p>Tbp_pr781.1_v1_sec_untrimmed.txt (SW)</p>	<p>Tbp_pr781.1_v2_sec_untrimmed.txt (SW)  [Z-Beta = 304.07]  [Pearson Full 0.865480077, Pearson Zeroed 0.49543]</p>	<p>Tbp_pr781.1_combined_sec_untrimmed.txt (SW)</p>
<p>Tbp_pr781.1_v1_prim_trimmed.txt (RM8)  [Z-Beta = 105.878]  [Pearson Full 0.824298794, Pearson Zeroed 0.817525]</p>	<p>Tbp_pr781.1_v2_prim_trimmed.txt (RM8)  [Z-Beta = 121.19]  [Pearson Full 0.865480077, Pearson Zeroed 0.850436]</p>	<p>Tbp_pr781.1_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Tbp_pr781.1_v1_sec_trimmed.txt (RM8)  [Z-Beta = 12.5627]  [Pearson Full 0.824298794, Pearson Zeroed 0.823445]</p>	<p>Tbp_pr781.1_v2_sec_trimmed.txt (RM8)</p>	<p>Tbp_pr781.1_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Tbp_pr781.1_v1_prim_untrimmed.txt (RM8)</p>	<p>Tbp_pr781.1_v2_prim_untrimmed.txt (RM8)  [Z-Beta = -141.032]  [Pearson Full 0.865480077, Pearson Zeroed 0.863559]</p>	<p>Tbp_pr781.1_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>




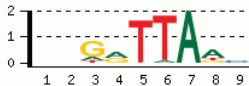
	<p>0.830337]</p> 	
<p>Tbp_pr781.1_v1_sec_untrimmed.txt (RM8)</p> 	<p>Tbp_pr781.1_v2_sec_untrimmed.txt (RM8) [Z-Beta = 304.07] [Pearson Full 0.865480077, Pearson Zeroed 0.49543]</p> 	<p>Tbp_pr781.1_RM8_combined_sec_untrimmed.txt (RM8) No PWM Available</p>
<p>Tbp_pr781.1_v1_RM35_prim_trimmed.txt (RM35) [Z-Beta = 73.669] [Pearson Full 0.824298794, Pearson Zeroed 0.821136]</p> 	<p>Tbp_pr781.1_v2_RM35_prim_trimmed.txt (RM35) [Z-Beta = -27.5623] [Pearson Full 0.865480077, Pearson Zeroed 0.863787]</p> 	<p>Tbp_pr781.1_RM_combined_prim_trimmed.txt (RM35) [Z-Beta = 84.5896] [Pearson Full 0.801240403, Pearson Zeroed 0.805272]</p> 
<p>Tbp_pr781.1_v1_RM35_sec_trimmed.txt (RM35)</p> 	<p>Tbp_pr781.1_v2_RM35_sec_trimmed.txt (RM35) [Z-Beta = 23.4365] [Pearson Full 0.865480077, Pearson Zeroed 0.862977]</p> 	<p>Tbp_pr781.1_RM_combined_sec_trimmed.txt (RM35) [Z-Beta = 16.421] [Pearson Full 0.801240403, Pearson Zeroed 0.796246]</p> 
<p>Tbp_pr781.1_v1_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Tbp_pr781.1_v2_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Tbp_pr781.1_RM_combined_prim_untrimmed.txt (RM35) [Z-Beta = 226.933] [Pearson Full 0.801240403, Pearson Zeroed 0.793028]</p> 
<p>Tbp_pr781.1_v1_RM35_sec_untrimmed.txt (RM35) [Z-Beta = 12.9933] [Pearson Full 0.824298794, Pearson Zeroed 0.820515]</p> 	<p>Tbp_pr781.1_v2_RM35_sec_untrimmed.txt (RM35) [Z-Beta = 33.2771] [Pearson Full 0.865480077, Pearson Zeroed 0.860619]</p> 	<p>Tbp_pr781.1_RM_combined_sec_untrimmed.txt (RM35) [Z-Beta = 67.9859] [Pearson Full 0.801240403, Pearson Zeroed 0.769446]</p> 
<p>Tbp_pr781.1_PWM_1.txt (K-afal) [Z-Beta = 275.778] [Pearson Full 0.824298794, Pearson Zeroed 0.822677]</p> 	<p>Tbp_pr781.1_PWM_1.txt (K-afal) [Z-Beta = 55.9745] [Pearson Full 0.865480077, Pearson Zeroed 0.859934]</p> 	<p>Tbp_pr781.1_combined_PWM_1.txt (K-afal) [Z-Beta = 11.2409] [Pearson Full 0.801240403, Pearson Zeroed 0.799598]</p> 
<p>Tbp_pr781.1_PWM_2.txt (Kafal) No PWM Available</p>	<p>Tbp_pr781.1_PWM_2.txt (Kafal) [Z-Beta = 42.9822] [Pearson Full 0.865480077, Pearson Zeroed 0.861446]</p> 	<p>Tbp_pr781.1_combined_PWM_2.txt (Kafal)</p> 
<p>Tbp_pr781.1_PWM_3.txt (Kafal) No PWM Available</p>	<p>Tbp_pr781.1_PWM_3.txt (Kafal) [Z-Beta = 12.5624] [Pearson Full 0.865480077, Pearson Zeroed 0.865097]</p> 	<p>Tbp_pr781.1_combined_PWM_3.txt (Kafal)</p> 

<b>Tbp_pr781.1_PWM_4.txt (Kafal)</b> No PWM Available	<b>Tbp_pr781.1_PWM_4.txt (Kafal)</b> <b>[Z-Beta = 10.2865]</b> <b>[Pearson Full 0.865480077, Pearson Zeroed 0.865247]</b>	<b>Tbp_pr781.1_combined_PWM_4.txt (Kafal)</b> <b>[Z-Beta = 18.428]</b> <b>[Pearson Full 0.801240403, Pearson Zeroed 0.797852]</b>
<b>Tbp_pr781.1_PWM_5.txt (Kafal)</b> No PWM Available	<b>Tbp_pr781.1_PWM_5.txt (Kafal)</b> <b>[Z-Beta = 30.9621]</b> <b>[Pearson Full 0.865480077, Pearson Zeroed 0.863553]</b>	<b>Tbp_pr781.1_combined_PWM_5.txt (Kafal)</b>
<b>Tbp_pr781.1_PWM_6.txt (Kafal)</b> No PWM Available	<b>Tbp_pr781.1_PWM_6.txt (Kafal)</b>	<b>Tbp_pr781.1_combined_PWM_6.txt (Kafal)</b> <b>[Z-Beta = 16.4639]</b> <b>[Pearson Full 0.801240403, Pearson Zeroed 0.798004]</b>

# Tcf1\_2666.2

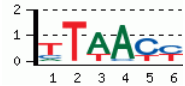


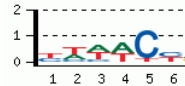






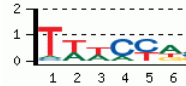
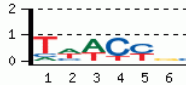


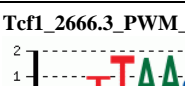
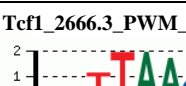
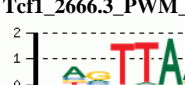

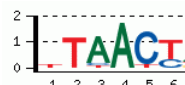
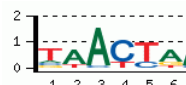
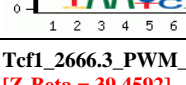
Array Version 1	Array Version 2	Combined
<p>Tcf1_2666.2_v1_prim_auc.txt (SW)</p>	<p>Tcf1_2666.2_v2_prim_auc.txt (SW)  [Z-Beta = 20.8821]  [Pearson Full 0.751480576, Pearson Zeroed 0.751632]</p>	<p>Tcf1_2666.2_combined_prim_auc.txt (SW)  No PWM Available</p>
<p>Tcf1_2666.2_v1_prim_ic.txt (SW)</p>	<p>Tcf1_2666.2_v2_prim_ic.txt (SW)  [Z-Beta = 2.37137]  [Pearson Full 0.751480576, Pearson Zeroed 0.751283]</p>	<p>Tcf1_2666.2_combined_prim_ic_trimmed.txt (SW)  [Z-Beta = 38.0326]  [Pearson Full 0.788911384, Pearson Zeroed 0.776903]</p>
<p>Tcf1_2666.2_v1_prim_untrimmed.txt (SW)</p>	<p>Tcf1_2666.2_v2_prim_untrimmed.txt (SW)</p>	<p>Tcf1_2666.2_combined_prim_untrimmed.txt (SW)</p>
<p>Tcf1_2666.2_v1_sec_auc.txt (SW)  [Z-Beta = 6.37551]  [Pearson Full 0.774159891, Pearson Zeroed 0.771114]</p>	<p>Tcf1_2666.2_v2_sec_auc.txt (SW)</p>	<p>Tcf1_2666.2_combined_sec_auc.txt (SW)</p>
<p>Tcf1_2666.2_v1_sec_ic.txt (SW)</p>	<p>Tcf1_2666.2_v2_sec_ic.txt (SW)</p>	<p>Tcf1_2666.2_combined_sec_ic_trimmed.txt (SW)</p>
<p>Tcf1_2666.2_v1_sec_untrimmed.txt (SW)</p>	<p>Tcf1_2666.2_v2_sec_untrimmed.txt (SW)  [Z-Beta = 13.1787]  [Pearson Full 0.751480576, Pearson Zeroed 0.750628]</p>	<p>Tcf1_2666.2_combined_sec_untrimmed.txt (SW)</p>
<p>Tcf1_2666.2_v1_prim_trimmed.txt (RM8)</p>	<p>Tcf1_2666.2_v2_prim_trimmed.txt (RM8)</p>	<p>Tcf1_2666.2_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Tcf1_2666.2_v1_sec_trimmed.txt (RM8)</p>	<p>Tcf1_2666.2_v2_sec_trimmed.txt (RM8)</p>	<p>Tcf1_2666.2_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Tcf1_2666.2_v1_prim_untrimmed.txt (RM8)</p>	<p>Tcf1_2666.2_v2_prim_untrimmed.txt (RM8)</p>	<p>Tcf1_2666.2_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Tcf1_2666.2_v1_sec_untrimmed.txt (RM8)</p>	<p>Tcf1_2666.2_v2_sec_untrimmed.txt (RM8)  [Z-Beta = 13.1787]  [Pearson Full 0.751480576, Pearson Zeroed 0.750628]</p>	<p>Tcf1_2666.2_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>

<p>Tcf1_2666.2_v1_RM35_prim_trimmed.txt (RM35)  [Z-Beta = 25.8348]  [Pearson Full 0.774159891, Pearson Zeroed 0.772468]</p>	<p>Tcf1_2666.2_v2_RM35_prim_trimmed.txt (RM35)  [Z-Beta = 168.357]  [Pearson Full 0.751480576, Pearson Zeroed 0.727539]</p>	<p>Tcf1_2666.2_RM_combined_prim_trimmed.txt (RM35)  [Z-Beta = 84.6058]  [Pearson Full 0.788911384, Pearson Zeroed 0.78344]</p>
<p>Tcf1_2666.2_v1_RM35_sec_trimmed.txt (RM35)  [Z-Beta = 137.738]  [Pearson Full 0.774159891, Pearson Zeroed 0.752995]</p>	<p>Tcf1_2666.2_v2_RM35_sec_trimmed.txt (RM35)  [Z-Beta = 148.484]  [Pearson Full 0.751480576, Pearson Zeroed 0.716087]</p>	<p>Tcf1_2666.2_RM_combined_sec_trimmed.txt (RM35)  [Z-Beta = 172.855]  [Pearson Full 0.788911384, Pearson Zeroed 0.763879]</p>
<p>Tcf1_2666.2_v1_RM35_prim_untrimmed.txt (RM35)  [Z-Beta = 36.2471]  [Pearson Full 0.774159891, Pearson Zeroed 0.768634]</p>	<p>Tcf1_2666.2_v2_RM35_prim_untrimmed.txt (RM35)  </p>	<p>Tcf1_2666.2_RM_combined_prim_untrimmed.txt (RM35)  [Z-Beta = 45.8348]  [Pearson Full 0.788911384, Pearson Zeroed 0.787728]</p>
<p>Tcf1_2666.2_v1_RM35_sec_untrimmed.txt (RM35)  </p>	<p>Tcf1_2666.2_v2_RM35_sec_untrimmed.txt (RM35)  [Z-Beta = 39.0667]  [Pearson Full 0.751480576, Pearson Zeroed 0.741735]</p>	<p>Tcf1_2666.2_RM_combined_sec_untrimmed.txt (RM35)  [Z-Beta = 111.512]  [Pearson Full 0.788911384, Pearson Zeroed 0.769643]</p>
<p>Tcf1_2666.2_PWM_1.txt (K-afal)  [Z-Beta = 21.5157]  [Pearson Full 0.774159891, Pearson Zeroed 0.773196]</p>	<p>Tcf1_2666.2_PWM_1.txt (K-afal)  [Z-Beta = 31.5679]  [Pearson Full 0.751480576, Pearson Zeroed 0.746593]</p>	<p>Tcf1_2666.2_combined_PWM_1.txt (K-afal)  [Z-Beta = 19.4299]  [Pearson Full 0.788911384, Pearson Zeroed 0.787673]</p>
<p>Tcf1_2666.2_PWM_2.txt (K-afal)  </p>	<p>Tcf1_2666.2_PWM_2.txt (K-afal)  [Z-Beta = 2.6938]  [Pearson Full 0.751480576, Pearson Zeroed 0.751349]</p>	<p>Tcf1_2666.2_combined_PWM_2.txt (K-afal)  No PWM Available</p>
<p>Tcf1_2666.2_PWM_3.txt (K-afal)  [Z-Beta = 174.812]  [Pearson Full 0.774159891, Pearson Zeroed 0.681342]</p>	<p>Tcf1_2666.2_PWM_3.txt (K-afal)  [Z-Beta = 17.2089]  [Pearson Full 0.751480576, Pearson Zeroed 0.751457]</p>	<p>Tcf1_2666.2_combined_PWM_3.txt (K-afal)  No PWM Available</p>
<p>Tcf1_2666.2_PWM_4.txt (K-afal)  </p>	<p>Tcf1_2666.2_PWM_4.txt (K-afal)  [Z-Beta = 4.89802]  [Pearson Full 0.751480576, Pearson Zeroed 0.751667]</p>	<p>Tcf1_2666.2_combined_PWM_4.txt (K-afal)  No PWM Available</p>

	 <p>Sequence logo showing peaks for positions 1-8. The sequence TAAc is highlighted. Position 1 has a peak of ~1.8, position 2 has ~1.5, position 3 has ~1.5, position 4 has ~1.5, position 5 has ~1.0, position 6 has ~0.5, position 7 has ~0.5, and position 8 has ~0.5.</p>	
<p><b>Tcf1_2666.2_PWM_5.txt (K-afal)</b>  <b>[Z-Beta = 119.06]</b>  <b>[Pearson Full 0.774159891, Pearson Zeroed 0.725584]</b></p>  <p>Sequence logo showing peaks for positions 1-9. The sequence TTA is highlighted. Position 1 has a peak of ~1.8, position 2 has ~0.5, position 3 has ~0.5, position 4 has ~0.5, position 5 has ~1.5, position 6 has ~1.5, position 7 has ~1.5, position 8 has ~0.5, and position 9 has ~0.5.</p>	<p><b>Tcf1_2666.2_PWM_5.txt (K-afal)</b>  <b>No PWM Available</b></p>	<p><b>Tcf1_2666.2_combined_PWM_5.txt (K-afal)</b>  <b>No PWM Available</b></p>

# Tcf1\_2666.3

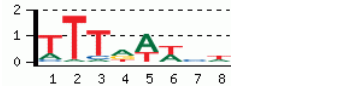
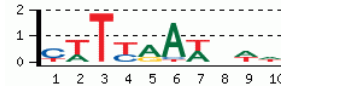
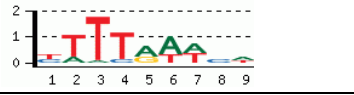
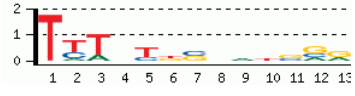

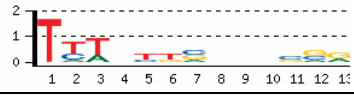

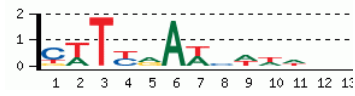
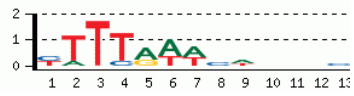
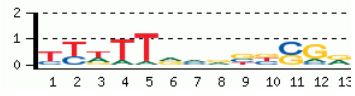
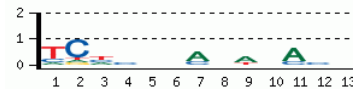
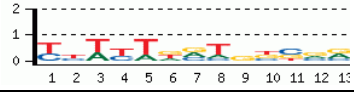



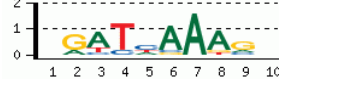





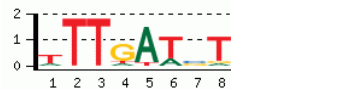


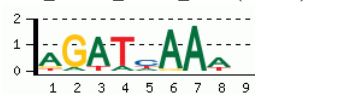
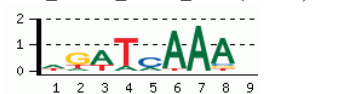

Array Version 1	Array Version 2	Combined
<p>Tcf1_2666.3_v1_prim_auc.txt (SW)  [Z-Beta = 5.66921]  [Pearson Full 0.484263961, Pearson Zeroed 0.483844]</p>	<p>Tcf1_2666.3_v2_prim_auc.txt (SW)</p>	<p>Tcf1_2666.3_combined_prim_auc.txt (SW)</p>
<p>Tcf1_2666.3_v1_prim_ic.txt (SW)</p>	<p>Tcf1_2666.3_v2_prim_ic.txt (SW)</p>	<p>Tcf1_2666.3_combined_prim_ic_trimmed.txt (SW)</p>
<p>Tcf1_2666.3_v1_prim_untrimmed.txt (SW)</p>	<p>Tcf1_2666.3_v2_prim_untrimmed.txt (SW)</p>	<p>Tcf1_2666.3_combined_prim_untrimmed.txt (SW)</p>
<p>Tcf1_2666.3_v1_sec_auc.txt (SW)</p>	<p>Tcf1_2666.3_v2_sec_auc.txt (SW)</p>	<p>Tcf1_2666.3_combined_sec_auc.txt (SW)</p>
<p>Tcf1_2666.3_v1_sec_ic.txt (SW)</p>	<p>Tcf1_2666.3_v2_sec_ic.txt (SW)</p>	<p>Tcf1_2666.3_combined_sec_ic_trimmed.txt (SW)  [Z-Beta = 4.96514]  [Pearson Full 0.596128682, Pearson Zeroed 0.586584]</p>
<p>Tcf1_2666.3_v1_sec_untrimmed.txt (SW)</p>	<p>Tcf1_2666.3_v2_sec_untrimmed.txt (SW)</p>	<p>Tcf1_2666.3_combined_sec_untrimmed.txt (SW)</p>
<p>Tcf1_2666.3_v1_prim_trimmed.txt (RM8)</p>	<p>Tcf1_2666.3_v2_prim_trimmed.txt (RM8)</p>	<p>Tcf1_2666.3_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Tcf1_2666.3_v1_sec_trimmed.txt (RM8)  [Z-Beta = 23.9136]  [Pearson Full 0.484263961, Pearson Zeroed 0.46882]</p>	<p>Tcf1_2666.3_v2_sec_trimmed.txt (RM8)  [Z-Beta = 45.2594]  [Pearson Full 0.602347302, Pearson Zeroed 0.570569]</p>	<p>Tcf1_2666.3_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Tcf1_2666.3_v1_prim_untrimmed.txt (RM8)</p>	<p>Tcf1_2666.3_v2_prim_untrimmed.txt (RM8)</p>	<p>Tcf1_2666.3_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Tcf1_2666.3_v1_sec_untrimmed.txt (RM8)</p>	<p>Tcf1_2666.3_v2_sec_untrimmed.txt (RM8)</p>	<p>Tcf1_2666.3_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>
<p>Tcf1_2666.3_v1_RM35_prim_trimmed.txt (RM35)  [Z-Beta = 38.6949]  [Pearson Full 0.484263961, Pearson Zeroed 0.483463]</p>	<p>Tcf1_2666.3_v2_RM35_prim_trimmed.txt (RM35)</p>	<p>Tcf1_2666.3_RM_combined_prim_trimmed.txt (RM35)  [Z-Beta = 29.8549]  [Pearson Full 0.596128682, Pearson Zeroed 0.598232]</p>



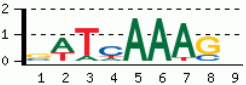
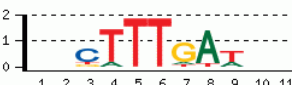

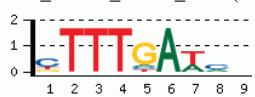
 <p>Tcf1_2666.3_v1_RM35_sec_trimmed.txt (RM35) [Z-Beta = 23.577] [Pearson Full 0.484263961, Pearson Zeroed 0.462001]</p>	 <p>Tcf1_2666.3_v2_RM35_sec_trimmed.txt (RM35) [Z-Beta = 25.5968] [Pearson Full 0.602347302, Pearson Zeroed 0.567155]</p>	 <p>Tcf1_2666.3_RM_combined_sec_trimmed.txt (RM35) [Z-Beta = 21.595] [Pearson Full 0.596128682, Pearson Zeroed 0.565932]</p>
 <p>Tcf1_2666.3_v1_RM35_prim_untrimmed.txt (RM35) [Z-Beta = 46.9165] [Pearson Full 0.484263961, Pearson Zeroed 0.485864]</p>	 <p>Tcf1_2666.3_v2_RM35_prim_untrimmed.txt (RM35)</p>	 <p>Tcf1_2666.3_RM_combined_prim_untrimmed.txt (RM35) [Z-Beta = 86.8074] [Pearson Full 0.596128682, Pearson Zeroed 0.597096]</p>
 <p>Tcf1_2666.3_v1_RM35_sec_untrimmed.txt (RM35)</p>	 <p>Tcf1_2666.3_v2_RM35_sec_untrimmed.txt (RM35)</p>	 <p>Tcf1_2666.3_RM_combined_sec_untrimmed.txt (RM35)</p>
<p>Tcf1_2666.3_PWM_1.txt (K-afal)</p> 	<p>Tcf1_2666.3_PWM_1.txt (K-afal) [Z-Beta = 53.0682] [Pearson Full 0.602347302, Pearson Zeroed 0.556776]</p> 	<p>Tcf1_2666.3_combined_PWM_1.txt (K-afal)</p> 
<p>Tcf1_2666.3_PWM_2.txt (K-afal)</p> 	<p>Tcf1_2666.3_PWM_2.txt (K-afal)</p> 	<p>Tcf1_2666.3_combined_PWM_2.txt (K-afal) No PWM Available</p>
<p>Tcf1_2666.3_PWM_3.txt (K-afal)</p> 	<p>Tcf1_2666.3_PWM_3.txt (K-afal)</p> 	<p>Tcf1_2666.3_combined_PWM_3.txt (K-afal) No PWM Available</p>
<p>Tcf1_2666.3_PWM_4.txt (K-afal)</p> 	<p>Tcf1_2666.3_PWM_4.txt (K-afal)</p> 	<p>Tcf1_2666.3_combined_PWM_4.txt (K-afal) No PWM Available</p>
<p>Tcf1_2666.3_PWM_5.txt (K-afal)</p> 	<p>Tcf1_2666.3_PWM_5.txt (K-afal)</p> 	<p>Tcf1_2666.3_combined_PWM_5.txt (K-afal) No PWM Available</p>
<p>Tcf1_2666.3_PWM_6.txt (Kafal) No PWM Available</p>	<p>Tcf1_2666.3_PWM_6.txt (Kafal) [Z-Beta = 39.4592] [Pearson Full 0.602347302, Pearson Zeroed 0.59389]</p> 	<p>Tcf1_2666.3_combined_PWM_6.txt (Kafal) No PWM Available</p>



# Tcf3\_3787.1

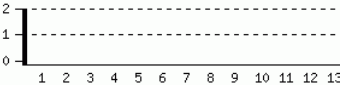
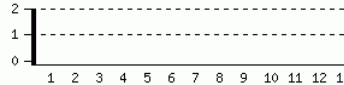
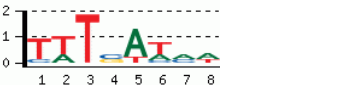
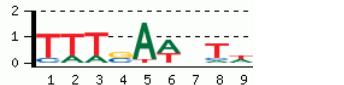

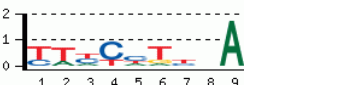
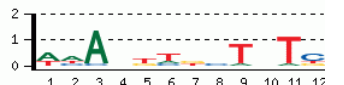



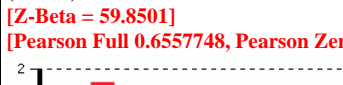
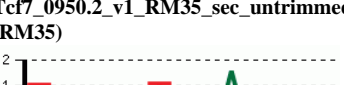

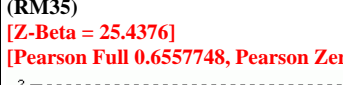
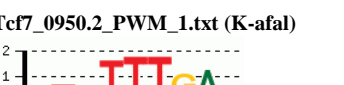


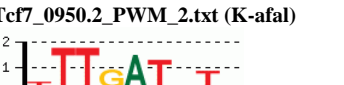
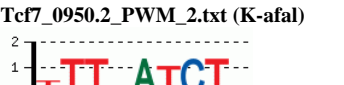


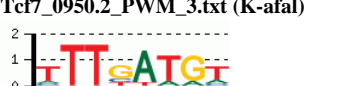


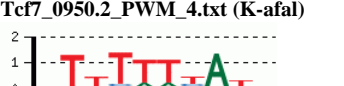

Array Version 1	Array Version 2	Combined
<p>Tcf3_3787.1_v1_prim_auc.txt (SW)</p>	<p>Tcf3_3787.1_v2_prim_auc.txt (SW)</p>	<p>Tcf3_3787.1_combined_prim_auc.txt (SW)</p>
<p>Tcf3_3787.1_v1_prim_ic.txt (SW)</p>	<p>Tcf3_3787.1_v2_prim_ic.txt (SW)</p>	<p>Tcf3_3787.1_combined_prim_ic_trimmed.txt (SW)</p>
<p>Tcf3_3787.1_v1_prim_untrimmed.txt (SW)</p>	<p>Tcf3_3787.1_v2_prim_untrimmed.txt (SW)</p>	<p>Tcf3_3787.1_combined_prim_untrimmed.txt (SW)</p>
<p>Tcf3_3787.1_v1_sec_auc.txt (SW)</p>	<p>Tcf3_3787.1_v2_sec_auc.txt (SW)</p>	<p>Tcf3_3787.1_combined_sec_auc.txt (SW)</p>
<p>Tcf3_3787.1_v1_sec_ic.txt (SW)</p>	<p>Tcf3_3787.1_v2_sec_ic.txt (SW)</p>	<p>Tcf3_3787.1_combined_sec_ic_trimmed.txt (SW)</p>
<p>Tcf3_3787.1_v1_sec_untrimmed.txt (SW)</p>	<p>Tcf3_3787.1_v2_sec_untrimmed.txt (SW)  [Z-Beta = 10.4154]  [Pearson Full 0.663149718, Pearson Zeroed NaN]</p>	<p>Tcf3_3787.1_combined_sec_untrimmed.txt (SW)  [Z-Beta = 25.0037]  [Pearson Full 0.662125759, Pearson Zeroed 0.651732]</p>
<p>Tcf3_3787.1_v1_prim_trimmed.txt (RM8)</p>	<p>Tcf3_3787.1_v2_prim_trimmed.txt (RM8)</p>	<p>Tcf3_3787.1_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Tcf3_3787.1_v1_sec_trimmed.txt (RM8)  [Z-Beta = 95.1948]  [Pearson Full 0.668441878, Pearson Zeroed 0.488647]</p>	<p>Tcf3_3787.1_v2_sec_trimmed.txt (RM8)</p>	<p>Tcf3_3787.1_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Tcf3_3787.1_v1_prim_untrimmed.txt (RM8)  [Z-Beta = 36.3851]  [Pearson Full 0.668441878, Pearson Zeroed 0.645335]</p>	<p>Tcf3_3787.1_v2_prim_untrimmed.txt (RM8)</p>	<p>Tcf3_3787.1_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Tcf3_3787.1_v1_sec_untrimmed.txt (RM8)</p>	<p>Tcf3_3787.1_v2_sec_untrimmed.txt (RM8)  [Z-Beta = 10.4154]  [Pearson Full 0.663149718, Pearson Zeroed NaN]</p>	<p>Tcf3_3787.1_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>


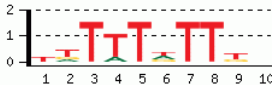




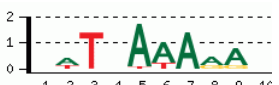




<p>Tcf3_3787.1_v1_RM35_prim_trimmed.txt (RM35)</p> 	<p>Tcf3_3787.1_v2_RM35_prim_trimmed.txt (RM35)</p> 	<p>Tcf3_3787.1_RM_combined_prim_trimmed.txt (RM35)</p> <p>[Z-Beta = 10.9984] [Pearson Full 0.662125759, Pearson Zeroed 0.659701]</p> 
<p>Tcf3_3787.1_v1_RM35_sec_trimmed.txt (RM35)</p> 	<p>Tcf3_3787.1_v2_RM35_sec_trimmed.txt (RM35)</p> 	<p>Tcf3_3787.1_RM_combined_sec_trimmed.txt (RM35)</p> <p>[Z-Beta = 8.4367] [Pearson Full 0.662125759, Pearson Zeroed 0.658697]</p> 
<p>Tcf3_3787.1_v1_RM35_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = 1.99352] [Pearson Full 0.668441878, Pearson Zeroed 0.6672]</p> 	<p>Tcf3_3787.1_v2_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Tcf3_3787.1_RM_combined_prim_untrimmed.txt (RM35)</p> 
<p>Tcf3_3787.1_v1_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Tcf3_3787.1_v2_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Tcf3_3787.1_RM_combined_sec_untrimmed.txt (RM35)</p> <p>[Z-Beta = 0.985854] [Pearson Full 0.662125759, Pearson Zeroed 0.661845]</p> 
<p>Tcf3_3787.1_PWM_1.txt (K-afal)</p> 	<p>Tcf3_3787.1_PWM_1.txt (K-afal)</p> 	<p>Tcf3_3787.1_combined_PWM_1.txt (K-afal)</p> <p>[Z-Beta = 4.13568] [Pearson Full 0.662125759, Pearson Zeroed 0.66121]</p> 
<p>Tcf3_3787.1_PWM_2.txt (K-afal)</p> 	<p>Tcf3_3787.1_PWM_2.txt (K-afal)</p> 	<p>Tcf3_3787.1_combined_PWM_2.txt (K-afal)</p> <p>[Z-Beta = 20.659] [Pearson Full 0.662125759, Pearson Zeroed 0.648697]</p> 
<p>Tcf3_3787.1_PWM_3.txt (K-afal)</p> 	<p>Tcf3_3787.1_PWM_3.txt (K-afal)</p> 	<p>Tcf3_3787.1_combined_PWM_3.txt (K-afal)</p> <p>[Z-Beta = 6.48289] [Pearson Full 0.662125759, Pearson Zeroed 0.661178]</p> 
<p>Tcf3_3787.1_PWM_4.txt (K-afal)</p> <p>[Z-Beta = 5.46145] [Pearson Full 0.668441878, Pearson Zeroed 0.661886]</p> 	<p>Tcf3_3787.1_PWM_4.txt (K-afal)</p> 	<p>Tcf3_3787.1_combined_PWM_4.txt (K-afal)</p> <p>[Z-Beta = 12.9598] [Pearson Full 0.662125759, Pearson Zeroed 0.655526]</p> 
<p>Tcf3_3787.1_PWM_5.txt (K-afal)</p> 	<p>Tcf3_3787.1_PWM_5.txt (K-afal)</p> 	<p>Tcf3_3787.1_combined_PWM_5.txt (K-afal)</p> 

<p>Tcf3_3787.1_PWM_6.txt (K-afal)  [Z-Beta = 5.81941]  [Pearson Full 0.668441878, Pearson Zeroed 0.66212]</p> 	<p>Tcf3_3787.1_PWM_6.txt (K-afal)</p> 	<p>Tcf3_3787.1_combined_PWM_6.txt (K-afal)  [Z-Beta = 30.3866]  [Pearson Full 0.662125759, Pearson Zeroed 0.631304]</p> 
<p>Tcf3_3787.1_PWM_7.txt (K-afal)  [Z-Beta = 8.82812]  [Pearson Full 0.668441878, Pearson Zeroed 0.655402]</p> 	<p>Tcf3_3787.1_PWM_7.txt (K-afal)</p> 	<p>Tcf3_3787.1_combined_PWM_7.txt (K-afal)  No PWM Available</p>
<p>Tcf3_3787.1_PWM_8.txt (Kafal)  No PWM Available</p>	<p>Tcf3_3787.1_PWM_8.txt (Kafal)</p> 	<p>Tcf3_3787.1_combined_PWM_8.txt (Kafal)  No PWM Available</p>

# Tcf7\_0950.2

Array Version 1	Array Version 2	Combined
<p>Tcf7_0950.2_v1_prim_auc.txt (SW)</p>	<p>Tcf7_0950.2_v2_prim_auc.txt (SW)</p>	<p>Tcf7_0950.2_combined_prim_auc.txt (SW)</p>
<p>Tcf7_0950.2_v1_prim_ic.txt (SW)</p>	<p>Tcf7_0950.2_v2_prim_ic.txt (SW)</p>	<p>Tcf7_0950.2_combined_prim_ic_trimmed.txt (SW)</p> <p>[Z-Beta = -79.3491] [Pearson Full 0.6557748, Pearson Zeroed 0.590278]</p>
<p>Tcf7_0950.2_v1_prim_untrimmed.txt (SW)</p>	<p>Tcf7_0950.2_v2_prim_untrimmed.txt (SW)</p> <p>[Z-Beta = 14.4584] [Pearson Full 0.700518481, Pearson Zeroed 0.691468]</p>	<p>Tcf7_0950.2_combined_prim_untrimmed.txt (SW)</p> <p>[Z-Beta = -30.4876] [Pearson Full 0.6557748, Pearson Zeroed 0.643787]</p>
<p>Tcf7_0950.2_v1_sec_auc.txt (SW)</p>	<p>Tcf7_0950.2_v2_sec_auc.txt (SW)</p>	<p>Tcf7_0950.2_combined_sec_auc.txt (SW)</p>
<p>Tcf7_0950.2_v1_sec_ic.txt (SW)</p>	<p>Tcf7_0950.2_v2_sec_ic.txt (SW)</p>	<p>Tcf7_0950.2_combined_sec_ic_trimmed.txt (SW)</p> <p>[Z-Beta = 21.6456] [Pearson Full 0.6557748, Pearson Zeroed 0.651164]</p>
<p>Tcf7_0950.2_v1_sec_untrimmed.txt (SW)</p>	<p>Tcf7_0950.2_v2_sec_untrimmed.txt (SW)</p> <p>[Z-Beta = 143.65] [Pearson Full 0.700518481, Pearson Zeroed 0.458759]</p>	<p>Tcf7_0950.2_combined_sec_untrimmed.txt (SW)</p> <p>[Z-Beta = 12.1994] [Pearson Full 0.6557748, Pearson Zeroed 0.654261]</p>
<p>Tcf7_0950.2_v1_prim_trimmed.txt (RM8)</p>	<p>Tcf7_0950.2_v2_prim_trimmed.txt (RM8)</p>	<p>Tcf7_0950.2_RM8_combined_prim_trimmed.txt (RM8)</p> <p>No PWM Available</p>
<p>Tcf7_0950.2_v1_sec_trimmed.txt (RM8)</p> <p>[Z-Beta = 55.9671] [Pearson Full 0.638275264, Pearson Zeroed 0.634316]</p>	<p>Tcf7_0950.2_v2_sec_trimmed.txt (RM8)</p>	<p>Tcf7_0950.2_RM8_combined_sec_trimmed.txt (RM8)</p> <p>No PWM Available</p>
<p>Tcf7_0950.2_v1_prim_untrimmed.txt (RM8)</p> <p>[Z-Beta = 8.12092] [Pearson Full 0.638275264, Pearson Zeroed 0.635217]</p>	<p>Tcf7_0950.2_v2_prim_untrimmed.txt (RM8)</p> <p>[Z-Beta = 14.4584] [Pearson Full 0.700518481, Pearson Zeroed 0.691468]</p>	<p>Tcf7_0950.2_RM8_combined_prim_untrimmed.txt (RM8)</p> <p>No PWM Available</p>

<p>Tcf7_0950.2_v1_sec_untrimmed.txt (RM8)  [Z-Beta = 68.179]  [Pearson Full 0.638275264, Pearson Zeroed 0.605735]</p> 	<p>Tcf7_0950.2_v2_sec_untrimmed.txt (RM8)  [Z-Beta = 143.65]  [Pearson Full 0.700518481, Pearson Zeroed 0.458759]</p> 	<p>Tcf7_0950.2_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>
<p>Tcf7_0950.2_v1_RM35_prim_trimmed.txt (RM35)</p> 	<p>Tcf7_0950.2_v2_RM35_prim_trimmed.txt (RM35)</p> 	<p>Tcf7_0950.2_RM_combined_prim_trimmed.txt (RM35)  [Z-Beta = -8.95605]  [Pearson Full 0.6557748, Pearson Zeroed 0.655213]</p> 
<p>Tcf7_0950.2_v1_RM35_sec_trimmed.txt (RM35)</p> 	<p>Tcf7_0950.2_v2_RM35_sec_trimmed.txt (RM35)</p> 	<p>Tcf7_0950.2_RM_combined_sec_trimmed.txt (RM35)  [Z-Beta = 42.8603]  [Pearson Full 0.6557748, Pearson Zeroed 0.624868]</p> 
<p>Tcf7_0950.2_v1_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Tcf7_0950.2_v2_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Tcf7_0950.2_RM_combined_prim_untrimmed.txt (RM35)  [Z-Beta = 59.8501]  [Pearson Full 0.6557748, Pearson Zeroed 0.61239]</p> 
<p>Tcf7_0950.2_v1_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Tcf7_0950.2_v2_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Tcf7_0950.2_RM_combined_sec_untrimmed.txt (RM35)  [Z-Beta = 25.4376]  [Pearson Full 0.6557748, Pearson Zeroed 0.643791]</p> 
<p>Tcf7_0950.2_PWM_1.txt (K-afal)</p> 	<p>Tcf7_0950.2_PWM_1.txt (K-afal)</p> 	<p>Tcf7_0950.2_combined_PWM_1.txt (K-afal)  [Z-Beta = -6.24293]  [Pearson Full 0.6557748, Pearson Zeroed 0.655279]</p> 
<p>Tcf7_0950.2_PWM_2.txt (K-afal)</p> 	<p>Tcf7_0950.2_PWM_2.txt (K-afal)</p> 	<p>Tcf7_0950.2_combined_PWM_2.txt (K-afal)  [Z-Beta = -0.968708]  [Pearson Full 0.6557748, Pearson Zeroed 0.655761]</p> 
<p>Tcf7_0950.2_PWM_3.txt (K-afal)</p> 	<p>Tcf7_0950.2_PWM_3.txt (K-afal)</p> 	<p>Tcf7_0950.2_combined_PWM_3.txt (K-afal)  [Z-Beta = 10.4078]  [Pearson Full 0.6557748, Pearson Zeroed 0.653936]</p> 
<p>Tcf7_0950.2_PWM_4.txt (K-afal)  [Z-Beta = 0.639095]  [Pearson Full 0.638275264, Pearson Zeroed 0.637217]</p> 	<p>Tcf7_0950.2_PWM_4.txt (K-afal)</p> 	<p>Tcf7_0950.2_combined_PWM_4.txt (K-afal)  [Z-Beta = -18.8997]  [Pearson Full 0.6557748, Pearson Zeroed 0.650625]</p> 


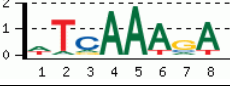




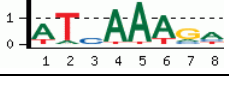




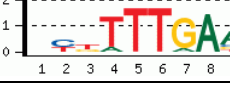
<p>Tcf7_0950.2_PWM_5.txt (K-afal)  [Z-Beta = 3.8958]  [Pearson Full 0.638275264, Pearson Zeroed 0.632286]</p> 	<p>Tcf7_0950.2_PWM_5.txt (K-afal)</p> 	<p>Tcf7_0950.2_combined_PWM_5.txt (K-afal)  [Z-Beta = 25.8769]  [Pearson Full 0.6557748, Pearson Zeroed 0.645]</p> 
<p>Tcf7_0950.2_PWM_6.txt (K-afal)  [Z-Beta = 2.79174]  [Pearson Full 0.638275264, Pearson Zeroed 0.634488]</p> 	<p>Tcf7_0950.2_PWM_6.txt (K-afal)</p> 	<p>Tcf7_0950.2_combined_PWM_6.txt (K-afal)  [Z-Beta = -28.7415]  [Pearson Full 0.6557748, Pearson Zeroed 0.646314]</p> 
<p>Tcf7_0950.2_PWM_7.txt (Kafal)  No PWM Available</p>	<p>Tcf7_0950.2_PWM_7.txt (Kafal)</p> 	<p>Tcf7_0950.2_combined_PWM_7.txt (Kafal)  [Z-Beta = 53.685]  [Pearson Full 0.6557748, Pearson Zeroed 0.60401]</p> 
<p>Tcf7_0950.2_PWM_8.txt (Kafal)  No PWM Available</p>	<p>Tcf7_0950.2_PWM_8.txt (Kafal)</p> 	<p>Tcf7_0950.2_combined_PWM_8.txt (Kafal)  [Z-Beta = 27.5125]  [Pearson Full 0.6557748, Pearson Zeroed 0.642508]</p> 
<p>Tcf7_0950.2_PWM_9.txt (Kafal)  No PWM Available</p>	<p>Tcf7_0950.2_PWM_9.txt (Kafal)  [Z-Beta = 1.6892]  [Pearson Full 0.700518481, Pearson Zeroed 0.697539]</p> 	<p>Tcf7_0950.2_combined_PWM_9.txt (Kafal)  No PWM Available</p>







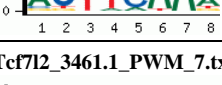



# Tcf7l2\_3461.1

Array Version 1	Array Version 2	Combined
<p>Tcf7l2_3461.1_v1_prim_auc.txt (SW)  [Z-Beta = 54.6258]  [Pearson Full 0.738380123, Pearson Zeroed 0.740981]</p>	<p>Tcf7l2_3461.1_v2_prim_auc.txt (SW)</p>	<p>Tcf7l2_3461.1_combined_prim_auc.txt (SW)</p>
<p>Tcf7l2_3461.1_v1_prim_ic.txt (SW)</p>	<p>Tcf7l2_3461.1_v2_prim_ic.txt (SW)</p>	<p>Tcf7l2_3461.1_combined_prim_ic_trimmed.txt (SW)</p>
<p>Tcf7l2_3461.1_v1_prim_untrimmed.txt (SW)</p>	<p>Tcf7l2_3461.1_v2_prim_untrimmed.txt (SW)  [Z-Beta = 8.59506]  [Pearson Full 0.764550621, Pearson Zeroed 0.763603]</p>	<p>Tcf7l2_3461.1_combined_prim_untrimmed.txt (SW)</p>
<p>Tcf7l2_3461.1_v1_sec_auc.txt (SW)</p>	<p>Tcf7l2_3461.1_v2_sec_auc.txt (SW)</p>	<p>Tcf7l2_3461.1_combined_sec_auc.txt (SW)</p>
<p>Tcf7l2_3461.1_v1_sec_ic.txt (SW)</p>	<p>Tcf7l2_3461.1_v2_sec_ic.txt (SW)  [Z-Beta = 6.33721]  [Pearson Full 0.764550621, Pearson Zeroed 0.760767]</p>	<p>Tcf7l2_3461.1_combined_sec_ic_trimmed.txt (SW)  [Z-Beta = 3.98569]  [Pearson Full 0.605508143, Pearson Zeroed 0.601228]</p>
<p>Tcf7l2_3461.1_v1_sec_untrimmed.txt (SW)  [Z-Beta = 2.61502]  [Pearson Full 0.738380123, Pearson Zeroed 0.737252]</p>	<p>Tcf7l2_3461.1_v2_sec_untrimmed.txt (SW)  [Z-Beta = 125.017]  [Pearson Full 0.764550621, Pearson Zeroed 0.710108]</p>	<p>Tcf7l2_3461.1_combined_sec_untrimmed.txt (SW)</p>
<p>Tcf7l2_3461.1_v1_prim_trimmed.txt (RM8)</p>	<p>Tcf7l2_3461.1_v2_prim_trimmed.txt (RM8)</p>	<p>Tcf7l2_3461.1_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Tcf7l2_3461.1_v1_sec_trimmed.txt (RM8)</p>	<p>Tcf7l2_3461.1_v2_sec_trimmed.txt (RM8)  [Z-Beta = 16.5468]  [Pearson Full 0.764550621, Pearson Zeroed 0.765331]</p>	<p>Tcf7l2_3461.1_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Tcf7l2_3461.1_v1_prim_untrimmed.txt (RM8)</p>	<p>Tcf7l2_3461.1_v2_prim_untrimmed.txt (RM8)  [Z-Beta = 8.59506]  [Pearson Full 0.764550621, Pearson Zeroed 0.763603]</p>	<p>Tcf7l2_3461.1_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>



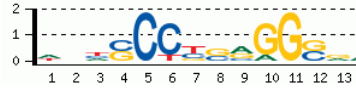




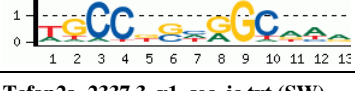
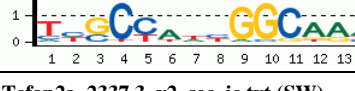
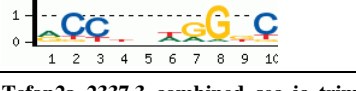
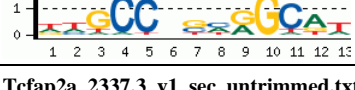

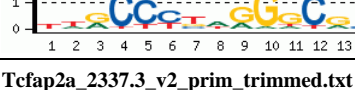
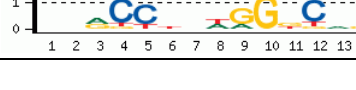
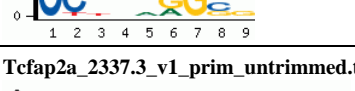
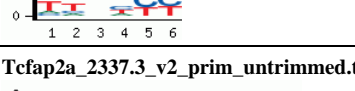
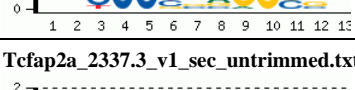
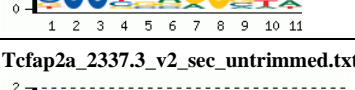
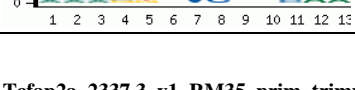
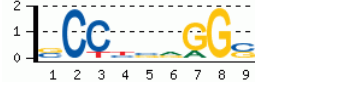

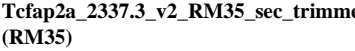
<p>Tcf712_3461.1_v1_sec_untrimmed.txt (RM8)</p>	<p>Tcf712_3461.1_v2_sec_untrimmed.txt (RM8)</p> <p>[Z-Beta = 125.017] [Pearson Full 0.764550621, Pearson Zeroed 0.710108]</p>	<p>Tcf712_3461.1_RM8_combined_sec_untrimmed.txt (RM8)</p> <p>No PWM Available</p>
<p>Tcf712_3461.1_v1_RM35_prim_trimmed.txt (RM35)</p>	<p>Tcf712_3461.1_v2_RM35_prim_trimmed.txt (RM35)</p>	<p>Tcf712_3461.1_RM_combined_prim_trimmed.txt (RM35)</p>
<p>Tcf712_3461.1_v1_RM35_sec_trimmed.txt (RM35)</p> <p>[Z-Beta = 26.3696] [Pearson Full 0.738380123, Pearson Zeroed 0.731867]</p>	<p>Tcf712_3461.1_v2_RM35_sec_trimmed.txt (RM35)</p> <p>[Z-Beta = 79.5969] [Pearson Full 0.764550621, Pearson Zeroed 0.749299]</p>	<p>Tcf712_3461.1_RM_combined_sec_trimmed.txt (RM35)</p> <p>[Z-Beta = 0.638397] [Pearson Full 0.605508143, Pearson Zeroed 0.605285]</p>
<p>Tcf712_3461.1_v1_RM35_prim_untrimmed.txt (RM35)</p>	<p>Tcf712_3461.1_v2_RM35_prim_untrimmed.txt (RM35)</p>	<p>Tcf712_3461.1_RM_combined_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = 27.7366] [Pearson Full 0.605508143, Pearson Zeroed 0.581628]</p>
<p>Tcf712_3461.1_v1_RM35_sec_untrimmed.txt (RM35)</p>	<p>Tcf712_3461.1_v2_RM35_sec_untrimmed.txt (RM35)</p>	<p>Tcf712_3461.1_RM_combined_sec_untrimmed.txt (RM35)</p>
<p>Tcf712_3461.1_PWM_10.txt (K-afal)</p>	<p>Tcf712_3461.1_PWM_10.txt (K-afal)</p>	<p>Tcf712_3461.1_combined_PWM_10.txt (K-afal)</p> <p>No PWM Available</p>
<p>Tcf712_3461.1_PWM_11.txt (K-afal)</p>	<p>Tcf712_3461.1_PWM_11.txt (K-afal)</p> <p>No PWM Available</p>	<p>Tcf712_3461.1_combined_PWM_11.txt (K-afal)</p> <p>No PWM Available</p>
<p>Tcf712_3461.1_PWM_12.txt (K-afal)</p> <p>[Z-Beta = 14.2508] [Pearson Full 0.738380123, Pearson Zeroed 0.736107]</p>	<p>Tcf712_3461.1_PWM_12.txt (K-afal)</p> <p>No PWM Available</p>	<p>Tcf712_3461.1_combined_PWM_12.txt (K-afal)</p> <p>No PWM Available</p>
<p>Tcf712_3461.1_PWM_13.txt (K-afal)</p> <p>[Z-Beta = 93.8959] [Pearson Full 0.738380123, Pearson Zeroed 0.719188]</p>	<p>Tcf712_3461.1_PWM_13.txt (K-afal)</p> <p>No PWM Available</p>	<p>Tcf712_3461.1_combined_PWM_13.txt (K-afal)</p> <p>No PWM Available</p>
<p>Tcf712_3461.1_PWM_14.txt (K-afal)</p>	<p>Tcf712_3461.1_PWM_14.txt (K-afal)</p> <p>No PWM Available</p>	<p>Tcf712_3461.1_combined_PWM_14.txt (K-afal)</p> <p>No PWM Available</p>

<p>Tcf712_3461.1_PWM_15.txt (K-afal)</p> 	<p>Tcf712_3461.1_PWM_15.txt (K-afal) No PWM Available</p>	<p>Tcf712_3461.1_combined_PWM_15.txt (K-afal) No PWM Available</p>
<p>Tcf712_3461.1_PWM_16.txt (K-afal) [Z-Beta = 36.1219] [Pearson Full 0.738380123, Pearson Zeroed 0.732695]</p> 	<p>Tcf712_3461.1_PWM_16.txt (K-afal) No PWM Available</p>	<p>Tcf712_3461.1_combined_PWM_16.txt (K-afal) No PWM Available</p>
<p>Tcf712_3461.1_PWM_17.txt (K-afal)</p> 	<p>Tcf712_3461.1_PWM_17.txt (K-afal) No PWM Available</p>	<p>Tcf712_3461.1_combined_PWM_17.txt (K-afal) No PWM Available</p>
<p>Tcf712_3461.1_PWM_18.txt (K-afal) [Z-Beta = 4.03382] [Pearson Full 0.738380123, Pearson Zeroed 0.738483]</p> 	<p>Tcf712_3461.1_PWM_18.txt (K-afal) No PWM Available</p>	<p>Tcf712_3461.1_combined_PWM_18.txt (K-afal) No PWM Available</p>
<p>Tcf712_3461.1_PWM_19.txt (K-afal) [Z-Beta = 57.1973] [Pearson Full 0.738380123, Pearson Zeroed 0.712686]</p> 	<p>Tcf712_3461.1_PWM_19.txt (K-afal) No PWM Available</p>	<p>Tcf712_3461.1_combined_PWM_19.txt (K-afal) No PWM Available</p>
<p>Tcf712_3461.1_PWM_1.txt (K-afal) [Z-Beta = 48.7456] [Pearson Full 0.738380123, Pearson Zeroed 0.710175]</p> 	<p>Tcf712_3461.1_PWM_1.txt (K-afal) [Z-Beta = 12.6707] [Pearson Full 0.764550621, Pearson Zeroed 0.761777]</p> 	<p>Tcf712_3461.1_combined_PWM_1.txt (K-afal)</p> 
<p>Tcf712_3461.1_PWM_20.txt (K-afal)</p> 	<p>Tcf712_3461.1_PWM_20.txt (K-afal) No PWM Available</p>	<p>Tcf712_3461.1_combined_PWM_20.txt (K-afal) No PWM Available</p>
<p>Tcf712_3461.1_PWM_21.txt (K-afal) [Z-Beta = 10.9776] [Pearson Full 0.738380123, Pearson Zeroed 0.736123]</p> 	<p>Tcf712_3461.1_PWM_21.txt (K-afal) No PWM Available</p>	<p>Tcf712_3461.1_combined_PWM_21.txt (K-afal) No PWM Available</p>
<p>Tcf712_3461.1_PWM_22.txt (K-afal)</p> 	<p>Tcf712_3461.1_PWM_22.txt (K-afal) No PWM Available</p>	<p>Tcf712_3461.1_combined_PWM_22.txt (K-afal) No PWM Available</p>
<p>Tcf712_3461.1_PWM_23.txt (K-afal)</p> 	<p>Tcf712_3461.1_PWM_23.txt (K-afal) No PWM Available</p>	<p>Tcf712_3461.1_combined_PWM_23.txt (K-afal) No PWM Available</p>
<p>Tcf712_3461.1_PWM_24.txt (K-afal) [Z-Beta = 20.6922] [Pearson Full 0.738380123, Pearson Zeroed 0.730791]</p>	<p>Tcf712_3461.1_PWM_24.txt (K-afal) No PWM Available</p>	<p>Tcf712_3461.1_combined_PWM_24.txt (K-afal) No PWM Available</p>

 <p>Tcf712_3461.1_PWM_25.txt (K-afal) [Z-Beta = 62.7065] [Pearson Full 0.738380123, Pearson Zeroed 0.72902]</p>		
 <p>Tcf712_3461.1_PWM_2.txt (K-afal) [Z-Beta = 1.96311] [Pearson Full 0.738380123, Pearson Zeroed 0.73787]</p>	Tcf712_3461.1_PWM_25.txt (K-afal) No PWM Available	Tcf712_3461.1_combined_PWM_25.txt (K-afal) No PWM Available
 <p>Tcf712_3461.1_PWM_2.txt (K-afal) [Z-Beta = 1.96311] [Pearson Full 0.738380123, Pearson Zeroed 0.73787]</p>	Tcf712_3461.1_PWM_2.txt (K-afal) [Z-Beta = 12.7518] [Pearson Full 0.764550621, Pearson Zeroed 0.759141]	Tcf712_3461.1_combined_PWM_2.txt (K-afal) [Z-Beta = 14.0475] [Pearson Full 0.605508143, Pearson Zeroed 0.604703]
 <p>Tcf712_3461.1_PWM_3.txt (K-afal) [Z-Beta = 43.7481] [Pearson Full 0.738380123, Pearson Zeroed 0.723068]</p>	Tcf712_3461.1_PWM_3.txt (K-afal) [Z-Beta = 15.4588] [Pearson Full 0.764550621, Pearson Zeroed 0.75724]	Tcf712_3461.1_combined_PWM_3.txt (K-afal) [Z-Beta = 68.4801] [Pearson Full 0.605508143, Pearson Zeroed 0.559601]
 <p>Tcf712_3461.1_PWM_4.txt (K-afal)</p>	Tcf712_3461.1_PWM_4.txt (K-afal) [Z-Beta = 3.57548] [Pearson Full 0.764550621, Pearson Zeroed 0.763964]	Tcf712_3461.1_combined_PWM_4.txt (K-afal) [Z-Beta = 87.7867] [Pearson Full 0.605508143, Pearson Zeroed 0.567058]
 <p>Tcf712_3461.1_PWM_5.txt (K-afal)</p>	Tcf712_3461.1_PWM_5.txt (K-afal)	Tcf712_3461.1_combined_PWM_5.txt (K-afal) [Z-Beta = 64.6267] [Pearson Full 0.605508143, Pearson Zeroed 0.608704]
 <p>Tcf712_3461.1_PWM_6.txt (K-afal)</p>	Tcf712_3461.1_PWM_6.txt (K-afal)	Tcf712_3461.1_combined_PWM_6.txt (K-afal) No PWM Available
 <p>Tcf712_3461.1_PWM_7.txt (K-afal)</p>	Tcf712_3461.1_PWM_7.txt (K-afal)	Tcf712_3461.1_combined_PWM_7.txt (K-afal) No PWM Available
 <p>Tcf712_3461.1_PWM_8.txt (K-afal)</p>	Tcf712_3461.1_PWM_8.txt (K-afal)	Tcf712_3461.1_combined_PWM_8.txt (K-afal) No PWM Available
 <p>Tcf712_3461.1_PWM_9.txt (K-afal) [Z-Beta = 28.5747] [Pearson Full 0.738380123, Pearson Zeroed 0.722598]</p>	Tcf712_3461.1_PWM_9.txt (K-afal) [Z-Beta = 31.7198] [Pearson Full 0.764550621, Pearson Zeroed 0.743733]	Tcf712_3461.1_combined_PWM_9.txt (K-afal) No PWM Available
Tcf712_3461.1_PWM_10.txt (Kafal) No PWM Available	Tcf712_3461.1_PWM_10.txt (Kafal)	Tcf712_3461.1_combined_PWM_10.txt (Kafal) No PWM Available



# Tcfap2a\_2337.3

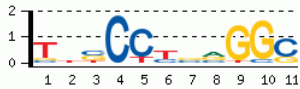
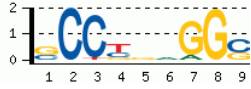
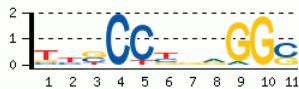
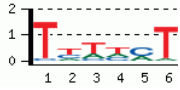
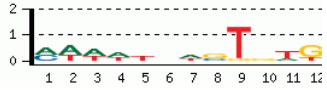
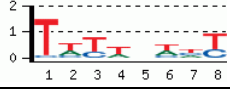
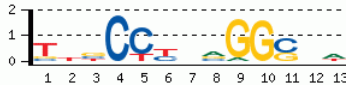

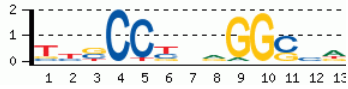
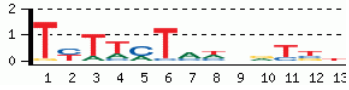
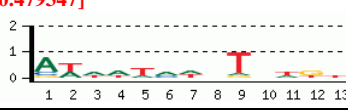
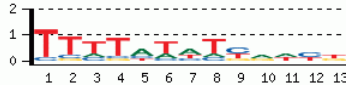
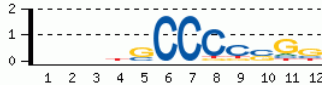






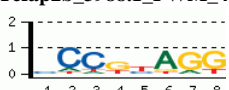



Array Version 1	Array Version 2	Combined
Tcfap2a_2337.3_v1_prim_auc.txt (SW) 	Tcfap2a_2337.3_v2_prim_auc.txt (SW) 	Tcfap2a_2337.3_combined_prim_auc.txt (SW) 
Tcfap2a_2337.3_v1_prim_ic.txt (SW) 	Tcfap2a_2337.3_v2_prim_ic.txt (SW) 	Tcfap2a_2337.3_combined_prim_ic_trimmed.txt (SW) 
Tcfap2a_2337.3_v1_prim_untrimmed.txt (SW) 	Tcfap2a_2337.3_v2_prim_untrimmed.txt (SW) 	Tcfap2a_2337.3_combined_prim_untrimmed.txt (SW) 
Tcfap2a_2337.3_v1_sec_auc.txt (SW) 	Tcfap2a_2337.3_v2_sec_auc.txt (SW) 	Tcfap2a_2337.3_combined_sec_auc.txt (SW) 
Tcfap2a_2337.3_v1_sec_ic.txt (SW) 	Tcfap2a_2337.3_v2_sec_ic.txt (SW) 	Tcfap2a_2337.3_combined_sec_ic_trimmed.txt (SW) 
Tcfap2a_2337.3_v1_sec_untrimmed.txt (SW) 	Tcfap2a_2337.3_v2_sec_untrimmed.txt (SW) 	Tcfap2a_2337.3_combined_sec_untrimmed.txt (SW) 
Tcfap2a_2337.3_v1_prim_trimmed.txt (RM8) 	Tcfap2a_2337.3_v2_prim_trimmed.txt (RM8) 	Tcfap2a_2337.3_RM8_combined_prim_trimmed.txt (RM8) No PWM Available
Tcfap2a_2337.3_v1_sec_trimmed.txt (RM8) 	Tcfap2a_2337.3_v2_sec_trimmed.txt (RM8) 	Tcfap2a_2337.3_RM8_combined_sec_trimmed.txt (RM8) No PWM Available
Tcfap2a_2337.3_v1_prim_untrimmed.txt (RM8) 	Tcfap2a_2337.3_v2_prim_untrimmed.txt (RM8) 	Tcfap2a_2337.3_RM8_combined_prim_untrimmed.txt (RM8) No PWM Available
Tcfap2a_2337.3_v1_sec_untrimmed.txt (RM8) 	Tcfap2a_2337.3_v2_sec_untrimmed.txt (RM8) 	Tcfap2a_2337.3_RM8_combined_sec_untrimmed.txt (RM8) No PWM Available
Tcfap2a_2337.3_v1_RM35_prim_trimmed.txt (RM35)  [Z-Beta = 1.11803] [Pearson Full 0.563274338, Pearson Zeroed NaN]	Tcfap2a_2337.3_v2_RM35_prim_trimmed.txt (RM35)  [Z-Beta = 0.159885] [Pearson Full 0.620721775, Pearson Zeroed NaN]	Tcfap2a_2337.3_RM_combined_prim_trimmed.txt (RM35)  [Z-Beta = 0.159885] [Pearson Full 0.620721775, Pearson Zeroed NaN]
Tcfap2a_2337.3_v1_RM35_sec_trimmed.txt (RM35) 	Tcfap2a_2337.3_v2_RM35_sec_trimmed.txt (RM35) 	Tcfap2a_2337.3_RM_combined_sec_trimmed.txt (RM35) 

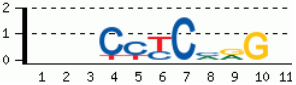

		
<b>Tcfap2a_2337.3_v1_RM35_prim_untrimmed.txt (RM35)</b> [Z-Beta = 0.721144] [Pearson Full 0.557876296, Pearson Zeroed NaN]	<b>Tcfap2a_2337.3_v2_RM35_prim_untrimmed.txt (RM35)</b>	<b>Tcfap2a_2337.3_RM_combined_prim_untrimmed.txt (RM35)</b>
		
<b>Tcfap2a_2337.3_v1_RM35_sec_untrimmed.txt (RM35)</b>	<b>Tcfap2a_2337.3_v2_RM35_sec_untrimmed.txt (RM35)</b>	<b>Tcfap2a_2337.3_RM_combined_sec_untrimmed.txt (RM35)</b>
<b>Tcfap2a_2337.3_PWM_1.txt (K-afal)</b>	<b>Tcfap2a_2337.3_PWM_1.txt (K-afal)</b>	<b>Tcfap2a_2337.3_combined_PWM_1.txt (K-afal)</b>
		
<b>Tcfap2a_2337.3_PWM_2.txt (K-afal)</b>	<b>Tcfap2a_2337.3_PWM_2.txt (K-afal)</b>	<b>Tcfap2a_2337.3_combined_PWM_2.txt (K-afal)</b>
<b>Tcfap2a_2337.3_PWM_3.txt (K-afal)</b>	<b>Tcfap2a_2337.3_PWM_3.txt (K-afal)</b>	<b>Tcfap2a_2337.3_combined_PWM_3.txt (K-afal)</b> No PWM Available
		<b>Tcfap2a_2337.3_combined_PWM_4.txt (Kafal)</b> No PWM Available
<b>Tcfap2a_2337.3_PWM_4.txt (Kafal)</b> No PWM Available	<b>Tcfap2a_2337.3_PWM_4.txt (Kafal)</b>	<b>Tcfap2a_2337.3_combined_PWM_4.txt (Kafal)</b> No PWM Available
<b>Tcfap2a_2337.3_PWM_5.txt (Kafal)</b> No PWM Available	<b>Tcfap2a_2337.3_PWM_5.txt (Kafal)</b>	<b>Tcfap2a_2337.3_combined_PWM_5.txt (Kafal)</b> No PWM Available
<b>Tcfap2a_2337.3_PWM_6.txt (Kafal)</b> No PWM Available	<b>Tcfap2a_2337.3_PWM_6.txt (Kafal)</b>	<b>Tcfap2a_2337.3_combined_PWM_6.txt (Kafal)</b> No PWM Available
<b>Tcfap2a_2337.3_PWM_7.txt (Kafal)</b> No PWM Available	<b>Tcfap2a_2337.3_PWM_7.txt (Kafal)</b>	<b>Tcfap2a_2337.3_combined_PWM_7.txt (Kafal)</b> No PWM Available

# Tcfap2b\_3988.1











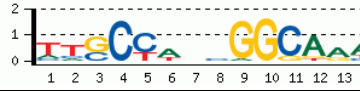
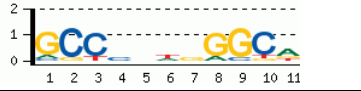

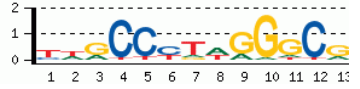
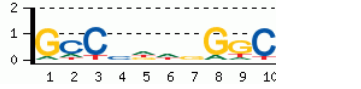
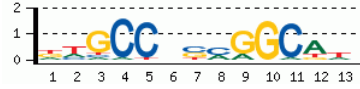
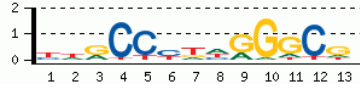
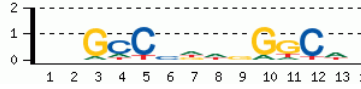
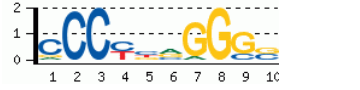



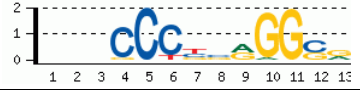
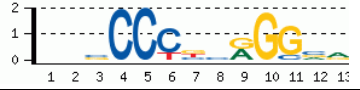
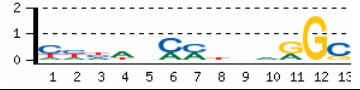
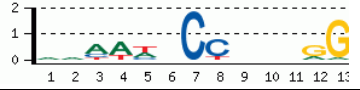



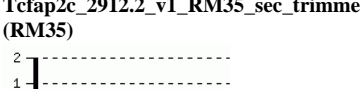
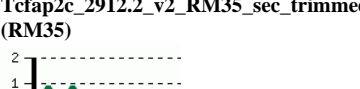
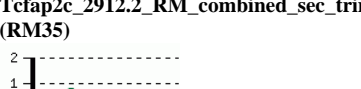
Array Version 1	Array Version 2	Combined
<p>Tcfap2b_3988.1_v1_prim_auc.txt (SW)</p>	<p>Tcfap2b_3988.1_v2_prim_auc.txt (SW)  [Z-Beta = 5.88889]  [Pearson Full 0.483539638, Pearson Zeroed 0.458576]</p>	<p>Tcfap2b_3988.1_combined_prim_auc.txt (SW)</p>
<p>Tcfap2b_3988.1_v1_prim_ic.txt (SW)</p>	<p>Tcfap2b_3988.1_v2_prim_ic.txt (SW)</p>	<p>Tcfap2b_3988.1_combined_prim_ic_trimmed.txt (SW)</p>
<p>Tcfap2b_3988.1_v1_prim_untrimmed.txt (SW)</p>	<p>Tcfap2b_3988.1_v2_prim_untrimmed.txt (SW)</p>	<p>Tcfap2b_3988.1_combined_prim_untrimmed.txt (SW)  [Z-Beta = 14.3928]  [Pearson Full 0.409198619, Pearson Zeroed 0.393841]</p>
<p>Tcfap2b_3988.1_v1_sec_auc.txt (SW)</p>	<p>Tcfap2b_3988.1_v2_sec_auc.txt (SW)</p>	<p>Tcfap2b_3988.1_combined_sec_auc.txt (SW)</p>
<p>Tcfap2b_3988.1_v1_sec_ic.txt (SW)</p>	<p>Tcfap2b_3988.1_v2_sec_ic.txt (SW)</p>	<p>Tcfap2b_3988.1_combined_sec_ic_trimmed.txt (SW)</p>
<p>Tcfap2b_3988.1_v1_sec_untrimmed.txt (SW)</p>	<p>Tcfap2b_3988.1_v2_sec_untrimmed.txt (SW)</p>	<p>Tcfap2b_3988.1_combined_sec_untrimmed.txt (SW)</p>
<p>Tcfap2b_3988.1_v1_prim_trimmed.txt (RM8)</p>	<p>Tcfap2b_3988.1_v2_prim_trimmed.txt (RM8)  [Z-Beta = 16.2267]  [Pearson Full 0.483539638, Pearson Zeroed 0.395119]</p>	<p>Tcfap2b_3988.1_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Tcfap2b_3988.1_v1_sec_trimmed.txt (RM8)</p>	<p>Tcfap2b_3988.1_v2_sec_trimmed.txt (RM8)  [Z-Beta = -57.686]  [Pearson Full 0.483539638, Pearson Zeroed 0.323421]</p>	<p>Tcfap2b_3988.1_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Tcfap2b_3988.1_v1_prim_untrimmed.txt (RM8)</p>	<p>Tcfap2b_3988.1_v2_prim_untrimmed.txt (RM8)</p>	<p>Tcfap2b_3988.1_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Tcfap2b_3988.1_v1_sec_untrimmed.txt (RM8)</p>	<p>Tcfap2b_3988.1_v2_sec_untrimmed.txt (RM8)</p>	<p>Tcfap2b_3988.1_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>



<p>Tcfap2b_3988.1_v1_RM35_prim_trimmed.txt (RM35)  [Z-Beta = 0.356015]  [Pearson Full 0.169423928, Pearson Zeroed NaN]</p> 	<p>Tcfap2b_3988.1_v2_RM35_prim_trimmed.txt (RM35)</p> 	<p>Tcfap2b_3988.1_RM_combined_prim_trimmed.txt (RM35)  [Z-Beta = 50.9903]  [Pearson Full 0.409198619, Pearson Zeroed 0.251377]</p> 
<p>Tcfap2b_3988.1_v1_RM35_sec_trimmed.txt (RM35)</p> 	<p>Tcfap2b_3988.1_v2_RM35_sec_trimmed.txt (RM35)</p> 	<p>Tcfap2b_3988.1_RM_combined_sec_trimmed.txt (RM35)  [Z-Beta = 58.5475]  [Pearson Full 0.409198619, Pearson Zeroed 0.195562]</p> 
<p>Tcfap2b_3988.1_v1_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Tcfap2b_3988.1_v2_RM35_prim_untrimmed.txt (RM35)  [Z-Beta = 7.52155]  [Pearson Full 0.483539638, Pearson Zeroed 0.448333]</p> 	<p>Tcfap2b_3988.1_RM_combined_prim_untrimmed.txt (RM35)</p> 
<p>Tcfap2b_3988.1_v1_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Tcfap2b_3988.1_v2_RM35_sec_untrimmed.txt (RM35)  [Z-Beta = 20.5982]  [Pearson Full 0.483539638, Pearson Zeroed 0.479347]</p> 	<p>Tcfap2b_3988.1_RM_combined_sec_untrimmed.txt (RM35)</p> 
<p>Tcfap2b_3988.1_PWM_1.txt (K-afal)</p> 	<p>Tcfap2b_3988.1_PWM_1.txt (K-afal)</p> 	<p>Tcfap2b_3988.1_combined_PWM_1.txt (K-afal)</p> 
<p>Tcfap2b_3988.1_PWM_2.txt (K-afal)</p> 	<p>Tcfap2b_3988.1_PWM_2.txt (K-afal)  No PWM Available</p>	<p>Tcfap2b_3988.1_combined_PWM_2.txt (K-afal)</p> 
<p>Tcfap2b_3988.1_PWM_3.txt (K-afal)</p> 	<p>Tcfap2b_3988.1_PWM_3.txt (K-afal)  No PWM Available</p>	<p>Tcfap2b_3988.1_combined_PWM_3.txt (K-afal)</p> 
<p>Tcfap2b_3988.1_PWM_4.txt (K-afal)</p> 	<p>Tcfap2b_3988.1_PWM_4.txt (K-afal)  No PWM Available</p>	<p>Tcfap2b_3988.1_combined_PWM_4.txt (K-afal)  No PWM Available</p>
<p>Tcfap2b_3988.1_PWM_5.txt (K-afal)</p> 	<p>Tcfap2b_3988.1_PWM_5.txt (K-afal)  No PWM Available</p>	<p>Tcfap2b_3988.1_combined_PWM_5.txt (K-afal)  No PWM Available</p>
<p>Tcfap2b_3988.1_PWM_6.txt (K-afal)</p> 	<p>Tcfap2b_3988.1_PWM_6.txt (K-afal)  No PWM Available</p>	<p>Tcfap2b_3988.1_combined_PWM_6.txt (K-afal)  No PWM Available</p>
<p>Tcfap2b_3988.1_PWM_7.txt (K-afal)</p> 	<p>Tcfap2b_3988.1_PWM_7.txt (K-afal)  No PWM Available</p>	<p>Tcfap2b_3988.1_combined_PWM_7.txt (K-afal)  No PWM Available</p>

<p><b>Tcfap2b_3988.1_PWM_8.txt (K-afal)</b></p> 	<p><b>Tcfap2b_3988.1_PWM_8.txt (K-afal)</b> No PWM Available</p>	<p><b>Tcfap2b_3988.1_combined_PWM_8.txt (K-afal)</b> No PWM Available</p>
<p><b>Tcfap2b_3988.1_PWM_9.txt (K-afal)</b></p> 	<p><b>Tcfap2b_3988.1_PWM_9.txt (K-afal)</b> No PWM Available</p>	<p><b>Tcfap2b_3988.1_combined_PWM_9.txt (K-afal)</b> No PWM Available</p>

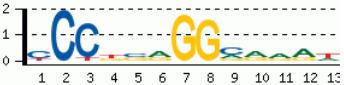
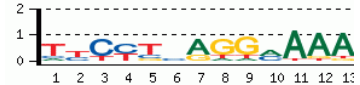

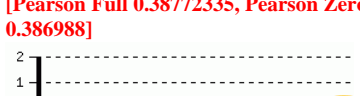
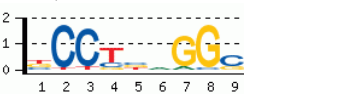

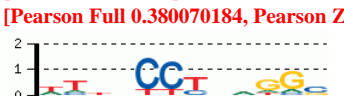

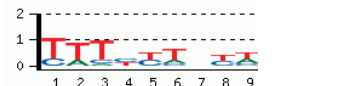
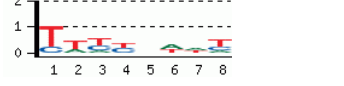
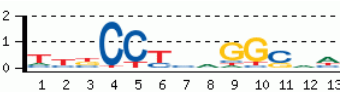


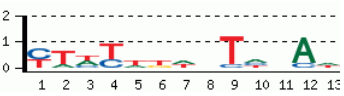
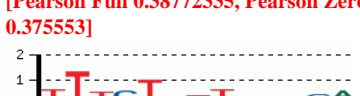
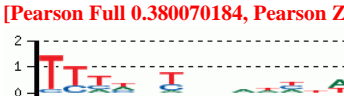
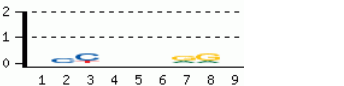
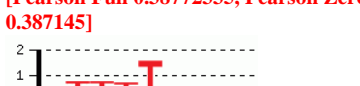

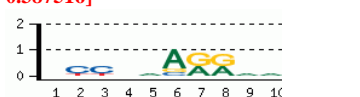
# Tcfap2c\_2912.2

Array Version 1	Array Version 2	Combined
Tcfap2c_2912.2_v1_prim_auc.txt (SW) 	Tcfap2c_2912.2_v2_prim_auc.txt (SW) 	Tcfap2c_2912.2_combined_prim_auc.txt (SW) 
Tcfap2c_2912.2_v1_prim_ic.txt (SW) 	Tcfap2c_2912.2_v2_prim_ic.txt (SW) 	Tcfap2c_2912.2_combined_prim_ic_trimmed.txt (SW) 
Tcfap2c_2912.2_v1_prim_untrimmed.txt (SW) 	Tcfap2c_2912.2_v2_prim_untrimmed.txt (SW) 	Tcfap2c_2912.2_combined_prim_untrimmed.txt (SW) 
Tcfap2c_2912.2_v1_sec_auc.txt (SW) 	Tcfap2c_2912.2_v2_sec_auc.txt (SW) 	Tcfap2c_2912.2_combined_sec_auc.txt (SW) 
Tcfap2c_2912.2_v1_sec_ic.txt (SW) 	Tcfap2c_2912.2_v2_sec_ic.txt (SW) 	Tcfap2c_2912.2_combined_sec_ic_trimmed.txt (SW) 
Tcfap2c_2912.2_v1_sec_untrimmed.txt (SW) 	Tcfap2c_2912.2_v2_sec_untrimmed.txt (SW) 	Tcfap2c_2912.2_combined_sec_untrimmed.txt (SW) 
Tcfap2c_2912.2_v1_prim_trimmed.txt (RM8) 	Tcfap2c_2912.2_v2_prim_trimmed.txt (RM8) 	Tcfap2c_2912.2_RM8_combined_prim_trimmed.txt (RM8) No PWM Available
Tcfap2c_2912.2_v1_sec_trimmed.txt (RM8) 	Tcfap2c_2912.2_v2_sec_trimmed.txt (RM8) 	Tcfap2c_2912.2_RM8_combined_sec_trimmed.txt (RM8) No PWM Available
Tcfap2c_2912.2_v1_prim_untrimmed.txt (RM8) 	Tcfap2c_2912.2_v2_prim_untrimmed.txt (RM8) 	Tcfap2c_2912.2_RM8_combined_prim_untrimmed.txt (RM8) No PWM Available
Tcfap2c_2912.2_v1_sec_untrimmed.txt (RM8) 	Tcfap2c_2912.2_v2_sec_untrimmed.txt (RM8) 	Tcfap2c_2912.2_RM8_combined_sec_untrimmed.txt (RM8) No PWM Available
Tcfap2c_2912.2_v1_RM35_prim_trimmed.txt (RM35) 	Tcfap2c_2912.2_v2_RM35_prim_trimmed.txt (RM35) 	Tcfap2c_2912.2_RM_combined_prim_trimmed.txt (RM35) 
Tcfap2c_2912.2_v1_RM35_sec_trimmed.txt (RM35) 	Tcfap2c_2912.2_v2_RM35_sec_trimmed.txt (RM35) 	Tcfap2c_2912.2_RM_combined_sec_trimmed.txt (RM35) 

<p>Tcfap2c_2912.2_v1_RM35_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = 1.33695] [Pearson Full 0.543526578, Pearson Zeroed NaN]</p>	<p>Tcfap2c_2912.2_v2_RM35_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = 0.682797] [Pearson Full 0.539729482, Pearson Zeroed NaN]</p>	<p>Tcfap2c_2912.2_RM_combined_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = 0.701875] [Pearson Full 0.578949311, Pearson Zeroed NaN]</p>
<p>Tcfap2c_2912.2_v1_RM35_sec_untrimmed.txt (RM35)</p>	<p>Tcfap2c_2912.2_v2_RM35_sec_untrimmed.txt (RM35)</p>	<p>Tcfap2c_2912.2_RM_combined_sec_untrimmed.txt (RM35)</p>
<p>Tcfap2c_2912.2_PWM_1.txt (K-afal)</p>	<p>Tcfap2c_2912.2_PWM_1.txt (K-afal)</p>	<p>Tcfap2c_2912.2_combined_PWM_1.txt (K-afal)</p>
<p>Tcfap2c_2912.2_PWM_2.txt (K-afal)</p>	<p>Tcfap2c_2912.2_PWM_2.txt (K-afal)</p>	<p>Tcfap2c_2912.2_combined_PWM_2.txt (K-afal)</p> <p>No PWM Available</p>
<p>Tcfap2c_2912.2_PWM_3.txt (K-afal)</p>	<p>Tcfap2c_2912.2_PWM_3.txt (K-afal)</p> <p>No PWM Available</p>	<p>Tcfap2c_2912.2_combined_PWM_3.txt (K-afal)</p> <p>No PWM Available</p>

# Tcfap2e\_3713.1


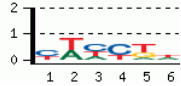
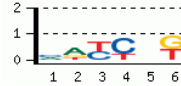
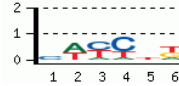
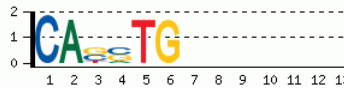
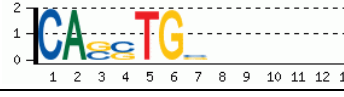
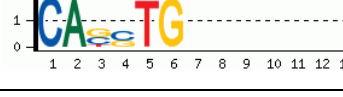
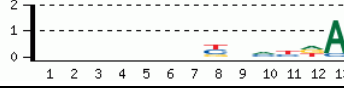
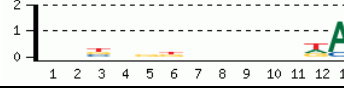
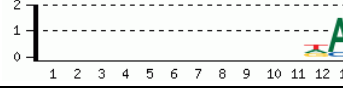






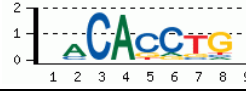


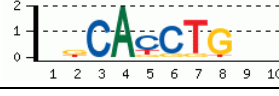

Array Version 1	Array Version 2	Combined
<p>Tcfap2e_3713.1_v1_prim_auc.txt (SW)</p>	<p>Tcfap2e_3713.1_v2_prim_auc.txt (SW)  [Z-Beta = -490.187]  [Pearson Full 0.38772335, Pearson Zeroed 0.120924]</p>	<p>Tcfap2e_3713.1_combined_prim_auc.txt (SW)</p>
<p>Tcfap2e_3713.1_v1_prim_ic.txt (SW)</p>	<p>Tcfap2e_3713.1_v2_prim_ic.txt (SW)  [Z-Beta = 107.316]  [Pearson Full 0.38772335, Pearson Zeroed 0.175064]</p>	<p>Tcfap2e_3713.1_combined_prim_ic_trimmed.txt (SW)  [Z-Beta = 52.3397]  [Pearson Full 0.380070184, Pearson Zeroed 0.284491]</p>
<p>Tcfap2e_3713.1_v1_prim_untrimmed.txt (SW)</p>	<p>Tcfap2e_3713.1_v2_prim_untrimmed.txt (SW)  [Z-Beta = -2.28982]  [Pearson Full 0.38772335, Pearson Zeroed 0.387621]</p>	<p>Tcfap2e_3713.1_combined_prim_untrimmed.txt (SW)  [Z-Beta = 410.325]  [Pearson Full 0.380070184, Pearson Zeroed 0.024212]</p>
<p>Tcfap2e_3713.1_v1_sec_auc.txt (SW)</p>	<p>Tcfap2e_3713.1_v2_sec_auc.txt (SW)  [Z-Beta = 0.125413]  [Pearson Full 0.38772335, Pearson Zeroed 0.387723]</p>	<p>Tcfap2e_3713.1_combined_sec_auc.txt (SW)</p>
<p>Tcfap2e_3713.1_v1_sec_ic.txt (SW)</p>	<p>Tcfap2e_3713.1_v2_sec_ic.txt (SW)  [Z-Beta = 3.55602]  [Pearson Full 0.38772335, Pearson Zeroed 0.387504]</p>	<p>Tcfap2e_3713.1_combined_sec_ic_trimmed.txt (SW)</p>
<p>Tcfap2e_3713.1_v1_sec_untrimmed.txt (SW)</p>	<p>Tcfap2e_3713.1_v2_sec_untrimmed.txt (SW)  [Z-Beta = -8.22621]  [Pearson Full 0.38772335, Pearson Zeroed 0.386988]</p>	<p>Tcfap2e_3713.1_combined_sec_untrimmed.txt (SW)  [Z-Beta = -5.57398]  [Pearson Full 0.380070184, Pearson Zeroed 0.379356]</p>
<p>Tcfap2e_3713.1_v1_prim_trimmed.txt (RM8)</p>	<p>Tcfap2e_3713.1_v2_prim_trimmed.txt (RM8)  [Z-Beta = 4.89553]  [Pearson Full 0.38772335, Pearson Zeroed 0.387232]</p>	<p>Tcfap2e_3713.1_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Tcfap2e_3713.1_v1_sec_trimmed.txt (RM8)</p>	<p>Tcfap2e_3713.1_v2_sec_trimmed.txt (RM8)  [Z-Beta = 89.6151]  [Pearson Full 0.38772335, Pearson Zeroed 0.212169]</p>	<p>Tcfap2e_3713.1_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>

<p>Tcfap2e_3713.1_v1_prim_untrimmed.txt (RM8)  [Z-Beta = 5.57498]  [Pearson Full 0.12564496, Pearson Zeroed 0.0737901]</p> 	<p>Tcfap2e_3713.1_v2_prim_untrimmed.txt (RM8)  [Z-Beta = -2.28982]  [Pearson Full 0.38772335, Pearson Zeroed 0.387621]</p> 	<p>Tcfap2e_3713.1_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Tcfap2e_3713.1_v1_sec_untrimmed.txt (RM8)</p> 	<p>Tcfap2e_3713.1_v2_sec_untrimmed.txt (RM8)  [Z-Beta = -8.22621]  [Pearson Full 0.38772335, Pearson Zeroed 0.386988]</p> 	<p>Tcfap2e_3713.1_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>
<p>Tcfap2e_3713.1_v1_RM35_prim_trimmed.txt (RM35)</p> 	<p>Tcfap2e_3713.1_v2_RM35_prim_trimmed.txt (RM35)  [Z-Beta = -11.1096]  [Pearson Full 0.38772335, Pearson Zeroed 0.38537]</p> 	<p>Tcfap2e_3713.1_RM_combined_prim_trimmed.txt (RM35)  [Z-Beta = -30.2576]  [Pearson Full 0.380070184, Pearson Zeroed 0.361309]</p> 
<p>Tcfap2e_3713.1_v1_RM35_sec_trimmed.txt (RM35)  [Z-Beta = 1.38754]  [Pearson Full 0.12564496, Pearson Zeroed 0.0860199]</p> 	<p>Tcfap2e_3713.1_v2_RM35_sec_trimmed.txt (RM35)  [Z-Beta = 12.7331]  [Pearson Full 0.38772335, Pearson Zeroed 0.385401]</p> 	<p>Tcfap2e_3713.1_RM_combined_sec_trimmed.txt (RM35)  [Z-Beta = 43.5999]  [Pearson Full 0.380070184, Pearson Zeroed 0.331797]</p> 
<p>Tcfap2e_3713.1_v1_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Tcfap2e_3713.1_v2_RM35_prim_untrimmed.txt (RM35)  [Z-Beta = 32.483]  [Pearson Full 0.38772335, Pearson Zeroed 0.363564]</p> 	<p>Tcfap2e_3713.1_RM_combined_prim_untrimmed.txt (RM35)  [Z-Beta = 45.3075]  [Pearson Full 0.380070184, Pearson Zeroed 0.308447]</p> 
<p>Tcfap2e_3713.1_v1_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Tcfap2e_3713.1_v2_RM35_sec_untrimmed.txt (RM35)  [Z-Beta = 26.6079]  [Pearson Full 0.38772335, Pearson Zeroed 0.375553]</p> 	<p>Tcfap2e_3713.1_RM_combined_sec_untrimmed.txt (RM35)  [Z-Beta = 45.5217]  [Pearson Full 0.380070184, Pearson Zeroed 0.326688]</p> 
<p>Tcfap2e_3713.1_PWM_1.txt (K-afal)</p> 	<p>Tcfap2e_3713.1_PWM_1.txt (K-afal)  [Z-Beta = 5.87375]  [Pearson Full 0.38772335, Pearson Zeroed 0.387145]</p> 	<p>Tcfap2e_3713.1_combined_PWM_1.txt (K-afal)  [Z-Beta = 7.1793]  [Pearson Full 0.380070184, Pearson Zeroed 0.378386]</p> 
<p>Tcfap2e_3713.1_PWM_2.txt (Kafal)  No PWM Available</p>	<p>Tcfap2e_3713.1_PWM_2.txt (Kafal)  [Z-Beta = 3.05741]  [Pearson Full 0.38772335, Pearson Zeroed 0.387516]</p> 	<p>Tcfap2e_3713.1_combined_PWM_2.txt (Kafal)  No PWM Available</p>

# Tcfe2a\_3865.1


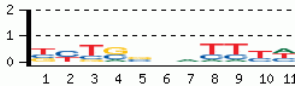
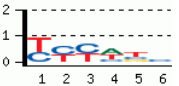
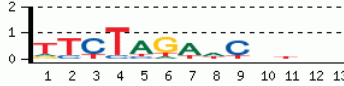


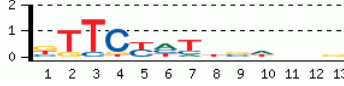
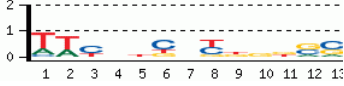
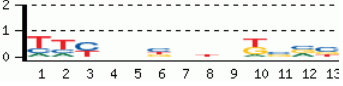






Array Version 1	Array Version 2	Combined
<p>Tcfe2a_3865.1_v1_prim_auc.txt (SW)  [Z-Beta = 695.128]  [Pearson Full 0.693342714, Pearson Zeroed NaN]</p>	<p>Tcfe2a_3865.1_v2_prim_auc.txt (SW)</p>	<p>Tcfe2a_3865.1_combined_prim_auc.txt (SW)</p>
<p>Tcfe2a_3865.1_v1_prim_ic.txt (SW)</p>	<p>Tcfe2a_3865.1_v2_prim_ic.txt (SW)  [Z-Beta = 202.798]  [Pearson Full 0.691959412, Pearson Zeroed 0.634725]</p>	<p>Tcfe2a_3865.1_combined_prim_ic_trimmed.txt (SW)  [Z-Beta = 439.135]  [Pearson Full 0.579627924, Pearson Zeroed 0.511091]</p>
<p>Tcfe2a_3865.1_v1_prim_untrimmed.txt (SW)</p>	<p>Tcfe2a_3865.1_v2_prim_untrimmed.txt (SW)</p>	<p>Tcfe2a_3865.1_combined_prim_untrimmed.txt (SW)</p>
<p>Tcfe2a_3865.1_v1_sec_auc.txt (SW)</p>	<p>Tcfe2a_3865.1_v2_sec_auc.txt (SW)</p>	<p>Tcfe2a_3865.1_combined_sec_auc.txt (SW)</p>
<p>Tcfe2a_3865.1_v1_sec_ic.txt (SW)</p>	<p>Tcfe2a_3865.1_v2_sec_ic.txt (SW)</p>	<p>Tcfe2a_3865.1_combined_sec_ic_trimmed.txt (SW)</p>
<p>Tcfe2a_3865.1_v1_sec_untrimmed.txt (SW)</p>	<p>Tcfe2a_3865.1_v2_sec_untrimmed.txt (SW)</p>	<p>Tcfe2a_3865.1_combined_sec_untrimmed.txt (SW)</p>
<p>Tcfe2a_3865.1_v1_prim_trimmed.txt (RM8)</p>	<p>Tcfe2a_3865.1_v2_prim_trimmed.txt (RM8)</p>	<p>Tcfe2a_3865.1_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Tcfe2a_3865.1_v1_sec_trimmed.txt (RM8)</p>	<p>Tcfe2a_3865.1_v2_sec_trimmed.txt (RM8)</p>	<p>Tcfe2a_3865.1_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Tcfe2a_3865.1_v1_prim_untrimmed.txt (RM8)</p>	<p>Tcfe2a_3865.1_v2_prim_untrimmed.txt (RM8)</p>	<p>Tcfe2a_3865.1_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Tcfe2a_3865.1_v1_sec_untrimmed.txt (RM8)</p>	<p>Tcfe2a_3865.1_v2_sec_untrimmed.txt (RM8)</p>	<p>Tcfe2a_3865.1_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>
<p>Tcfe2a_3865.1_v1_RM35_prim_trimmed.txt (RM35)</p>	<p>Tcfe2a_3865.1_v2_RM35_prim_trimmed.txt (RM35)  [Z-Beta = 64.425]  [Pearson Full 0.691959412, Pearson Zeroed 0.690548]</p>	<p>Tcfe2a_3865.1_RM_combined_prim_trimmed.txt (RM35)</p>



		
<b>Tcfe2a_3865.1_v1_RM35_sec_trimmed.txt (RM35)</b> 	<b>Tcfe2a_3865.1_v2_RM35_sec_trimmed.txt (RM35)</b> 	<b>Tcfe2a_3865.1_RM_combined_sec_trimmed.txt (RM35)</b> 
<b>Tcfe2a_3865.1_v1_RM35_prim_untrimmed.txt (RM35)</b> 	<b>Tcfe2a_3865.1_v2_RM35_prim_untrimmed.txt (RM35)</b> <b>[Z-Beta = 202.656]</b> <b>[Pearson Full 0.691959412, Pearson Zeroed 0.680806]</b> 	<b>Tcfe2a_3865.1_RM_combined_prim_untrimmed.txt (RM35)</b> <b>[Z-Beta = 3.33496]</b> <b>[Pearson Full 0.579627924, Pearson Zeroed 0.577342]</b> 
<b>Tcfe2a_3865.1_v1_RM35_sec_untrimmed.txt (RM35)</b> 	<b>Tcfe2a_3865.1_v2_RM35_sec_untrimmed.txt (RM35)</b> 	<b>Tcfe2a_3865.1_RM_combined_sec_untrimmed.txt (RM35)</b> 
<b>Tcfe2a_3865.1_PWM_1.txt (K-afal)</b> 	<b>Tcfe2a_3865.1_PWM_1.txt (K-afal)</b> 	<b>Tcfe2a_3865.1_combined_PWM_1.txt (K-afal)</b> 
<b>Tcfe2a_3865.1_PWM_2.txt (K-afal)</b> 	<b>Tcfe2a_3865.1_PWM_2.txt (K-afal)</b> <b>[Z-Beta = 610.019]</b> <b>[Pearson Full 0.691959412, Pearson Zeroed 0.675344]</b> 	<b>Tcfe2a_3865.1_combined_PWM_2.txt (K-afal)</b> No PWM Available
<b>Tcfe2a_3865.1_PWM_3.txt (K-afal)</b> 	<b>Tcfe2a_3865.1_PWM_3.txt (K-afal)</b> 	<b>Tcfe2a_3865.1_combined_PWM_3.txt (K-afal)</b> No PWM Available
<b>Tcfe2a_3865.1_PWM_4.txt (K-afal)</b> 	<b>Tcfe2a_3865.1_PWM_4.txt (K-afal)</b> 	<b>Tcfe2a_3865.1_combined_PWM_4.txt (K-afal)</b> No PWM Available
<b>Tcfe2a_3865.1_PWM_5.txt (Kafal)</b> No PWM Available	<b>Tcfe2a_3865.1_PWM_5.txt (Kafal)</b> 	<b>Tcfe2a_3865.1_combined_PWM_5.txt (Kafal)</b> No PWM Available
<b>Tcfe2a_3865.1_PWM_6.txt (Kafal)</b> No PWM Available	<b>Tcfe2a_3865.1_PWM_6.txt (Kafal)</b> 	<b>Tcfe2a_3865.1_combined_PWM_6.txt (Kafal)</b> No PWM Available




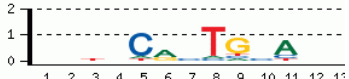









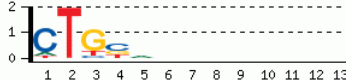

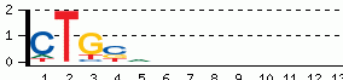



# Zbtb12\_2932.2

Array Version 1	Array Version 2	Combined
<p>Zbtb12_2932.2_v1_prim_auc.txt (SW)</p>	<p>Zbtb12_2932.2_v2_prim_auc.txt (SW)  [Z-Beta = 63.5679]  [Pearson Full 0.567299103, Pearson Zeroed 0.513762]</p>	<p>Zbtb12_2932.2_combined_prim_auc.txt (SW)  No PWM Available</p>
<p>Zbtb12_2932.2_v1_prim_ic.txt (SW)  [Z-Beta = 43.5407]  [Pearson Full 0.614143515, Pearson Zeroed 0.539586]</p>	<p>Zbtb12_2932.2_v2_prim_ic.txt (SW)  [Z-Beta = 4.96717]  [Pearson Full 0.567299103, Pearson Zeroed 0.565571]</p>	<p>Zbtb12_2932.2_combined_prim_ic_trimmed.txt (SW)  [Z-Beta = 43.0527]  [Pearson Full 0.644266408, Pearson Zeroed 0.575459]</p>
<p>Zbtb12_2932.2_v1_prim_untrimmed.txt (SW)</p>	<p>Zbtb12_2932.2_v2_prim_untrimmed.txt (SW)</p>	<p>Zbtb12_2932.2_combined_prim_untrimmed.txt (SW)</p>
<p>Zbtb12_2932.2_v1_sec_auc.txt (SW)</p>	<p>Zbtb12_2932.2_v2_sec_auc.txt (SW)</p>	<p>Zbtb12_2932.2_combined_sec_auc.txt (SW)</p>
<p>Zbtb12_2932.2_v1_sec_ic.txt (SW)</p>	<p>Zbtb12_2932.2_v2_sec_ic.txt (SW)</p>	<p>Zbtb12_2932.2_combined_sec_ic_trimmed.txt (SW)</p>
<p>Zbtb12_2932.2_v1_sec_untrimmed.txt (SW)</p>	<p>Zbtb12_2932.2_v2_sec_untrimmed.txt (SW)</p>	<p>Zbtb12_2932.2_combined_sec_untrimmed.txt (SW)</p>
<p>Zbtb12_2932.2_v1_prim_trimmed.txt (RM8)</p>	<p>Zbtb12_2932.2_v2_prim_trimmed.txt (RM8)</p>	<p>Zbtb12_2932.2_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Zbtb12_2932.2_v1_sec_trimmed.txt (RM8)</p>	<p>Zbtb12_2932.2_v2_sec_trimmed.txt (RM8)</p>	<p>Zbtb12_2932.2_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Zbtb12_2932.2_v1_prim_untrimmed.txt (RM8)</p>	<p>Zbtb12_2932.2_v2_prim_untrimmed.txt (RM8)</p>	<p>Zbtb12_2932.2_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Zbtb12_2932.2_v1_sec_untrimmed.txt (RM8)</p>	<p>Zbtb12_2932.2_v2_sec_untrimmed.txt (RM8)</p>	<p>Zbtb12_2932.2_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>
<p>Zbtb12_2932.2_v1_RM35_prim_trimmed.txt (RM35)</p>	<p>Zbtb12_2932.2_v2_RM35_prim_trimmed.txt (RM35)</p>	<p>Zbtb12_2932.2_RM_combined_prim_trimmed.txt (RM35)</p>

<p>Zbtb12_2932.2_v1_RM35_sec_trimmed.txt (RM35)</p> 	<p>Zbtb12_2932.2_v2_RM35_sec_trimmed.txt (RM35)</p> 	<p>Zbtb12_2932.2_RM_combined_sec_trimmed.txt (RM35)</p> 
<p>Zbtb12_2932.2_v1_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Zbtb12_2932.2_v2_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Zbtb12_2932.2_RM_combined_prim_untrimmed.txt (RM35)</p> 
<p>Zbtb12_2932.2_v1_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Zbtb12_2932.2_v2_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Zbtb12_2932.2_RM_combined_sec_untrimmed.txt (RM35)</p> 
<p>Zbtb12_2932.2_PWM_1.txt (K-afal)</p> 	<p>Zbtb12_2932.2_PWM_1.txt (K-afal)</p> 	<p>Zbtb12_2932.2_combined_PWM_1.txt (K-afal)  [Z-Beta = 88.1506]  [Pearson Full 0.644266408, Pearson Zeroed 0.51452]</p> 
<p>Zbtb12_2932.2_PWM_2.txt (K-afal)  [Z-Beta = 82.5906]  [Pearson Full 0.614143515, Pearson Zeroed 0.489504]</p> 	<p>Zbtb12_2932.2_PWM_2.txt (K-afal)  [Z-Beta = 91.6021]  [Pearson Full 0.567299103, Pearson Zeroed 0.478118]</p> 	<p>Zbtb12_2932.2_combined_PWM_2.txt (K-afal)</p> 

# Zbtb3\_1048.2

Array Version 1	Array Version 2	Combined
<p>Zbtb3_1048.2_v1_prim_auc.txt (SW)  [Z-Beta = 8732.21]  [Pearson Full 0.7638817, Pearson Zeroed 0.691873]</p> 	<p>Zbtb3_1048.2_v2_prim_auc.txt (SW)</p> 	<p>Zbtb3_1048.2_combined_prim_auc.txt (SW)  No PWM Available</p>
<p>Zbtb3_1048.2_v1_prim_ic.txt (SW)  [Z-Beta = -8545.7]  [Pearson Full 0.7638817, Pearson Zeroed 0.710072]</p> 	<p>Zbtb3_1048.2_v2_prim_ic.txt (SW)</p> 	<p>Zbtb3_1048.2_combined_prim_ic_trimmed.txt (SW)  [Z-Beta = 18.6653]  [Pearson Full 0.763392128, Pearson Zeroed 0.759044]</p> 
<p>Zbtb3_1048.2_v1_prim_untrimmed.txt (SW)  [Z-Beta = 347.583]  [Pearson Full 0.7638817, Pearson Zeroed 0.698388]</p> 	<p>Zbtb3_1048.2_v2_prim_untrimmed.txt (SW)</p> 	<p>Zbtb3_1048.2_combined_prim_untrimmed.txt (SW)</p> 
<p>Zbtb3_1048.2_v1_sec_auc.txt (SW)  [Z-Beta = -36.687]  [Pearson Full 0.7638817, Pearson Zeroed 0.762108]</p> 	<p>Zbtb3_1048.2_v2_sec_auc.txt (SW)</p> 	<p>Zbtb3_1048.2_combined_sec_auc.txt (SW)</p> 
<p>Zbtb3_1048.2_v1_sec_ic.txt (SW)  [Z-Beta = 19.7025]  [Pearson Full 0.7638817, Pearson Zeroed 0.763193]</p> 	<p>Zbtb3_1048.2_v2_sec_ic.txt (SW)</p> 	<p>Zbtb3_1048.2_combined_sec_ic_trimmed.txt (SW)</p> 
<p>Zbtb3_1048.2_v1_sec_untrimmed.txt (SW)  [Z-Beta = 63.7762]  [Pearson Full 0.7638817, Pearson Zeroed 0.75669]</p> 	<p>Zbtb3_1048.2_v2_sec_untrimmed.txt (SW)  [Z-Beta = 1.70547]  [Pearson Full 0.587361242, Pearson Zeroed 0.586785]</p> 	<p>Zbtb3_1048.2_combined_sec_untrimmed.txt (SW)</p> 
<p>Zbtb3_1048.2_v1_prim_trimmed.txt (RM8)  [Z-Beta = -66.1608]  [Pearson Full 0.7638817, Pearson Zeroed 0.758897]</p> 	<p>Zbtb3_1048.2_v2_prim_trimmed.txt (RM8)</p> 	<p>Zbtb3_1048.2_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Zbtb3_1048.2_v1_sec_trimmed.txt (RM8)  [Z-Beta = 3.60113]  [Pearson Full 0.7638817, Pearson Zeroed 0.763863]</p> 	<p>Zbtb3_1048.2_v2_sec_trimmed.txt (RM8)</p> 	<p>Zbtb3_1048.2_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>

<p>Zbtb3_1048.2_v1_prim_untrimmed.txt (RM8)  [Z-Beta = -27.6967]  [Pearson Full 0.7638817, Pearson Zeroed 0.762959]</p> 	<p>Zbtb3_1048.2_v2_prim_untrimmed.txt (RM8)</p> 	<p>Zbtb3_1048.2_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Zbtb3_1048.2_v1_sec_untrimmed.txt (RM8)  [Z-Beta = 23.3003]  [Pearson Full 0.7638817, Pearson Zeroed 0.763407]</p> 	<p>Zbtb3_1048.2_v2_sec_untrimmed.txt (RM8)  [Z-Beta = 1.70547]  [Pearson Full 0.587361242, Pearson Zeroed 0.586785]</p> 	<p>Zbtb3_1048.2_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>
<p>Zbtb3_1048.2_v1_RM35_prim_trimmed.txt (RM35)  [Z-Beta = 61.5956]  [Pearson Full 0.7638817, Pearson Zeroed 0.761479]</p> 	<p>Zbtb3_1048.2_v2_RM35_prim_trimmed.txt (RM35)  [Z-Beta = 20.4001]  [Pearson Full 0.587361242, Pearson Zeroed 0.579578]</p> 	<p>Zbtb3_1048.2_RM_combined_prim_trimmed.txt (RM35)</p> 
<p>Zbtb3_1048.2_v1_RM35_sec_trimmed.txt (RM35)  [Z-Beta = -120.052]  [Pearson Full 0.7638817, Pearson Zeroed 0.74397]</p> 	<p>Zbtb3_1048.2_v2_RM35_sec_trimmed.txt (RM35)</p> 	<p>Zbtb3_1048.2_RM_combined_sec_trimmed.txt (RM35)</p> 
<p>Zbtb3_1048.2_v1_RM35_prim_untrimmed.txt (RM35)  [Z-Beta = 148.046]  [Pearson Full 0.7638817, Pearson Zeroed 0.744763]</p> 	<p>Zbtb3_1048.2_v2_RM35_prim_untrimmed.txt (RM35)  [Z-Beta = 109.983]  [Pearson Full 0.587361242, Pearson Zeroed 0.496178]</p> 	<p>Zbtb3_1048.2_RM_combined_prim_untrimmed.txt (RM35)</p> 
<p>Zbtb3_1048.2_v1_RM35_sec_untrimmed.txt (RM35)  [Z-Beta = 86.2003]  [Pearson Full 0.7638817, Pearson Zeroed 0.751321]</p> 	<p>Zbtb3_1048.2_v2_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Zbtb3_1048.2_RM_combined_sec_untrimmed.txt (RM35)</p> 
<p>Zbtb3_1048.2_PWM_1.txt (K-afal)  [Z-Beta = 26.9801]  [Pearson Full 0.7638817, Pearson Zeroed 0.763211]</p> 	<p>Zbtb3_1048.2_PWM_1.txt (K-afal)  [Z-Beta = 50.2641]  [Pearson Full 0.587361242, Pearson Zeroed 0.542418]</p> 	<p>Zbtb3_1048.2_combined_PWM_1.txt (K-afal)</p> 












# Zbtb7b\_1054.2

Array Version 1	Array Version 2	Combined
<p>Zbtb7b_1054.2_v1_prim_auc.txt (SW)</p>	<p>Zbtb7b_1054.2_v2_prim_auc.txt (SW)  [Z-Beta = 148.893]  [Pearson Full 0.798212379, Pearson Zeroed 0.784827]</p>	<p>Zbtb7b_1054.2_combined_prim_auc.txt (SW)</p>
<p>Zbtb7b_1054.2_v1_prim_ic.txt (SW)</p>	<p>Zbtb7b_1054.2_v2_prim_ic.txt (SW)</p>	<p>Zbtb7b_1054.2_combined_prim_ic_trimmed.txt (SW)</p>
<p>Zbtb7b_1054.2_v1_prim_untrimmed.txt (SW)</p>	<p>Zbtb7b_1054.2_v2_prim_untrimmed.txt (SW)</p>	<p>Zbtb7b_1054.2_combined_prim_untrimmed.txt (SW)</p>
<p>Zbtb7b_1054.2_v1_sec_auc.txt (SW)</p>	<p>Zbtb7b_1054.2_v2_sec_auc.txt (SW)</p>	<p>Zbtb7b_1054.2_combined_sec_auc.txt (SW)</p>
<p>Zbtb7b_1054.2_v1_sec_ic.txt (SW)</p>	<p>Zbtb7b_1054.2_v2_sec_ic.txt (SW)</p>	<p>Zbtb7b_1054.2_combined_sec_ic_trimmed.txt (SW)</p>
<p>Zbtb7b_1054.2_v1_sec_untrimmed.txt (SW)</p>	<p>Zbtb7b_1054.2_v2_sec_untrimmed.txt (SW)</p>	<p>Zbtb7b_1054.2_combined_sec_untrimmed.txt (SW)</p>
<p>Zbtb7b_1054.2_v1_prim_trimmed.txt (RM8)</p>	<p>Zbtb7b_1054.2_v2_prim_trimmed.txt (RM8)</p>	<p>Zbtb7b_1054.2_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Zbtb7b_1054.2_v1_sec_trimmed.txt (RM8)</p>	<p>Zbtb7b_1054.2_v2_sec_trimmed.txt (RM8)</p>	<p>Zbtb7b_1054.2_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Zbtb7b_1054.2_v1_prim_untrimmed.txt (RM8)</p>	<p>Zbtb7b_1054.2_v2_prim_untrimmed.txt (RM8)</p>	<p>Zbtb7b_1054.2_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Zbtb7b_1054.2_v1_sec_untrimmed.txt (RM8)</p>	<p>Zbtb7b_1054.2_v2_sec_untrimmed.txt (RM8)</p>	<p>Zbtb7b_1054.2_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>
<p>Zbtb7b_1054.2_v1_RM35_prim_trimmed.txt (RM35)  [Z-Beta = 333.011]  [Pearson Full 0.763168544, Pearson Zeroed 0.735314]</p>	<p>Zbtb7b_1054.2_v2_RM35_prim_trimmed.txt (RM35)  [Z-Beta = 150.322]  [Pearson Full 0.798212379, Pearson Zeroed 0.784164]</p>	<p>Zbtb7b_1054.2_RM_combined_prim_trimmed.txt (RM35)  [Z-Beta = 206.839]  [Pearson Full 0.82667461, Pearson Zeroed 0.812477]</p>

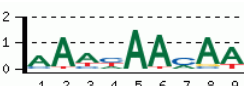
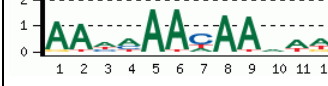
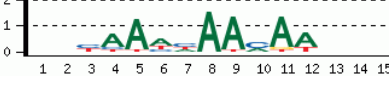
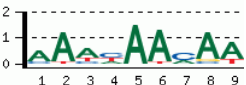
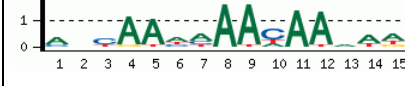
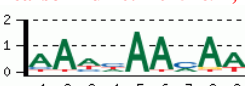
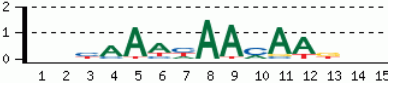
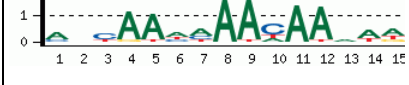
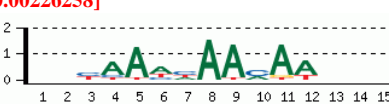

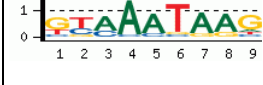
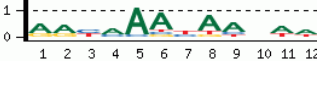

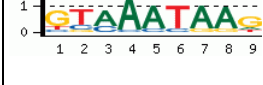

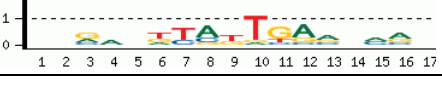




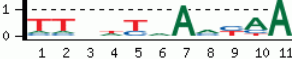
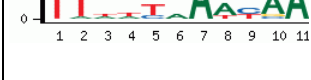


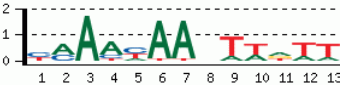
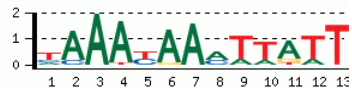
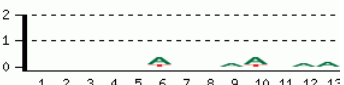
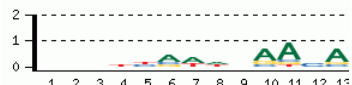
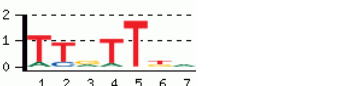
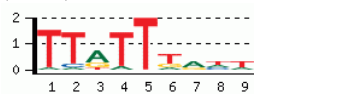
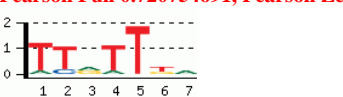
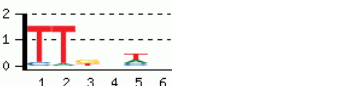
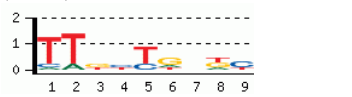
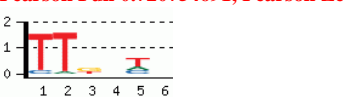
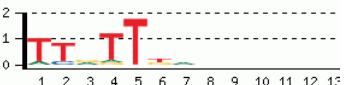
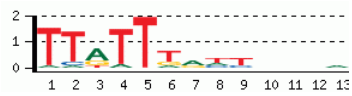

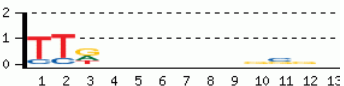
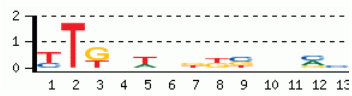
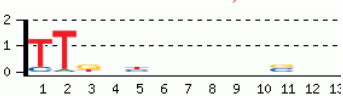


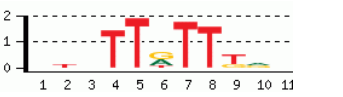
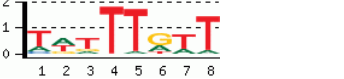


<p>Zbtb7b_1054.2_v1_RM35_sec_trimmed.txt (RM35)</p>	<p>Zbtb7b_1054.2_v2_RM35_sec_trimmed.txt (RM35)</p> <p>[Z-Beta = 101.524] [Pearson Full 0.798212379, Pearson Zeroed 0.787967]</p>	<p>Zbtb7b_1054.2_RM_combined_sec_trimmed.txt (RM35)</p> <p>[Z-Beta = 167.649] [Pearson Full 0.82667461, Pearson Zeroed 0.814247]</p>
<p>Zbtb7b_1054.2_v1_RM35_prim_untrimmed.txt (RM35)</p>	<p>Zbtb7b_1054.2_v2_RM35_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = 36.3] [Pearson Full 0.798212379, Pearson Zeroed 0.796527]</p>	<p>Zbtb7b_1054.2_RM_combined_prim_untrimmed.txt (RM35)</p>
<p>Zbtb7b_1054.2_v1_RM35_sec_untrimmed.txt (RM35)</p> <p>[Z-Beta = 102.045] [Pearson Full 0.763168544, Pearson Zeroed 0.755454]</p>	<p>Zbtb7b_1054.2_v2_RM35_sec_untrimmed.txt (RM35)</p>	<p>Zbtb7b_1054.2_RM_combined_sec_untrimmed.txt (RM35)</p> <p>[Z-Beta = 1.29105] [Pearson Full 0.82667461, Pearson Zeroed 0.826647]</p>
<p>Zbtb7b_1054.2_PWM_1.txt (K-afal)</p>	<p>Zbtb7b_1054.2_PWM_1.txt (K-afal)</p> <p>[Z-Beta = 30.8002] [Pearson Full 0.798212379, Pearson Zeroed 0.794]</p>	<p>Zbtb7b_1054.2_combined_PWM_1.txt (K-afal)</p> <p>[Z-Beta = 35.5837] [Pearson Full 0.82667461, Pearson Zeroed 0.824229]</p>
<p>Zbtb7b_1054.2_PWM_2.txt (K-afal)</p> <p>[Z-Beta = 125.841] [Pearson Full 0.763168544, Pearson Zeroed 0.749238]</p>	<p>Zbtb7b_1054.2_PWM_2.txt (K-afal)</p>	<p>Zbtb7b_1054.2_combined_PWM_2.txt (K-afal)</p> <p>[Z-Beta = 54.8584] [Pearson Full 0.82667461, Pearson Zeroed 0.820769]</p>
<p>Zbtb7b_1054.2_PWM_3.txt (Kafal)</p> <p>No PWM Available</p>	<p>Zbtb7b_1054.2_PWM_3.txt (Kafal)</p>	<p>Zbtb7b_1054.2_combined_PWM_3.txt (Kafal)</p> <p>[Z-Beta = 80.1239] [Pearson Full 0.82667461, Pearson Zeroed 0.805738]</p>
<p>Zbtb7b_1054.2_PWM_4.txt (Kafal)</p> <p>No PWM Available</p>	<p>Zbtb7b_1054.2_PWM_4.txt (Kafal)</p>	<p>Zbtb7b_1054.2_combined_PWM_4.txt (Kafal)</p>
<p>Zbtb7b_1054.2_PWM_5.txt (Kafal)</p> <p>No PWM Available</p>	<p>Zbtb7b_1054.2_PWM_5.txt (Kafal)</p>	<p>Zbtb7b_1054.2_combined_PWM_5.txt (Kafal)</p>
<p>Zbtb7b_1054.2_PWM_6.txt (Kafal)</p> <p>No PWM Available</p>	<p>Zbtb7b_1054.2_PWM_6.txt (Kafal)</p>	<p>Zbtb7b_1054.2_combined_PWM_6.txt (Kafal)</p>
<p>Zbtb7b_1054.2_PWM_7.txt (Kafal)</p> <p>No PWM Available</p>	<p>Zbtb7b_1054.2_PWM_7.txt (Kafal)</p>	<p>Zbtb7b_1054.2_combined_PWM_7.txt (Kafal)</p>



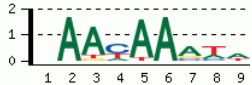
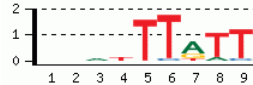
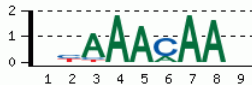
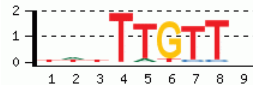


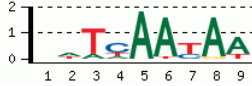





<p>Zbtb7b_1054.2_PWM_8.txt (Kafal) No PWM Available</p>	<p>Zbtb7b_1054.2_PWM_8.txt (Kafal)</p> 	<p>Zbtb7b_1054.2_combined_PWM_8.txt (Kafal)</p> 
<p>Zbtb7b_1054.2_PWM_9.txt (Kafal) No PWM Available</p>	<p>Zbtb7b_1054.2_PWM_9.txt (Kafal)</p> 	<p>Zbtb7b_1054.2_combined_PWM_9.txt (Kafal) [Z-Beta = 13.8702] [Pearson Full 0.82667461, Pearson Zeroed 0.824067]</p> 
<p>Zbtb7b_1054.2_PWM_10.txt (Kafal) No PWM Available</p>	<p>Zbtb7b_1054.2_PWM_10.txt (Kafal)</p> 	<p>Zbtb7b_1054.2_combined_PWM_10.txt (Kafal) No PWM Available</p>
<p>Zbtb7b_1054.2_PWM_11.txt (Kafal) No PWM Available</p>	<p>Zbtb7b_1054.2_PWM_11.txt (Kafal)</p> 	<p>Zbtb7b_1054.2_combined_PWM_11.txt (Kafal) No PWM Available</p>
<p>Zbtb7b_1054.2_PWM_12.txt (Kafal) No PWM Available</p>	<p>Zbtb7b_1054.2_PWM_12.txt (Kafal)</p> 	<p>Zbtb7b_1054.2_combined_PWM_12.txt (Kafal) No PWM Available</p>
<p>Zbtb7b_1054.2_PWM_13.txt (Kafal) No PWM Available</p>	<p>Zbtb7b_1054.2_PWM_13.txt (Kafal)</p> 	<p>Zbtb7b_1054.2_combined_PWM_13.txt (Kafal) No PWM Available</p>
<p>Zbtb7b_1054.2_PWM_14.txt (Kafal) No PWM Available</p>	<p>Zbtb7b_1054.2_PWM_14.txt (Kafal) [Z-Beta = 56.502] [Pearson Full 0.798212379, Pearson Zeroed 0.795391]</p> 	<p>Zbtb7b_1054.2_combined_PWM_14.txt (Kafal) No PWM Available</p>
<p>Zbtb7b_1054.2_PWM_15.txt (Kafal) No PWM Available</p>	<p>Zbtb7b_1054.2_PWM_15.txt (Kafal)</p> 	<p>Zbtb7b_1054.2_combined_PWM_15.txt (Kafal) No PWM Available</p>
<p>Zbtb7b_1054.2_PWM_16.txt (Kafal) No PWM Available</p>	<p>Zbtb7b_1054.2_PWM_16.txt (Kafal)</p> 	<p>Zbtb7b_1054.2_combined_PWM_16.txt (Kafal) No PWM Available</p>

# Zfp105\_2634.2

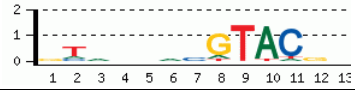
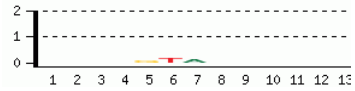
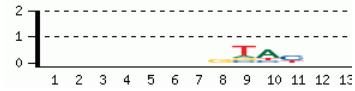


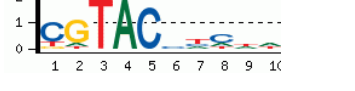
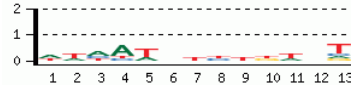

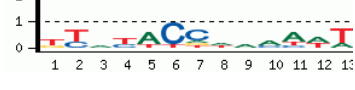


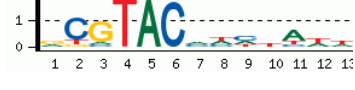
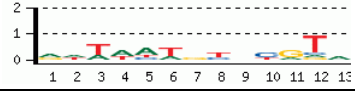
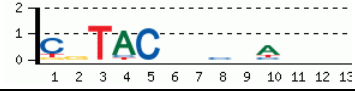
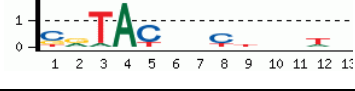

Array Version 1	Array Version 2	Combined
<p>Zfp105_2634.2_v1_prim_auc.txt (SW) [Z-Beta = -499.931] [Pearson Full 0.618043958, Pearson Zeroed 0.442818]</p> 	<p>Zfp105_2634.2_v2_prim_auc.txt (SW)</p> 	<p>Zfp105_2634.2_combined_prim_auc.txt (SW)</p> 
<p>Zfp105_2634.2_v1_prim_ic.txt (SW) [Z-Beta = 494.658] [Pearson Full 0.618043958, Pearson Zeroed 0.102966]</p> 	<p>Zfp105_2634.2_v2_prim_ic.txt (SW)</p> 	<p>Zfp105_2634.2_combined_prim_ic_trimmed.txt (SW) [Z-Beta = -16.6855] [Pearson Full 0.720754691, Pearson Zeroed 0.719298]</p> 
<p>Zfp105_2634.2_v1_prim_untrimmed.txt (SW) [Z-Beta = 29.6843] [Pearson Full 0.618043958, Pearson Zeroed 0.609999]</p> 	<p>Zfp105_2634.2_v2_prim_untrimmed.txt (SW)</p> 	<p>Zfp105_2634.2_combined_prim_untrimmed.txt (SW) [Z-Beta = 296.269] [Pearson Full 0.720754691, Pearson Zeroed 0.00226258]</p> 
<p>Zfp105_2634.2_v1_sec_auc.txt (SW) [Z-Beta = 28.6722] [Pearson Full 0.618043958, Pearson Zeroed 0.606823]</p> 	<p>Zfp105_2634.2_v2_sec_auc.txt (SW)</p> 	<p>Zfp105_2634.2_combined_sec_auc.txt (SW)</p> 
<p>Zfp105_2634.2_v1_sec_ic.txt (SW) [Z-Beta = -0.621576] [Pearson Full 0.618043958, Pearson Zeroed 0.618038]</p> 	<p>Zfp105_2634.2_v2_sec_ic.txt (SW)</p> 	<p>Zfp105_2634.2_combined_sec_ic_trimmed.txt (SW) [Z-Beta = -14.7204] [Pearson Full 0.720754691, Pearson Zeroed 0.718335]</p> 
<p>Zfp105_2634.2_v1_sec_untrimmed.txt (SW) [Z-Beta = 9.04897] [Pearson Full 0.618043958, Pearson Zeroed 0.616968]</p> 	<p>Zfp105_2634.2_v2_sec_untrimmed.txt (SW) [Z-Beta = 70.3284] [Pearson Full 0.650091841, Pearson Zeroed 0.562138]</p> 	<p>Zfp105_2634.2_combined_sec_untrimmed.txt (SW) [Z-Beta = 41.5317] [Pearson Full 0.720754691, Pearson Zeroed 0.697743]</p> 
<p>Zfp105_2634.2_v1_prim_trimmed.txt (RM8) [Z-Beta = -6.43094] [Pearson Full 0.618043958, Pearson Zeroed 0.617508]</p> 	<p>Zfp105_2634.2_v2_prim_trimmed.txt (RM8)</p> 	<p>Zfp105_2634.2_RM8_combined_prim_trimmed.txt (RM8) No PWM Available</p>
<p>Zfp105_2634.2_v1_sec_trimmed.txt (RM8) [Z-Beta = -3.61599] [Pearson Full 0.618043958, Pearson Zeroed 0.61797]</p> 	<p>Zfp105_2634.2_v2_sec_trimmed.txt (RM8)</p> 	<p>Zfp105_2634.2_RM8_combined_sec_trimmed.txt (RM8) No PWM Available</p>

<p>Zfp105_2634.2_v1_prim_untrimmed.txt (RM8)  [Z-Beta = 7.80357]  [Pearson Full 0.618043958, Pearson Zeroed 0.617495]</p> 	<p>Zfp105_2634.2_v2_prim_untrimmed.txt (RM8)</p> 	<p>Zfp105_2634.2_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Zfp105_2634.2_v1_sec_untrimmed.txt (RM8)  [Z-Beta = 91.6235]  [Pearson Full 0.618043958, Pearson Zeroed 0.550677]</p> 	<p>Zfp105_2634.2_v2_sec_untrimmed.txt (RM8)  [Z-Beta = 70.3284]  [Pearson Full 0.650091841, Pearson Zeroed 0.562138]</p> 	<p>Zfp105_2634.2_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>
<p>Zfp105_2634.2_v1_RM35_prim_trimmed.txt (RM35)  [Z-Beta = -8.08268]  [Pearson Full 0.618043958, Pearson Zeroed 0.617843]</p> 	<p>Zfp105_2634.2_v2_RM35_prim_trimmed.txt (RM35)</p> 	<p>Zfp105_2634.2_RM_combined_prim_trimmed.txt (RM35)  [Z-Beta = 59.5309]  [Pearson Full 0.720754691, Pearson Zeroed 0.707625]</p> 
<p>Zfp105_2634.2_v1_RM35_sec_trimmed.txt (RM35)  [Z-Beta = 11.0184]  [Pearson Full 0.618043958, Pearson Zeroed 0.617177]</p> 	<p>Zfp105_2634.2_v2_RM35_sec_trimmed.txt (RM35)</p> 	<p>Zfp105_2634.2_RM_combined_sec_trimmed.txt (RM35)  [Z-Beta = 53.1355]  [Pearson Full 0.720754691, Pearson Zeroed 0.696765]</p> 
<p>Zfp105_2634.2_v1_RM35_prim_untrimmed.txt (RM35)  [Z-Beta = 40.4204]  [Pearson Full 0.618043958, Pearson Zeroed 0.61207]</p> 	<p>Zfp105_2634.2_v2_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Zfp105_2634.2_RM_combined_prim_untrimmed.txt (RM35)  [Z-Beta = 21.8674]  [Pearson Full 0.720754691, Pearson Zeroed 0.719946]</p> 
<p>Zfp105_2634.2_v1_RM35_sec_untrimmed.txt (RM35)  [Z-Beta = 20.1282]  [Pearson Full 0.618043958, Pearson Zeroed 0.612031]</p> 	<p>Zfp105_2634.2_v2_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Zfp105_2634.2_RM_combined_sec_untrimmed.txt (RM35)  [Z-Beta = -3.55462]  [Pearson Full 0.720754691, Pearson Zeroed 0.720622]</p> 
<p>Zfp105_2634.2_PWM_1.txt (K-afal)  [Z-Beta = 18.4478]  [Pearson Full 0.618043958, Pearson Zeroed 0.615254]</p> 	<p>Zfp105_2634.2_PWM_1.txt (K-afal)</p> 	<p>Zfp105_2634.2_combined_PWM_1.txt (K-afal)  [Z-Beta = -4.85728]  [Pearson Full 0.720754691, Pearson Zeroed 0.720659]</p> 
<p>Zfp105_2634.2_PWM_2.txt (K-afal)  [Z-Beta = 0.941085]  [Pearson Full 0.618043958, Pearson Zeroed 0.618037]</p> 	<p>Zfp105_2634.2_PWM_2.txt (K-afal)</p> 	<p>Zfp105_2634.2_combined_PWM_2.txt (K-afal)  [Z-Beta = 7.51204]  [Pearson Full 0.720754691, Pearson Zeroed 0.720581]</p> 

<p>Zfp105_2634.2_PWM_3.txt (K-afal)  [Z-Beta = -24.2922]  [Pearson Full 0.618043958, Pearson Zeroed 0.613582]</p> 	<p>Zfp105_2634.2_PWM_3.txt (K-afal)</p> 	<p>Zfp105_2634.2_combined_PWM_3.txt (K-afal)  No PWM Available</p>
<p>Zfp105_2634.2_PWM_4.txt (K-afal)  [Z-Beta = 21.2751]  [Pearson Full 0.618043958, Pearson Zeroed 0.613939]</p> 	<p>Zfp105_2634.2_PWM_4.txt (K-afal)  [Z-Beta = 77.8341]  [Pearson Full 0.650091841, Pearson Zeroed 0.580688]</p> 	<p>Zfp105_2634.2_combined_PWM_4.txt (K-afal)  No PWM Available</p>
<p>Zfp105_2634.2_PWM_5.txt (K-afal)  [Z-Beta = -0.768368]  [Pearson Full 0.618043958, Pearson Zeroed 0.618037]</p> 	<p>Zfp105_2634.2_PWM_5.txt (K-afal)  [Z-Beta = 7.70952]  [Pearson Full 0.650091841, Pearson Zeroed 0.641254]</p> 	<p>Zfp105_2634.2_combined_PWM_5.txt (K-afal)  No PWM Available</p>
<p>Zfp105_2634.2_PWM_6.txt (K-afal)  [Z-Beta = 24.9971]  [Pearson Full 0.618043958, Pearson Zeroed 0.610359]</p> 	<p>Zfp105_2634.2_PWM_6.txt (K-afal)  [Z-Beta = 27.5486]  [Pearson Full 0.650091841, Pearson Zeroed 0.622809]</p> 	<p>Zfp105_2634.2_combined_PWM_6.txt (K-afal)  No PWM Available</p>
<p>Zfp105_2634.2_PWM_7.txt (K-afal)  [Z-Beta = 15.7064]  [Pearson Full 0.618043958, Pearson Zeroed 0.61537]</p> 	<p>Zfp105_2634.2_PWM_7.txt (K-afal)</p> 	<p>Zfp105_2634.2_combined_PWM_7.txt (K-afal)  No PWM Available</p>
<p>Zfp105_2634.2_PWM_8.txt (Kafal)  No PWM Available</p>	<p>Zfp105_2634.2_PWM_8.txt (Kafal)</p> 	<p>Zfp105_2634.2_combined_PWM_8.txt (Kafal)  No PWM Available</p>
<p>Zfp105_2634.2_PWM_9.txt (Kafal)  No PWM Available</p>	<p>Zfp105_2634.2_PWM_9.txt (Kafal)</p> 	<p>Zfp105_2634.2_combined_PWM_9.txt (Kafal)  No PWM Available</p>



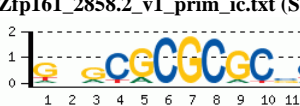
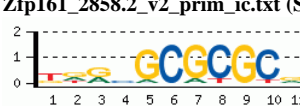
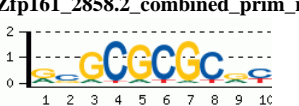
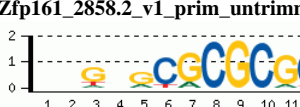
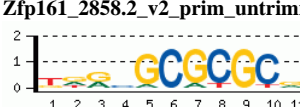
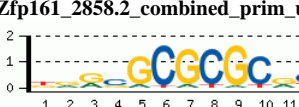
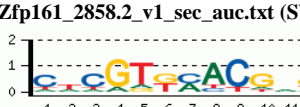
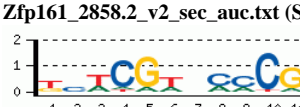
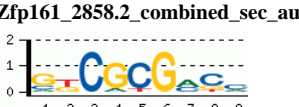
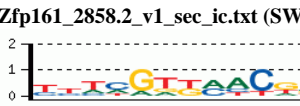
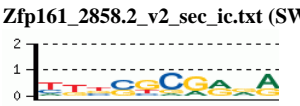
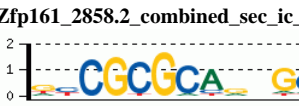
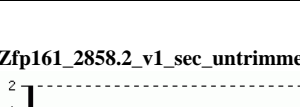
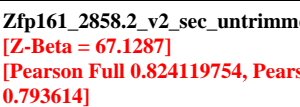
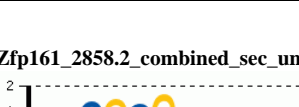

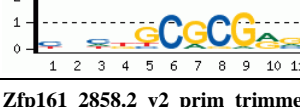


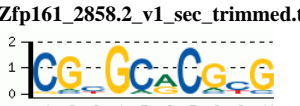
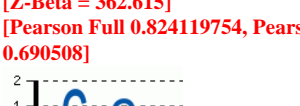
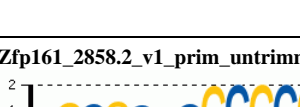
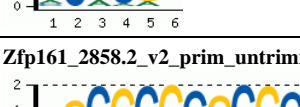
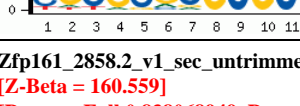
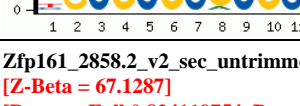
# Zfp128\_2806.2

Array Version 1	Array Version 2	Combined
<p>Zfp128_2806.2_v1_prim_auc.txt (SW)</p>	<p>Zfp128_2806.2_v2_prim_auc.txt (SW)</p>	<p>Zfp128_2806.2_combined_prim_auc.txt (SW)</p>
<p>Zfp128_2806.2_v1_prim_ic.txt (SW)</p>	<p>Zfp128_2806.2_v2_prim_ic.txt (SW)</p>	<p>Zfp128_2806.2_combined_prim_ic_trimmed.txt (SW)  [Z-Beta = -30.26]  [Pearson Full 0.581089765, Pearson Zeroed 0.56784]</p>
<p>Zfp128_2806.2_v1_prim_untrimmed.txt (SW)  [Z-Beta = 6.14254]  [Pearson Full 0.567796047, Pearson Zeroed 0.566754]</p>	<p>Zfp128_2806.2_v2_prim_untrimmed.txt (SW)</p>	<p>Zfp128_2806.2_combined_prim_untrimmed.txt (SW)  [Z-Beta = 64.5991]  [Pearson Full 0.581089765, Pearson Zeroed 0.484418]</p>
<p>Zfp128_2806.2_v1_sec_auc.txt (SW)  [Z-Beta = -2.79234]  [Pearson Full 0.567796047, Pearson Zeroed 0.567464]</p>	<p>Zfp128_2806.2_v2_sec_auc.txt (SW)</p>	<p>Zfp128_2806.2_combined_sec_auc.txt (SW)</p>
<p>Zfp128_2806.2_v1_sec_ic.txt (SW)</p>	<p>Zfp128_2806.2_v2_sec_ic.txt (SW)</p>	<p>Zfp128_2806.2_combined_sec_ic_trimmed.txt (SW)  [Z-Beta = -310.948]  [Pearson Full 0.581089765, Pearson Zeroed 0.31993]</p>
<p>Zfp128_2806.2_v1_sec_untrimmed.txt (SW)  [Z-Beta = 10.969]  [Pearson Full 0.567796047, Pearson Zeroed 0.565541]</p>	<p>Zfp128_2806.2_v2_sec_untrimmed.txt (SW)  [Z-Beta = 79.8407]  [Pearson Full 0.504990759, Pearson Zeroed 0.413422]</p>	<p>Zfp128_2806.2_combined_sec_untrimmed.txt (SW)  [Z-Beta = -5.2148]  [Pearson Full 0.581089765, Pearson Zeroed 0.580608]</p>
<p>Zfp128_2806.2_v1_prim_trimmed.txt (RM8)  [Z-Beta = -8.49394]  [Pearson Full 0.567796047, Pearson Zeroed 0.565899]</p>	<p>Zfp128_2806.2_v2_prim_trimmed.txt (RM8)</p>	<p>Zfp128_2806.2_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Zfp128_2806.2_v1_sec_trimmed.txt (RM8)  [Z-Beta = -110.002]  [Pearson Full 0.567796047, Pearson Zeroed 0.457818]</p>	<p>Zfp128_2806.2_v2_sec_trimmed.txt (RM8)</p>	<p>Zfp128_2806.2_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Zfp128_2806.2_v1_prim_untrimmed.txt (RM8)  [Z-Beta = 33.6749]  [Pearson Full 0.567796047, Pearson Zeroed 0.542501]</p>	<p>Zfp128_2806.2_v2_prim_untrimmed.txt (RM8)</p>	<p>Zfp128_2806.2_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>

		
<b>Zfp128_2806.2_v1_sec_untrimmed.txt (RM8)</b> [Z-Beta = 115.212] [Pearson Full 0.567796047, Pearson Zeroed 0.208493]	<b>Zfp128_2806.2_v2_sec_untrimmed.txt (RM8)</b> [Z-Beta = 79.8407] [Pearson Full 0.504990759, Pearson Zeroed 0.413422]	<b>Zfp128_2806.2_RM8_combined_sec_untrimmed.txt (RM8)</b> No PWM Available
		<b>Zfp128_2806.2_RM_combined_prim_trimmed.txt (RM35)</b> [Z-Beta = -58.375] [Pearson Full 0.581089765, Pearson Zeroed 0.550526]
<b>Zfp128_2806.2_v1_RM35_prim_trimmed.txt (RM35)</b> [Z-Beta = 2.34514] [Pearson Full 0.567796047, Pearson Zeroed 0.567663]	<b>Zfp128_2806.2_v2_RM35_prim_trimmed.txt (RM35)</b>	
		
<b>Zfp128_2806.2_v1_RM35_sec_trimmed.txt (RM35)</b> [Z-Beta = 31.7382] [Pearson Full 0.567796047, Pearson Zeroed 0.547629]	<b>Zfp128_2806.2_v2_RM35_sec_trimmed.txt (RM35)</b>	<b>Zfp128_2806.2_RM_combined_sec_trimmed.txt (RM35)</b> [Z-Beta = 50.6195] [Pearson Full 0.581089765, Pearson Zeroed 0.526764]
		
<b>Zfp128_2806.2_v1_RM35_prim_untrimmed.txt (RM35)</b> [Z-Beta = 6.49208] [Pearson Full 0.567796047, Pearson Zeroed 0.56674]	<b>Zfp128_2806.2_v2_RM35_prim_untrimmed.txt (RM35)</b> [Z-Beta = 35.5044] [Pearson Full 0.504990759, Pearson Zeroed 0.468163]	<b>Zfp128_2806.2_RM_combined_prim_untrimmed.txt (RM35)</b> [Z-Beta = 41.3372] [Pearson Full 0.581089765, Pearson Zeroed 0.554878]
		
<b>Zfp128_2806.2_v1_RM35_sec_untrimmed.txt (RM35)</b> [Z-Beta = -2.53044] [Pearson Full 0.567796047, Pearson Zeroed 0.56751]	<b>Zfp128_2806.2_v2_RM35_sec_untrimmed.txt (RM35)</b> [Z-Beta = 1.97068] [Pearson Full 0.504990759, Pearson Zeroed 0.504403]	<b>Zfp128_2806.2_RM_combined_sec_untrimmed.txt (RM35)</b> [Z-Beta = 78.5412] [Pearson Full 0.581089765, Pearson Zeroed 0.462957]
		
<b>Zfp128_2806.2_PWM_1.txt (K-afal)</b> [Z-Beta = 92.6496] [Pearson Full 0.567796047, Pearson Zeroed 0.329881]	<b>Zfp128_2806.2_PWM_1.txt (K-afal)</b>	<b>Zfp128_2806.2_combined_PWM_1.txt (K-afal)</b> [Z-Beta = 56.498] [Pearson Full 0.581089765, Pearson Zeroed 0.516859]
<b>Zfp128_2806.2_PWM_2.txt (Kafal)</b> No PWM Available	<b>Zfp128_2806.2_PWM_2.txt (Kafal)</b> [Z-Beta = 5.30945] [Pearson Full 0.504990759, Pearson Zeroed 0.501988]	<b>Zfp128_2806.2_combined_PWM_2.txt (Kafal)</b> No PWM Available
		



# Zfp161\_2858.2

Array Version 1	Array Version 2	Combined
Zfp161_2858.2_v1_prim_auc.txt (SW) 	Zfp161_2858.2_v2_prim_auc.txt (SW) 	Zfp161_2858.2_combined_prim_auc.txt (SW) No PWM Available
Zfp161_2858.2_v1_prim_ic.txt (SW) 	Zfp161_2858.2_v2_prim_ic.txt (SW) 	Zfp161_2858.2_combined_prim_ic_trimmed.txt (SW) 
Zfp161_2858.2_v1_prim_untrimmed.txt (SW) 	Zfp161_2858.2_v2_prim_untrimmed.txt (SW) 	Zfp161_2858.2_combined_prim_untrimmed.txt (SW) 
Zfp161_2858.2_v1_sec_auc.txt (SW) 	Zfp161_2858.2_v2_sec_auc.txt (SW) 	Zfp161_2858.2_combined_sec_auc.txt (SW) 
Zfp161_2858.2_v1_sec_ic.txt (SW) 	Zfp161_2858.2_v2_sec_ic.txt (SW) 	Zfp161_2858.2_combined_sec_ic_trimmed.txt (SW) 
Zfp161_2858.2_v1_sec_untrimmed.txt (SW) 	Zfp161_2858.2_v2_sec_untrimmed.txt (SW) [Z-Beta = 67.1287] [Pearson Full 0.824119754, Pearson Zeroed 0.793614] 	Zfp161_2858.2_combined_sec_untrimmed.txt (SW) 
Zfp161_2858.2_v1_prim_trimmed.txt (RM8) 	Zfp161_2858.2_v2_prim_trimmed.txt (RM8) 	Zfp161_2858.2_RM8_combined_prim_trimmed.txt (RM8) No PWM Available
Zfp161_2858.2_v1_sec_trimmed.txt (RM8) 	Zfp161_2858.2_v2_sec_trimmed.txt (RM8) [Z-Beta = 362.615] [Pearson Full 0.824119754, Pearson Zeroed 0.690508] 	Zfp161_2858.2_RM8_combined_sec_trimmed.txt (RM8) No PWM Available
Zfp161_2858.2_v1_prim_untrimmed.txt (RM8) 	Zfp161_2858.2_v2_prim_untrimmed.txt (RM8) 	Zfp161_2858.2_RM8_combined_prim_untrimmed.txt (RM8) No PWM Available
Zfp161_2858.2_v1_sec_untrimmed.txt (RM8) [Z-Beta = 160.559] [Pearson Full 0.838068949, Pearson Zeroed 0.82348] 	Zfp161_2858.2_v2_sec_untrimmed.txt (RM8) [Z-Beta = 67.1287] [Pearson Full 0.824119754, Pearson Zeroed 0.793614] 	Zfp161_2858.2_RM8_combined_sec_untrimmed.txt (RM8) No PWM Available
Zfp161_2858.2_v1_RM35_prim_trimmed.txt (RM35) 	Zfp161_2858.2_v2_RM35_prim_trimmed.txt (RM35) 	Zfp161_2858.2_RM_combined_prim_trimmed.txt (RM35)

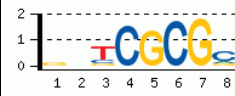


<p>Zfp161_2858.2_v1_RM35_sec_trimmed.txt (RM35) [Z-Beta = 117.649] [Pearson Full 0.838068949, Pearson Zeroed 0.825801]</p>	<p>Zfp161_2858.2_v2_RM35_sec_trimmed.txt (RM35)</p>	<p>Zfp161_2858.2_RM_combined_sec_trimmed.txt (RM35)</p>
<p>Zfp161_2858.2_v1_RM35_prim_untrimmed.txt (RM35)</p>	<p>Zfp161_2858.2_v2_RM35_prim_untrimmed.txt (RM35)</p>	<p>Zfp161_2858.2_RM_combined_prim_untrimmed.txt (RM35)</p>
<p>Zfp161_2858.2_v1_RM35_sec_untrimmed.txt (RM35)</p>	<p>Zfp161_2858.2_v2_RM35_sec_untrimmed.txt (RM35)</p>	<p>Zfp161_2858.2_RM_combined_sec_untrimmed.txt (RM35)</p>
<p>Zfp161_2858.2_PWM_1.txt (K-afal) [Z-Beta = 44.6119] [Pearson Full 0.838068949, Pearson Zeroed 0.834005]</p>	<p>Zfp161_2858.2_PWM_1.txt (K-afal)</p>	<p>Zfp161_2858.2_combined_PWM_1.txt (K-afal)</p>
<p>Zfp161_2858.2_PWM_2.txt (K-afal) [Z-Beta = 24.8673] [Pearson Full 0.838068949, Pearson Zeroed 0.829374]</p>	<p>Zfp161_2858.2_PWM_2.txt (K-afal)</p>	<p>Zfp161_2858.2_combined_PWM_2.txt (K-afal)</p>
<p>Zfp161_2858.2_PWM_3.txt (K-afal) [Z-Beta = 15.1936] [Pearson Full 0.838068949, Pearson Zeroed 0.83683]</p>	<p>Zfp161_2858.2_PWM_3.txt (K-afal) [Z-Beta = 31.7732] [Pearson Full 0.824119754, Pearson Zeroed 0.819822]</p>	<p>Zfp161_2858.2_combined_PWM_3.txt (K-afal) [Z-Beta = 3.62534] [Pearson Full 0.820686557, Pearson Zeroed 0.820157]</p>
<p>Zfp161_2858.2_PWM_4.txt (K-afal)</p>	<p>Zfp161_2858.2_PWM_4.txt (K-afal)</p>	<p>Zfp161_2858.2_combined_PWM_4.txt (K-afal)</p>
<p>Zfp161_2858.2_PWM_5.txt (K-afal) [Z-Beta = 259.781] [Pearson Full 0.838068949, Pearson Zeroed 0.834802]</p>	<p>Zfp161_2858.2_PWM_5.txt (K-afal)</p>	<p>Zfp161_2858.2_combined_PWM_5.txt (K-afal) [Z-Beta = 127.664] [Pearson Full 0.820686557, Pearson Zeroed 0.758854]</p>
<p>Zfp161_2858.2_PWM_6.txt (K-afal) [Z-Beta = 166.872] [Pearson Full 0.838068949, Pearson Zeroed 0.815438]</p>	<p>Zfp161_2858.2_PWM_6.txt (K-afal)</p>	<p>Zfp161_2858.2_combined_PWM_6.txt (K-afal)</p>

Zfp161\_2858.2\_PWM\_7.txt (K-afal)



Zfp161\_2858.2\_PWM\_7.txt (K-afal)

















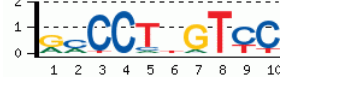



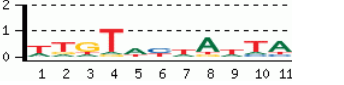


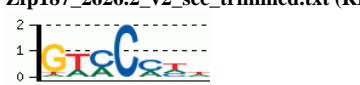
Zfp161\_2858.2\_combined\_PWM\_7.txt (K-afal)

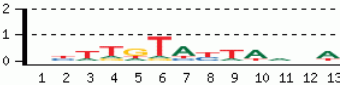
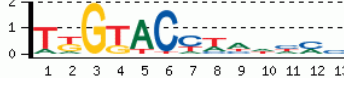
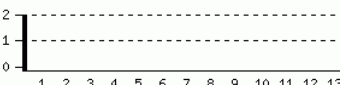
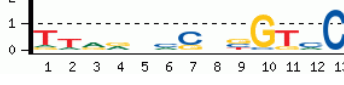
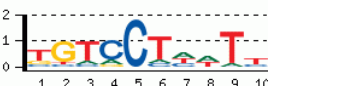


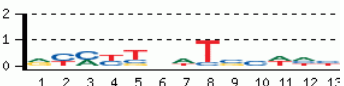

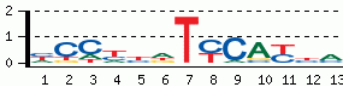
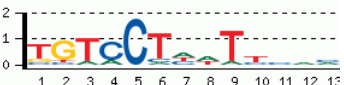


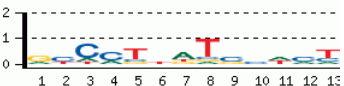
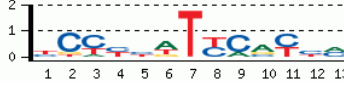
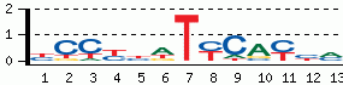
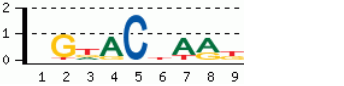


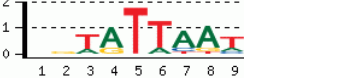


[Z-Beta = 148.293]




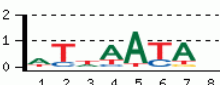


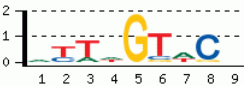













[Pearson Full 0.820686557, Pearson Zeroed 0.77898]

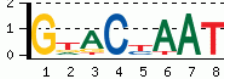





# Zfp187\_2626.2

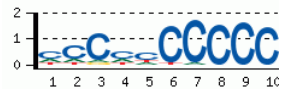


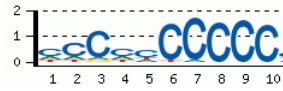

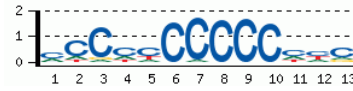
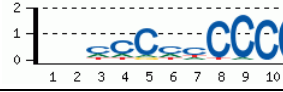
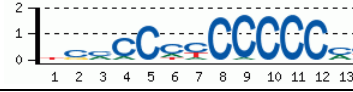



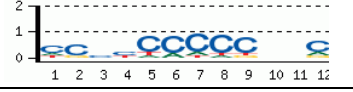









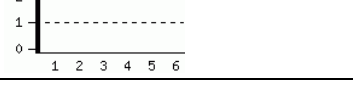

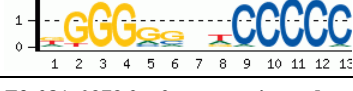
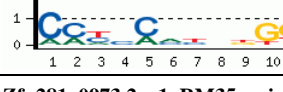
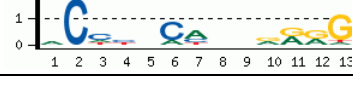


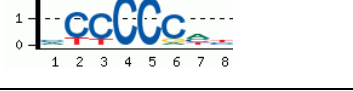
Array Version 1	Array Version 2	Combined
<p>Zfp187_2626.2_v1_prim_auc.txt (SW)  [Z-Beta = 32.8143]  [Pearson Full 0.592578904, Pearson Zeroed 0.567621]</p> 	<p>Zfp187_2626.2_v2_prim_auc.txt (SW)</p> 	<p>Zfp187_2626.2_combined_prim_auc.txt (SW)</p> 
<p>Zfp187_2626.2_v1_prim_ic.txt (SW)</p> 	<p>Zfp187_2626.2_v2_prim_ic.txt (SW)</p> 	<p>Zfp187_2626.2_combined_prim_ic_trimmed.txt (SW)  [Z-Beta = -46.0992]  [Pearson Full 0.457686677, Pearson Zeroed 0.435992]</p> 
<p>Zfp187_2626.2_v1_prim_untrimmed.txt (SW)  [Z-Beta = -16.0374]  [Pearson Full 0.592578904, Pearson Zeroed 0.587476]</p> 	<p>Zfp187_2626.2_v2_prim_untrimmed.txt (SW)</p> 	<p>Zfp187_2626.2_combined_prim_untrimmed.txt (SW)</p> 
<p>Zfp187_2626.2_v1_sec_auc.txt (SW)  [Z-Beta = -29.3141]  [Pearson Full 0.592578904, Pearson Zeroed 0.573819]</p> 	<p>Zfp187_2626.2_v2_sec_auc.txt (SW)</p> 	<p>Zfp187_2626.2_combined_sec_auc.txt (SW)</p> 
<p>Zfp187_2626.2_v1_sec_ic.txt (SW)  [Z-Beta = 9.65032]  [Pearson Full 0.592578904, Pearson Zeroed 0.5904]</p> 	<p>Zfp187_2626.2_v2_sec_ic.txt (SW)</p> 	<p>Zfp187_2626.2_combined_sec_ic_trimmed.txt (SW)  [Z-Beta = -20.0945]  [Pearson Full 0.457686677, Pearson Zeroed 0.450463]</p> 
<p>Zfp187_2626.2_v1_sec_untrimmed.txt (SW)  [Z-Beta = 41.1895]  [Pearson Full 0.592578904, Pearson Zeroed 0.554045]</p> 	<p>Zfp187_2626.2_v2_sec_untrimmed.txt (SW)</p> 	<p>Zfp187_2626.2_combined_sec_untrimmed.txt (SW)  [Z-Beta = 63.4181]  [Pearson Full 0.457686677, Pearson Zeroed 0.37602]</p> 
<p>Zfp187_2626.2_v1_prim_trimmed.txt (RM8)  [Z-Beta = 14.2621]  [Pearson Full 0.592578904, Pearson Zeroed 0.589909]</p> 	<p>Zfp187_2626.2_v2_prim_trimmed.txt (RM8)</p> 	<p>Zfp187_2626.2_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Zfp187_2626.2_v1_sec_trimmed.txt (RM8)  [Z-Beta = 47.4259]  [Pearson Full 0.592578904, Pearson Zeroed 0.574088]</p> 	<p>Zfp187_2626.2_v2_sec_trimmed.txt (RM8)</p> 	<p>Zfp187_2626.2_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>

<p>Zfp187_2626.2_v1_prim_untrimmed.txt (RM8)  [Z-Beta = 0.279837]  [Pearson Full 0.592578904, Pearson Zeroed 0.592578]</p> 	<p>Zfp187_2626.2_v2_prim_untrimmed.txt (RM8)</p> 	<p>Zfp187_2626.2_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Zfp187_2626.2_v1_sec_untrimmed.txt (RM8)  [Z-Beta = 59.1159]  [Pearson Full 0.592578904, Pearson Zeroed 0.560726]</p> 	<p>Zfp187_2626.2_v2_sec_untrimmed.txt (RM8)</p> 	<p>Zfp187_2626.2_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>
<p>Zfp187_2626.2_v1_RM35_prim_trimmed.txt (RM35)  [Z-Beta = 3.68457]  [Pearson Full 0.592578904, Pearson Zeroed 0.592278]</p> 	<p>Zfp187_2626.2_v2_RM35_prim_trimmed.txt (RM35)</p> 	<p>Zfp187_2626.2_RM_combined_prim_trimmed.txt (RM35)  [Z-Beta = -55.2307]  [Pearson Full 0.457686677, Pearson Zeroed 0.428617]</p> 
<p>Zfp187_2626.2_v1_RM35_sec_trimmed.txt (RM35)  [Z-Beta = 16.0998]  [Pearson Full 0.592578904, Pearson Zeroed 0.587637]</p> 	<p>Zfp187_2626.2_v2_RM35_sec_trimmed.txt (RM35)</p> 	<p>Zfp187_2626.2_RM_combined_sec_trimmed.txt (RM35)  [Z-Beta = 30.2584]  [Pearson Full 0.457686677, Pearson Zeroed 0.439532]</p> 
<p>Zfp187_2626.2_v1_RM35_prim_untrimmed.txt (RM35)  [Z-Beta = 2.08685]  [Pearson Full 0.592578904, Pearson Zeroed 0.592482]</p> 	<p>Zfp187_2626.2_v2_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Zfp187_2626.2_RM_combined_prim_untrimmed.txt (RM35)  [Z-Beta = 75.3582]  [Pearson Full 0.457686677, Pearson Zeroed 0.343306]</p> 
<p>Zfp187_2626.2_v1_RM35_sec_untrimmed.txt (RM35)  [Z-Beta = 3.56925]  [Pearson Full 0.592578904, Pearson Zeroed 0.592295]</p> 	<p>Zfp187_2626.2_v2_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Zfp187_2626.2_RM_combined_sec_untrimmed.txt (RM35)  [Z-Beta = 6.31974]  [Pearson Full 0.457686677, Pearson Zeroed 0.45695]</p> 
<p>Zfp187_2626.2_PWM_1.txt (K-afal)  [Z-Beta = 31.7901]  [Pearson Full 0.592578904, Pearson Zeroed 0.570032]</p> 	<p>Zfp187_2626.2_PWM_1.txt (K-afal)</p> 	<p>Zfp187_2626.2_combined_PWM_1.txt (K-afal)  [Z-Beta = -13.5389]  [Pearson Full 0.457686677, Pearson Zeroed 0.45462]</p> 
<p>Zfp187_2626.2_PWM_2.txt (K-afal)  [Z-Beta = 14.0168]  [Pearson Full 0.592578904, Pearson Zeroed 0.588923]</p> 	<p>Zfp187_2626.2_PWM_2.txt (K-afal)</p> 	<p>Zfp187_2626.2_combined_PWM_2.txt (K-afal)  [Z-Beta = 56.3674]  [Pearson Full 0.457686677, Pearson Zeroed 0.396052]</p> 

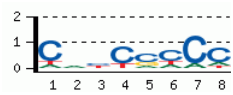
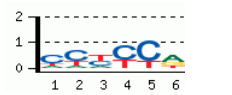
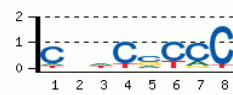
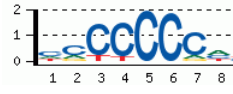

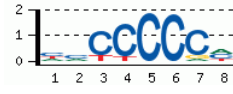

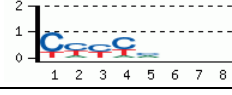


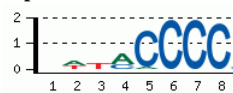

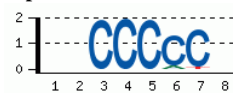
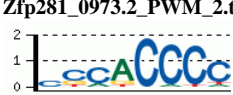
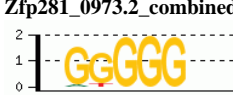
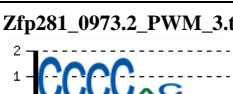
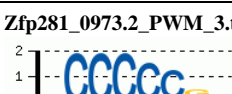
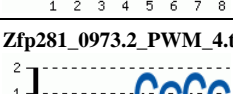
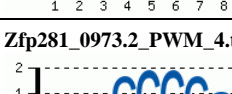
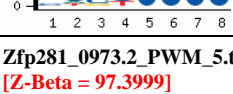
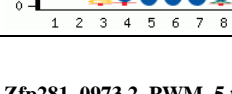
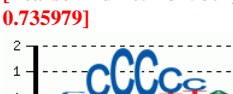
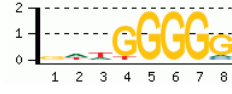
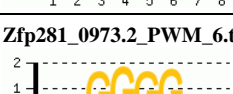
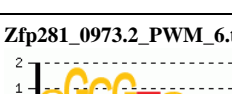
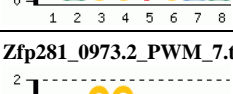
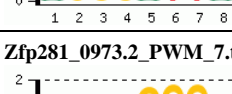
<p>Zfp187_2626.2_PWM_3.txt (K-afal)  [Z-Beta = -9.8638]  [Pearson Full 0.592578904, Pearson Zeroed 0.590711]</p> 	<p>Zfp187_2626.2_PWM_3.txt (K-afal)</p> 	<p>Zfp187_2626.2_combined_PWM_3.txt (K-afal)  [Z-Beta = -18.4491]  [Pearson Full 0.457686677, Pearson Zeroed 0.4522]</p> 
<p>Zfp187_2626.2_PWM_4.txt (K-afal)  [Z-Beta = 28.9492]  [Pearson Full 0.592578904, Pearson Zeroed 0.579266]</p> 	<p>Zfp187_2626.2_PWM_4.txt (K-afal)</p> 	<p>Zfp187_2626.2_combined_PWM_4.txt (K-afal)  [Z-Beta = 9.70356]  [Pearson Full 0.457686677, Pearson Zeroed 0.456105]</p> 
<p>Zfp187_2626.2_PWM_5.txt (K-afal)  [Z-Beta = -3.01698]  [Pearson Full 0.592578904, Pearson Zeroed 0.592398]</p> 	<p>Zfp187_2626.2_PWM_5.txt (K-afal)</p> 	<p>Zfp187_2626.2_combined_PWM_5.txt (K-afal)  [Z-Beta = 38.2812]  [Pearson Full 0.457686677, Pearson Zeroed 0.428786]</p> 
<p>Zfp187_2626.2_PWM_6.txt (Kafal)  No PWM Available</p>	<p>Zfp187_2626.2_PWM_6.txt (Kafal)</p> 	<p>Zfp187_2626.2_combined_PWM_6.txt (Kafal)  [Z-Beta = 19.4342]  [Pearson Full 0.457686677, Pearson Zeroed 0.451379]</p> 
<p>Zfp187_2626.2_PWM_7.txt (Kafal)  No PWM Available</p>	<p>Zfp187_2626.2_PWM_7.txt (Kafal)</p> 	<p>Zfp187_2626.2_combined_PWM_7.txt (Kafal)</p> 
<p>Zfp187_2626.2_PWM_8.txt (Kafal)  No PWM Available</p>	<p>Zfp187_2626.2_PWM_8.txt (Kafal)</p> 	<p>Zfp187_2626.2_combined_PWM_8.txt (Kafal)  [Z-Beta = 40.8758]  [Pearson Full 0.457686677, Pearson Zeroed 0.428566]</p> 
<p>Zfp187_2626.2_PWM_9.txt (Kafal)  No PWM Available</p>	<p>Zfp187_2626.2_PWM_9.txt (Kafal)</p> 	<p>Zfp187_2626.2_combined_PWM_9.txt (Kafal)  No PWM Available</p>
<p>Zfp187_2626.2_PWM_10.txt (Kafal)  No PWM Available</p>	<p>Zfp187_2626.2_PWM_10.txt (Kafal)</p> 	<p>Zfp187_2626.2_combined_PWM_10.txt (Kafal)  No PWM Available</p>
<p>Zfp187_2626.2_PWM_11.txt (Kafal)  No PWM Available</p>	<p>Zfp187_2626.2_PWM_11.txt (Kafal)</p> 	<p>Zfp187_2626.2_combined_PWM_11.txt (Kafal)  No PWM Available</p>
<p>Zfp187_2626.2_PWM_12.txt (Kafal)  No PWM Available</p>	<p>Zfp187_2626.2_PWM_12.txt (Kafal)</p> 	<p>Zfp187_2626.2_combined_PWM_12.txt (Kafal)  No PWM Available</p>
<p>Zfp187_2626.2_PWM_13.txt (Kafal)  No PWM Available</p>	<p>Zfp187_2626.2_PWM_13.txt (Kafal)</p> 	<p>Zfp187_2626.2_combined_PWM_13.txt (Kafal)  No PWM Available</p>

<p>Zfp187_2626.2_PWM_14.txt (Kafal) No PWM Available</p>	<p>Zfp187_2626.2_PWM_14.txt (Kafal) [Z-Beta = 1.59611] [Pearson Full 0.202919336, Pearson Zeroed NaN]</p> 	<p>Zfp187_2626.2_combined_PWM_14.txt (Kafal) No PWM Available</p>
<p>Zfp187_2626.2_PWM_15.txt (Kafal) No PWM Available</p>	<p>Zfp187_2626.2_PWM_15.txt (Kafal)</p> 	<p>Zfp187_2626.2_combined_PWM_15.txt (Kafal) No PWM Available</p>
<p>Zfp187_2626.2_PWM_16.txt (Kafal) No PWM Available</p>	<p>Zfp187_2626.2_PWM_16.txt (Kafal)</p> 	<p>Zfp187_2626.2_combined_PWM_16.txt (Kafal) No PWM Available</p>
<p>Zfp187_2626.2_PWM_17.txt (Kafal) No PWM Available</p>	<p>Zfp187_2626.2_PWM_17.txt (Kafal)</p> 	<p>Zfp187_2626.2_combined_PWM_17.txt (Kafal) No PWM Available</p>

# Zfp281\_0973.2

Array Version 1	Array Version 2	Combined
Zfp281_0973.2_v1_prim_auc.txt (SW) 	Zfp281_0973.2_v2_prim_auc.txt (SW) 	Zfp281_0973.2_combined_prim_auc.txt (SW) 
Zfp281_0973.2_v1_prim_ic.txt (SW) 	Zfp281_0973.2_v2_prim_ic.txt (SW) 	Zfp281_0973.2_combined_prim_ic_trimmed.txt (SW) 
Zfp281_0973.2_v1_prim_untrimmed.txt (SW) 	Zfp281_0973.2_v2_prim_untrimmed.txt (SW) 	Zfp281_0973.2_combined_prim_untrimmed.txt (SW) 
Zfp281_0973.2_v1_sec_auc.txt (SW) 	Zfp281_0973.2_v2_sec_auc.txt (SW) 	Zfp281_0973.2_combined_sec_auc.txt (SW) 
Zfp281_0973.2_v1_sec_ic.txt (SW) 	Zfp281_0973.2_v2_sec_ic.txt (SW) 	Zfp281_0973.2_combined_sec_ic_trimmed.txt (SW) 
Zfp281_0973.2_v1_sec_untrimmed.txt (SW) 	Zfp281_0973.2_v2_sec_untrimmed.txt (SW) 	Zfp281_0973.2_combined_sec_untrimmed.txt (SW) 
Zfp281_0973.2_v1_prim_trimmed.txt (RM8) 	Zfp281_0973.2_v2_prim_trimmed.txt (RM8) 	Zfp281_0973.2_RM8_combined_prim_trimmed.txt (RM8) No PWM Available
Zfp281_0973.2_v1_sec_trimmed.txt (RM8) 	Zfp281_0973.2_v2_sec_trimmed.txt (RM8) 	Zfp281_0973.2_RM8_combined_sec_trimmed.txt (RM8) No PWM Available
Zfp281_0973.2_v1_prim_untrimmed.txt (RM8) 	Zfp281_0973.2_v2_prim_untrimmed.txt (RM8) 	Zfp281_0973.2_RM8_combined_prim_untrimmed.txt (RM8) No PWM Available
Zfp281_0973.2_v1_sec_untrimmed.txt (RM8) 	Zfp281_0973.2_v2_sec_untrimmed.txt (RM8) 	Zfp281_0973.2_RM8_combined_sec_untrimmed.txt (RM8) No PWM Available
Zfp281_0973.2_v1_RM35_prim_trimmed.txt (RM35) [Z-Beta = 973.555] [Pearson Full 0.74345801, Pearson Zeroed 0.7039] 	Zfp281_0973.2_v2_RM35_prim_trimmed.txt (RM35) 	Zfp281_0973.2_RM_combined_prim_trimmed.txt (RM35) [Z-Beta = 238.476] [Pearson Full 0.702516561, Pearson Zeroed NaN] 
Zfp281_0973.2_v1_RM35_sec_trimmed.txt (RM35)	Zfp281_0973.2_v2_RM35_sec_trimmed.txt (RM35)	Zfp281_0973.2_RM_combined_sec_trimmed.txt (RM35)



		
Zfp281_0973.2_v1_RM35_prim_untrimmed.txt (RM35) 	Zfp281_0973.2_v2_RM35_prim_untrimmed.txt (RM35) [Z-Beta = 690.367] [Pearson Full 0.670862674, Pearson Zeroed NaN] 	Zfp281_0973.2_RM_combined_prim_untrimmed.txt (RM35) 
Zfp281_0973.2_v1_RM35_sec_untrimmed.txt (RM35) 	Zfp281_0973.2_v2_RM35_sec_untrimmed.txt (RM35) 	Zfp281_0973.2_RM_combined_sec_untrimmed.txt (RM35) 
Zfp281_0973.2_PWM_10.txt (K-afal) 	Zfp281_0973.2_PWM_10.txt (K-afal) No PWM Available	Zfp281_0973.2_combined_PWM_10.txt (K-afal) No PWM Available
Zfp281_0973.2_PWM_1.txt (K-afal) 	Zfp281_0973.2_PWM_1.txt (K-afal) 	Zfp281_0973.2_combined_PWM_1.txt (K-afal) 
Zfp281_0973.2_PWM_2.txt (K-afal) 	Zfp281_0973.2_PWM_2.txt (K-afal) 	Zfp281_0973.2_combined_PWM_2.txt (K-afal) 
Zfp281_0973.2_PWM_3.txt (K-afal) 	Zfp281_0973.2_PWM_3.txt (K-afal) 	Zfp281_0973.2_combined_PWM_3.txt (K-afal) No PWM Available
Zfp281_0973.2_PWM_4.txt (K-afal) 	Zfp281_0973.2_PWM_4.txt (K-afal) 	Zfp281_0973.2_combined_PWM_4.txt (K-afal) No PWM Available
Zfp281_0973.2_PWM_5.txt (K-afal) [Z-Beta = 97.3999] [Pearson Full 0.74345801, Pearson Zeroed 0.735979] 	Zfp281_0973.2_PWM_5.txt (K-afal) 	Zfp281_0973.2_combined_PWM_5.txt (K-afal) No PWM Available
Zfp281_0973.2_PWM_6.txt (K-afal) 	Zfp281_0973.2_PWM_6.txt (K-afal) 	Zfp281_0973.2_combined_PWM_6.txt (K-afal) No PWM Available
Zfp281_0973.2_PWM_7.txt (K-afal) 	Zfp281_0973.2_PWM_7.txt (K-afal) 	Zfp281_0973.2_combined_PWM_7.txt (K-afal) No PWM Available
Zfp281_0973.2_PWM_8.txt (K-afal) 	Zfp281_0973.2_PWM_8.txt (K-afal) 	Zfp281_0973.2_combined_PWM_8.txt (K-afal) No PWM Available

Zfp281\_0973.2\_PWM\_9.txt (K-afal)



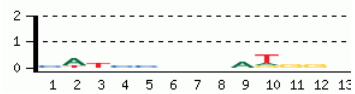
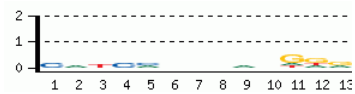

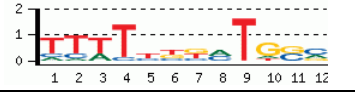
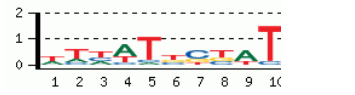
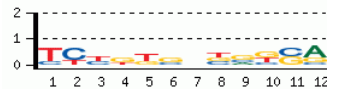
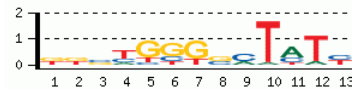


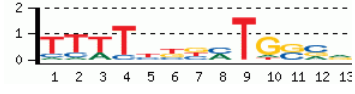
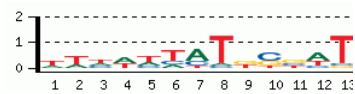
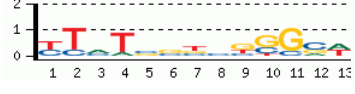





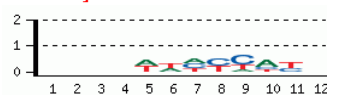





Zfp281\_0973.2\_PWM\_9.txt (K-afal)



Zfp281\_0973.2\_combined\_PWM\_9.txt (K-afal)  
No PWM Available


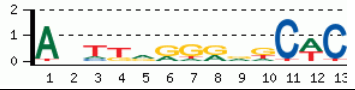

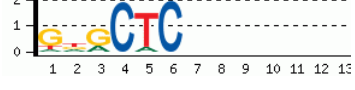
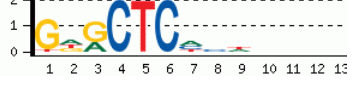
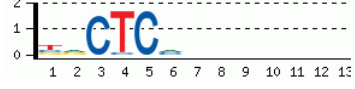
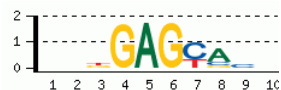



# Zfp410\_3034.2

Array Version 1	Array Version 2	Combined
<p>Zfp410_3034.2_v1_prim_auc.txt (SW)  [Z-Beta = 29.664]  [Pearson Full 0.552753673, Pearson Zeroed 0.551284]</p>	<p>Zfp410_3034.2_v2_prim_auc.txt (SW)</p>	<p>Zfp410_3034.2_combined_prim_auc.txt (SW)</p>
<p>Zfp410_3034.2_v1_prim_ic.txt (SW)</p>	<p>Zfp410_3034.2_v2_prim_ic.txt (SW)</p>	<p>Zfp410_3034.2_combined_prim_ic_trimmed.txt (SW)  [Z-Beta = 6.16698]  [Pearson Full 0.54451305, Pearson Zeroed 0.544071]</p>
<p>Zfp410_3034.2_v1_prim_untrimmed.txt (SW)  [Z-Beta = 50.1515]  [Pearson Full 0.552753673, Pearson Zeroed 0.517377]</p>	<p>Zfp410_3034.2_v2_prim_untrimmed.txt (SW)</p>	<p>Zfp410_3034.2_combined_prim_untrimmed.txt (SW)  [Z-Beta = 516.366]  [Pearson Full 0.54451305, Pearson Zeroed 0.142496]</p>
<p>Zfp410_3034.2_v1_sec_auc.txt (SW)</p>	<p>Zfp410_3034.2_v2_sec_auc.txt (SW)</p>	<p>Zfp410_3034.2_combined_sec_auc.txt (SW)</p>
<p>Zfp410_3034.2_v1_sec_ic.txt (SW)</p>	<p>Zfp410_3034.2_v2_sec_ic.txt (SW)</p>	<p>Zfp410_3034.2_combined_sec_ic_trimmed.txt (SW)  [Z-Beta = 118.261]  [Pearson Full 0.54451305, Pearson Zeroed 0.362847]</p>
<p>Zfp410_3034.2_v1_sec_untrimmed.txt (SW)  [Z-Beta = 6.2954]  [Pearson Full 0.552753673, Pearson Zeroed 0.541389]</p>	<p>Zfp410_3034.2_v2_sec_untrimmed.txt (SW)</p>	<p>Zfp410_3034.2_combined_sec_untrimmed.txt (SW)  [Z-Beta = 50.1118]  [Pearson Full 0.54451305, Pearson Zeroed 0.493085]</p>
<p>Zfp410_3034.2_v1_prim_trimmed.txt (RM8)</p>	<p>Zfp410_3034.2_v2_prim_trimmed.txt (RM8)  [Z-Beta = 0.905913]  [Pearson Full 0.204515771, Pearson Zeroed 0.203093]</p>	<p>Zfp410_3034.2_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Zfp410_3034.2_v1_sec_trimmed.txt (RM8)</p>	<p>Zfp410_3034.2_v2_sec_trimmed.txt (RM8)  [Z-Beta = 14.7462]  [Pearson Full 0.204515771, Pearson Zeroed 0.170753]</p>	<p>Zfp410_3034.2_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Zfp410_3034.2_v1_prim_untrimmed.txt (RM8)</p>	<p>Zfp410_3034.2_v2_prim_untrimmed.txt (RM8)</p>	<p>Zfp410_3034.2_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>

<p>Zfp410_3034.2_v1_sec_untrimmed.txt (RM8)</p> 	<p>Zfp410_3034.2_v2_sec_untrimmed.txt (RM8)</p> 	<p>Zfp410_3034.2_RM8_combined_sec_untrimmed.txt (RM8)</p> <p>No PWM Available</p>
<p>Zfp410_3034.2_v1_RM35_prim_trimmed.txt (RM35)</p> 	<p>Zfp410_3034.2_v2_RM35_prim_trimmed.txt (RM35)</p> <p>[Z-Beta = 14.0044] [Pearson Full 0.204515771, Pearson Zeroed 0.178032]</p> 	<p>Zfp410_3034.2_RM_combined_prim_trimmed.txt (RM35)</p> <p>[Z-Beta = 22.8983] [Pearson Full 0.54451305, Pearson Zeroed 0.535273]</p> 
<p>Zfp410_3034.2_v1_RM35_sec_trimmed.txt (RM35)</p> 	<p>Zfp410_3034.2_v2_RM35_sec_trimmed.txt (RM35)</p> 	<p>Zfp410_3034.2_RM_combined_sec_trimmed.txt (RM35)</p> <p>[Z-Beta = 4.02824] [Pearson Full 0.54451305, Pearson Zeroed 0.544181]</p> 
<p>Zfp410_3034.2_v1_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Zfp410_3034.2_v2_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Zfp410_3034.2_RM_combined_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = 61.3855] [Pearson Full 0.54451305, Pearson Zeroed 0.469228]</p> 
<p>Zfp410_3034.2_v1_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Zfp410_3034.2_v2_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Zfp410_3034.2_RM_combined_sec_untrimmed.txt (RM35)</p> <p>[Z-Beta = 6.41243] [Pearson Full 0.54451305, Pearson Zeroed 0.543638]</p> 
<p>Zfp410_3034.2_PWM_1.txt (K-afal)</p> 	<p>Zfp410_3034.2_PWM_1.txt (K-afal)</p> <p>[Z-Beta = -15.3135] [Pearson Full 0.204515771, Pearson Zeroed 0.157235]</p> 	<p>Zfp410_3034.2_combined_PWM_1.txt (K-afal)</p> <p>[Z-Beta = 7.95692] [Pearson Full 0.54451305, Pearson Zeroed 0.543134]</p> 
<p>Zfp410_3034.2_PWM_2.txt (K-afal)</p> <p>[Z-Beta = 60.427] [Pearson Full 0.552753673, Pearson Zeroed 0.396351]</p> 	<p>Zfp410_3034.2_PWM_2.txt (K-afal)</p> <p>No PWM Available</p>	<p>Zfp410_3034.2_combined_PWM_2.txt (K-afal)</p> <p>[Z-Beta = 0.867863] [Pearson Full 0.54451305, Pearson Zeroed 0.544501]</p> 
<p>Zfp410_3034.2_PWM_3.txt (K-afal)</p> 	<p>Zfp410_3034.2_PWM_3.txt (K-afal)</p> <p>No PWM Available</p>	<p>Zfp410_3034.2_combined_PWM_3.txt (K-afal)</p> <p>[Z-Beta = 7.18938] [Pearson Full 0.54451305, Pearson Zeroed 0.54339]</p> 
<p>Zfp410_3034.2_PWM_4.txt (K-afal)</p> <p>[Z-Beta = 19.2136] [Pearson Full 0.552753673, Pearson Zeroed 0.534441]</p> 	<p>Zfp410_3034.2_PWM_4.txt (K-afal)</p> <p>No PWM Available</p>	<p>Zfp410_3034.2_combined_PWM_4.txt (K-afal)</p> <p>[Z-Beta = 32.5344] [Pearson Full 0.54451305, Pearson Zeroed 0.531594]</p> 

# Zfp691\_0895.2

Array Version 1	Array Version 2	Combined
<p>Zfp691_0895.2_v1_prim_auc.txt (SW)</p>	<p>Zfp691_0895.2_v2_prim_auc.txt (SW)  [Z-Beta = 35.5049]  [Pearson Full 0.722369786, Pearson Zeroed 0.705158]</p>	<p>Zfp691_0895.2_combined_prim_auc.txt (SW)  No PWM Available</p>
<p>Zfp691_0895.2_v1_prim_ic.txt (SW)  [Z-Beta = 130.102]  [Pearson Full 0.677111195, Pearson Zeroed 0.449936]</p>	<p>Zfp691_0895.2_v2_prim_ic.txt (SW)</p>	<p>Zfp691_0895.2_combined_prim_ic_trimmed.txt (SW)  [Z-Beta = 11434.2]  [Pearson Full 0.775939751, Pearson Zeroed 0.367253]</p>
<p>Zfp691_0895.2_v1_prim_untrimmed.txt (SW)</p>	<p>Zfp691_0895.2_v2_prim_untrimmed.txt (SW)</p>	<p>Zfp691_0895.2_combined_prim_untrimmed.txt (SW)  [Z-Beta = 20.9598]  [Pearson Full 0.775939751, Pearson Zeroed 0.775195]</p>
<p>Zfp691_0895.2_v1_sec_auc.txt (SW)</p>	<p>Zfp691_0895.2_v2_sec_auc.txt (SW)</p>	<p>Zfp691_0895.2_combined_sec_auc.txt (SW)</p>
<p>Zfp691_0895.2_v1_sec_ic.txt (SW)</p>	<p>Zfp691_0895.2_v2_sec_ic.txt (SW)</p>	<p>Zfp691_0895.2_combined_sec_ic_trimmed.txt (SW)  [Z-Beta = 9.75694]  [Pearson Full 0.775939751, Pearson Zeroed 0.775656]</p>
<p>Zfp691_0895.2_v1_sec_untrimmed.txt (SW)</p>	<p>Zfp691_0895.2_v2_sec_untrimmed.txt (SW)</p>	<p>Zfp691_0895.2_combined_sec_untrimmed.txt (SW)  [Z-Beta = 22.0527]  [Pearson Full 0.775939751, Pearson Zeroed 0.774468]</p>
<p>Zfp691_0895.2_v1_prim_trimmed.txt (RM8)</p>	<p>Zfp691_0895.2_v2_prim_trimmed.txt (RM8)  [Z-Beta = 3.99346]  [Pearson Full 0.722369786, Pearson Zeroed 0.721449]</p>	<p>Zfp691_0895.2_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Zfp691_0895.2_v1_sec_trimmed.txt (RM8)</p>	<p>Zfp691_0895.2_v2_sec_trimmed.txt (RM8)</p>	<p>Zfp691_0895.2_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Zfp691_0895.2_v1_prim_untrimmed.txt (RM8)</p>	<p>Zfp691_0895.2_v2_prim_untrimmed.txt (RM8)</p>	<p>Zfp691_0895.2_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Zfp691_0895.2_v1_sec_untrimmed.txt (RM8)</p>	<p>Zfp691_0895.2_v2_sec_untrimmed.txt (RM8)</p>	<p>Zfp691_0895.2_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>

		
<p>Zfp691_0895.2_v1_RM35_prim_trimmed.txt (RM35)</p> 	<p>Zfp691_0895.2_v2_RM35_prim_trimmed.txt (RM35)</p> 	<p>Zfp691_0895.2_RM_combined_prim_trimmed.txt (RM35)</p> <p>[Z-Beta = -15.7535] [Pearson Full 0.775939751, Pearson Zeroed 0.775506]</p> 
<p>Zfp691_0895.2_v1_RM35_sec_trimmed.txt (RM35)</p> 	<p>Zfp691_0895.2_v2_RM35_sec_trimmed.txt (RM35)</p> <p>[Z-Beta = 2.24195] [Pearson Full 0.722369786, Pearson Zeroed 0.72174]</p> 	<p>Zfp691_0895.2_RM_combined_sec_trimmed.txt (RM35)</p> <p>[Z-Beta = 74.6767] [Pearson Full 0.775939751, Pearson Zeroed 0.763738]</p> 
<p>Zfp691_0895.2_v1_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Zfp691_0895.2_v2_RM35_prim_untrimmed.txt (RM35)</p> 	<p>Zfp691_0895.2_RM_combined_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = 90.7445] [Pearson Full 0.775939751, Pearson Zeroed 0.757293]</p> 
<p>Zfp691_0895.2_v1_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Zfp691_0895.2_v2_RM35_sec_untrimmed.txt (RM35)</p> <p>[Z-Beta = 51.7943] [Pearson Full 0.722369786, Pearson Zeroed 0.695389]</p> 	<p>Zfp691_0895.2_RM_combined_sec_untrimmed.txt (RM35)</p> <p>[Z-Beta = 57.9526] [Pearson Full 0.775939751, Pearson Zeroed 0.768627]</p> 
<p>Zfp691_0895.2_PWM_1.txt (K-afal)</p> <p>[Z-Beta = 59.0076] [Pearson Full 0.677111195, Pearson Zeroed 0.553848]</p> 	<p>Zfp691_0895.2_PWM_1.txt (K-afal)</p> <p>[Z-Beta = 57.4762] [Pearson Full 0.722369786, Pearson Zeroed 0.675331]</p> 	<p>Zfp691_0895.2_combined_PWM_1.txt (K-afal)</p> <p>[Z-Beta = 108.539] [Pearson Full 0.775939751, Pearson Zeroed 0.757031]</p> 
<p>Zfp691_0895.2_PWM_2.txt (K-afal)</p> 	<p>Zfp691_0895.2_PWM_2.txt (K-afal)</p> 	<p>Zfp691_0895.2_combined_PWM_2.txt (K-afal)</p> <p>[Z-Beta = 70.6528] [Pearson Full 0.775939751, Pearson Zeroed 0.771416]</p> 
<p>Zfp691_0895.2_PWM_3.txt (Kafal)</p> <p>No PWM Available</p>	<p>Zfp691_0895.2_PWM_3.txt (Kafal)</p> <p>[Z-Beta = 58.4473] [Pearson Full 0.722369786, Pearson Zeroed 0.6897]</p> 	<p>Zfp691_0895.2_combined_PWM_3.txt (Kafal)</p> <p>[Z-Beta = 57.6108] [Pearson Full 0.775939751, Pearson Zeroed 0.769672]</p> 
<p>Zfp691_0895.2_PWM_4.txt (Kafal)</p> <p>No PWM Available</p>	<p>Zfp691_0895.2_PWM_4.txt (Kafal)</p> <p>[Z-Beta = 236.277] [Pearson Full 0.722369786, Pearson Zeroed 0.575159]</p> 	<p>Zfp691_0895.2_combined_PWM_4.txt (Kafal)</p> <p>[Z-Beta = 6.53266] [Pearson Full 0.775939751, Pearson Zeroed 0.775847]</p> 

Zfp691\_0895.2\_PWM\_5.txt (Kafal)  
No PWM Available



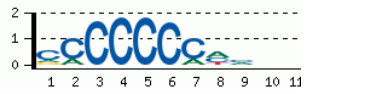

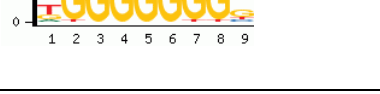
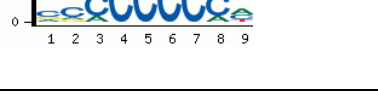
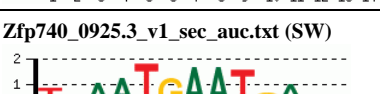
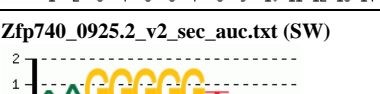
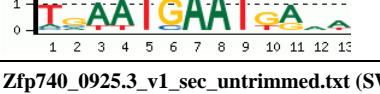


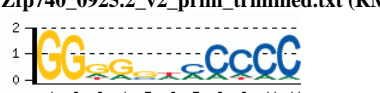
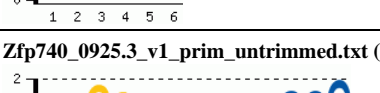
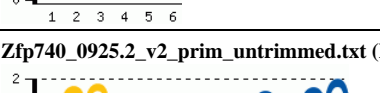
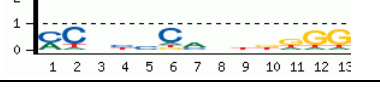
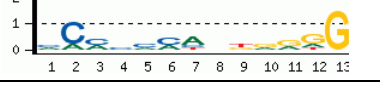
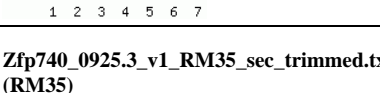
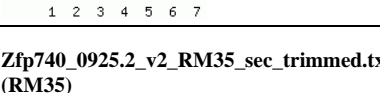


Zfp691\_0895.2\_PWM\_5.txt (Kafal)  
[Z-Beta = 71.6336]  
[Pearson Full 0.722369786, Pearson Zeroed  
0.720157]

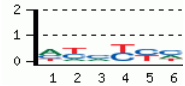
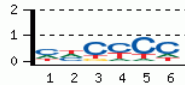


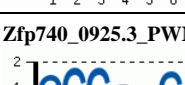
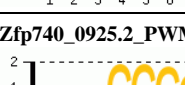
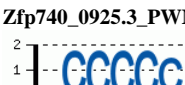
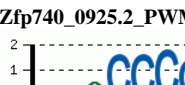


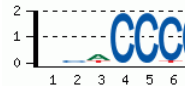
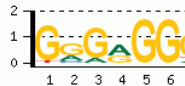
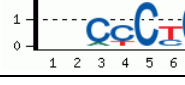

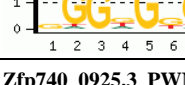
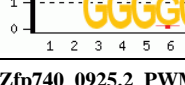

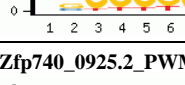

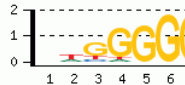



Zfp691\_0895.2\_combined\_PWM\_5.txt (Kafal)  
No PWM Available



# Zfp740\_0925.2

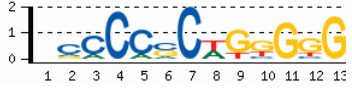


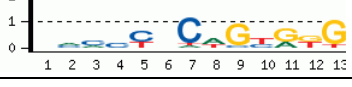
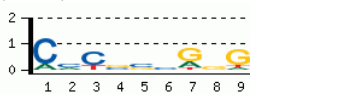



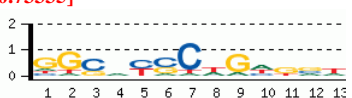




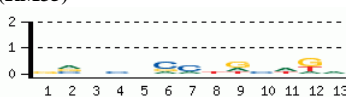

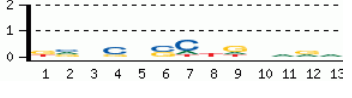





Array Version 1	Array Version 2	Combined
Zfp740_0925.3_v1_prim_auc.txt (SW) 	Zfp740_0925.2_v2_prim_auc.txt (SW) 	Zfp740_0925.2_combined_prim_auc.txt (SW) 
Zfp740_0925.3_v1_prim_ic.txt (SW) [Z-Beta = 122.865] [Pearson Full 0.790336195, Pearson Zeroed 0.749459] 	Zfp740_0925.2_v2_prim_ic.txt (SW) 	Zfp740_0925.2_combined_prim_ic_trimmed.txt (SW) 
Zfp740_0925.3_v1_prim_untrimmed.txt (SW) 	Zfp740_0925.2_v2_prim_untrimmed.txt (SW) 	Zfp740_0925.2_combined_prim_untrimmed.txt (SW) No PWM Available
Zfp740_0925.3_v1_sec_auc.txt (SW) 	Zfp740_0925.2_v2_sec_auc.txt (SW) 	Zfp740_0925.2_combined_sec_auc.txt (SW) No PWM Available
Zfp740_0925.3_v1_sec_ic.txt (SW) 	Zfp740_0925.2_v2_sec_ic.txt (SW) 	Zfp740_0925.2_combined_sec_ic_trimmed.txt (SW) No PWM Available
Zfp740_0925.3_v1_sec_untrimmed.txt (SW) 	Zfp740_0925.2_v2_sec_untrimmed.txt (SW) 	Zfp740_0925.2_combined_sec_untrimmed.txt (SW) No PWM Available
Zfp740_0925.3_v1_prim_trimmed.txt (RM8) 	Zfp740_0925.2_v2_prim_trimmed.txt (RM8) 	Zfp740_0925.2_RM8_combined_prim_trimmed.txt (RM8) No PWM Available
Zfp740_0925.3_v1_sec_trimmed.txt (RM8) 	Zfp740_0925.2_v2_sec_trimmed.txt (RM8) 	Zfp740_0925.2_RM8_combined_sec_trimmed.txt (RM8) No PWM Available
Zfp740_0925.3_v1_prim_untrimmed.txt (RM8) 	Zfp740_0925.2_v2_prim_untrimmed.txt (RM8) 	Zfp740_0925.2_RM8_combined_prim_untrimmed.txt (RM8) No PWM Available
Zfp740_0925.3_v1_sec_untrimmed.txt (RM8) 	Zfp740_0925.2_v2_sec_untrimmed.txt (RM8) 	Zfp740_0925.2_RM8_combined_sec_untrimmed.txt (RM8) No PWM Available
Zfp740_0925.3_v1_RM35_prim_trimmed.txt (RM35) 	Zfp740_0925.2_v2_RM35_prim_trimmed.txt (RM35) 	Zfp740_0925.2_RM_combined_prim_trimmed.txt (RM35) No PWM Available
Zfp740_0925.3_v1_RM35_sec_trimmed.txt (RM35) 	Zfp740_0925.2_v2_RM35_sec_trimmed.txt (RM35) 	Zfp740_0925.2_RM_combined_sec_trimmed.txt (RM35) No PWM Available

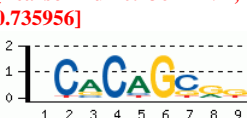
 <p>Zfp740_0925.3_v1_RM35_prim_untrimmed.txt (RM35) [Z-Beta = 304.251] [Pearson Full 0.790336195, Pearson Zeroed 0.682368]</p>	 <p>Zfp740_0925.2_v2_RM35_prim_untrimmed.txt (RM35)</p>	Zfp740_0925.2_RM_combined_prim_untrimmed.txt (RM35) No PWM Available
 <p>Zfp740_0925.3_v1_RM35_sec_untrimmed.txt (RM35)</p>	 <p>Zfp740_0925.2_v2_RM35_sec_untrimmed.txt (RM35)</p>	Zfp740_0925.2_RM_combined_sec_untrimmed.txt (RM35) No PWM Available
 <p>Zfp740_0925.3_PWM_1.txt (K-afal)</p>	 <p>Zfp740_0925.2_PWM_1.txt (K-afal)</p>	Zfp740_0925.2_combined_PWM_1.txt (K-afal) No PWM Available
 <p>Zfp740_0925.3_PWM_2.txt (K-afal)</p>	 <p>Zfp740_0925.2_PWM_2.txt (K-afal)</p>	Zfp740_0925.2_combined_PWM_2.txt (K-afal) No PWM Available
 <p>Zfp740_0925.3_PWM_3.txt (K-afal)</p>	 <p>Zfp740_0925.2_PWM_3.txt (K-afal)</p>	Zfp740_0925.2_combined_PWM_3.txt (K-afal) No PWM Available
 <p>Zfp740_0925.3_PWM_4.txt (K-afal)</p>	 <p>Zfp740_0925.2_PWM_4.txt (K-afal)</p>	Zfp740_0925.2_combined_PWM_4.txt (K-afal) No PWM Available
 <p>Zfp740_0925.3_PWM_5.txt (K-afal)</p>	 <p>Zfp740_0925.2_PWM_5.txt (K-afal)</p>	Zfp740_0925.2_combined_PWM_5.txt (K-afal) No PWM Available
 <p>Zfp740_0925.3_PWM_6.txt (K-afal)</p>	 <p>Zfp740_0925.2_PWM_6.txt (K-afal)</p>	Zfp740_0925.2_combined_PWM_6.txt (K-afal) No PWM Available
 <p>Zfp740_0925.3_PWM_7.txt (K-afal)</p>	 <p>Zfp740_0925.2_PWM_7.txt (K-afal)</p>	Zfp740_0925.2_combined_PWM_7.txt (K-afal) No PWM Available
Zfp740_0925.2_PWM_8.txt (Kafal) No PWM Available	 <p>Zfp740_0925.2_PWM_8.txt (Kafal)</p>	Zfp740_0925.2_combined_PWM_8.txt (Kafal) No PWM Available
Zfp740_0925.2_PWM_9.txt (Kafal) No PWM Available	 <p>Zfp740_0925.2_PWM_9.txt (Kafal)</p>	Zfp740_0925.2_combined_PWM_9.txt (Kafal) No PWM Available
Zfp740_0925.2_PWM_10.txt (Kafal) No PWM Available	 <p>Zfp740_0925.2_PWM_10.txt (Kafal)</p>	Zfp740_0925.2_combined_PWM_10.txt (Kafal) No PWM Available

<p>Zfp740_0925.2_PWM_11.txt (Kafal) No PWM Available</p>	<p>Zfp740_0925.2_PWM_11.txt (Kafal)</p> 	<p>Zfp740_0925.2_combined_PWM_11.txt (Kafal) No PWM Available</p>
<p>Zfp740_0925.2_PWM_12.txt (Kafal) No PWM Available</p>	<p>Zfp740_0925.2_PWM_12.txt (Kafal)</p> 	<p>Zfp740_0925.2_combined_PWM_12.txt (Kafal) No PWM Available</p>
<p>Zfp740_0925.2_PWM_13.txt (Kafal) No PWM Available</p>	<p>Zfp740_0925.2_PWM_13.txt (Kafal)</p> 	<p>Zfp740_0925.2_combined_PWM_13.txt (Kafal) No PWM Available</p>
<p>Zfp740_0925.2_PWM_14.txt (Kafal) No PWM Available</p>	<p>Zfp740_0925.2_PWM_14.txt (Kafal)</p> 	<p>Zfp740_0925.2_combined_PWM_14.txt (Kafal) No PWM Available</p>
<p>Zfp740_0925.2_PWM_15.txt (Kafal) No PWM Available</p>	<p>Zfp740_0925.2_PWM_15.txt (Kafal)</p> 	<p>Zfp740_0925.2_combined_PWM_15.txt (Kafal) No PWM Available</p>
<p>Zfp740_0925.2_PWM_16.txt (Kafal) No PWM Available</p>	<p>Zfp740_0925.2_PWM_16.txt (Kafal)</p> 	<p>Zfp740_0925.2_combined_PWM_16.txt (Kafal) No PWM Available</p>
<p>Zfp740_0925.2_PWM_17.txt (Kafal) No PWM Available</p>	<p>Zfp740_0925.2_PWM_17.txt (Kafal)</p> 	<p>Zfp740_0925.2_combined_PWM_17.txt (Kafal) No PWM Available</p>
<p>Zfp740_0925.2_PWM_18.txt (Kafal) No PWM Available</p>	<p>Zfp740_0925.2_PWM_18.txt (Kafal)</p> 	<p>Zfp740_0925.2_combined_PWM_18.txt (Kafal) No PWM Available</p>

# Zic1\_0991.2

Array Version 1	Array Version 2	Combined
<p>Zic1_0991.2_v1_prim_auc.txt (SW) [Z-Beta = 31.7764] [Pearson Full 0.589871686, Pearson Zeroed 0.554602]</p>	<p>Zic1_0991.2_v2_prim_auc.txt (SW) [Z-Beta = 89.4479] [Pearson Full 0.736441494, Pearson Zeroed 0.724951]</p>	<p>Zic1_0991.2_combined_prim_auc.txt (SW)</p>
<p>Zic1_0991.2_v1_prim_ic.txt (SW)</p>	<p>Zic1_0991.2_v2_prim_ic.txt (SW)</p>	<p>Zic1_0991.2_combined_prim_ic_trimmed.txt (SW) [Z-Beta = 4201.61] [Pearson Full 0.722802917, Pearson Zeroed 0.146221]</p>
<p>Zic1_0991.2_v1_prim_untrimmed.txt (SW)</p>	<p>Zic1_0991.2_v2_prim_untrimmed.txt (SW) [Z-Beta = -150.053] [Pearson Full 0.736441494, Pearson Zeroed 0.707749]</p>	<p>Zic1_0991.2_combined_prim_untrimmed.txt (SW) [Z-Beta = 585.851] [Pearson Full 0.722802917, Pearson Zeroed 0.164727]</p>
<p>Zic1_0991.2_v1_sec_auc.txt (SW)</p>	<p>Zic1_0991.2_v2_sec_auc.txt (SW) [Z-Beta = 107.82] [Pearson Full 0.736441494, Pearson Zeroed 0.714055]</p>	<p>Zic1_0991.2_combined_sec_auc.txt (SW)</p>
<p>Zic1_0991.2_v1_sec_ic.txt (SW)</p>	<p>Zic1_0991.2_v2_sec_ic.txt (SW) [Z-Beta = 24.3219] [Pearson Full 0.736441494, Pearson Zeroed 0.735446]</p>	<p>Zic1_0991.2_combined_sec_ic_trimmed.txt (SW) [Z-Beta = 78.2661] [Pearson Full 0.722802917, Pearson Zeroed 0.711088]</p>
<p>Zic1_0991.2_v1_sec_untrimmed.txt (SW)</p>	<p>Zic1_0991.2_v2_sec_untrimmed.txt (SW) [Z-Beta = 55.9351] [Pearson Full 0.736441494, Pearson Zeroed 0.731735]</p>	<p>Zic1_0991.2_combined_sec_untrimmed.txt (SW) [Z-Beta = 69.3454] [Pearson Full 0.722802917, Pearson Zeroed 0.713514]</p>
<p>Zic1_0991.2_v1_prim_trimmed.txt (RM8)</p>	<p>Zic1_0991.2_v2_prim_trimmed.txt (RM8) [Z-Beta = -32.7054] [Pearson Full 0.736441494, Pearson Zeroed 0.734547]</p>	<p>Zic1_0991.2_RM8_combined_prim_trimmed.txt (RM8) No PWM Available</p>
<p>Zic1_0991.2_v1_sec_trimmed.txt (RM8)</p>	<p>Zic1_0991.2_v2_sec_trimmed.txt (RM8) [Z-Beta = 71.1313] [Pearson Full 0.736441494, Pearson Zeroed 0.727899]</p>	<p>Zic1_0991.2_RM8_combined_sec_trimmed.txt (RM8) No PWM Available</p>

<p>Zic1_0991.2_v1_prim_untrimmed.txt (RM8)</p> 	<p>Zic1_0991.2_v2_prim_untrimmed.txt (RM8)  [Z-Beta = -150.053]  [Pearson Full 0.736441494, Pearson Zeroed 0.707749]</p> 	<p>Zic1_0991.2_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>
<p>Zic1_0991.2_v1_sec_untrimmed.txt (RM8)</p> 	<p>Zic1_0991.2_v2_sec_untrimmed.txt (RM8)  [Z-Beta = 55.9351]  [Pearson Full 0.736441494, Pearson Zeroed 0.731735]</p> 	<p>Zic1_0991.2_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>
<p>Zic1_0991.2_v1_RM35_prim_trimmed.txt (RM35)</p> 	<p>Zic1_0991.2_v2_RM35_prim_trimmed.txt (RM35)  [Z-Beta = 182.703]  [Pearson Full 0.736441494, Pearson Zeroed 0.676079]</p> 	<p>Zic1_0991.2_RM_combined_prim_trimmed.txt (RM35)  [Z-Beta = -47.3947]  [Pearson Full 0.722802917, Pearson Zeroed 0.721055]</p> 
<p>Zic1_0991.2_v1_RM35_sec_trimmed.txt (RM35)</p> 	<p>Zic1_0991.2_v2_RM35_sec_trimmed.txt (RM35)  [Z-Beta = 42.3019]  [Pearson Full 0.736441494, Pearson Zeroed 0.73335]</p> 	<p>Zic1_0991.2_RM_combined_sec_trimmed.txt (RM35)  [Z-Beta = 137.201]  [Pearson Full 0.722802917, Pearson Zeroed 0.690607]</p> 
<p>Zic1_0991.2_v1_RM35_prim_untrimmed.txt (RM35)  [Z-Beta = 145.918]  [Pearson Full 0.589871686, Pearson Zeroed 0.496797]</p> 	<p>Zic1_0991.2_v2_RM35_prim_untrimmed.txt (RM35)  [Z-Beta = 11.3483]  [Pearson Full 0.736441494, Pearson Zeroed 0.736308]</p> 	<p>Zic1_0991.2_RM_combined_prim_untrimmed.txt (RM35)  [Z-Beta = 412.37]  [Pearson Full 0.722802917, Pearson Zeroed 0.403837]</p> 
<p>Zic1_0991.2_v1_RM35_sec_untrimmed.txt (RM35)</p> 	<p>Zic1_0991.2_v2_RM35_sec_untrimmed.txt (RM35)  [Z-Beta = -23.2132]  [Pearson Full 0.736441494, Pearson Zeroed 0.735361]</p> 	<p>Zic1_0991.2_RM_combined_sec_untrimmed.txt (RM35)  [Z-Beta = -16.4044]  [Pearson Full 0.722802917, Pearson Zeroed 0.722327]</p> 
<p>Zic1_0991.2_PWM_10.txt (K-afal)</p> 	<p>Zic1_0991.2_PWM_10.txt (K-afal)  [Z-Beta = 217.283]  [Pearson Full 0.736441494, Pearson Zeroed 0.641363]</p> 	<p>Zic1_0991.2_combined_PWM_10.txt (K-afal)  No PWM Available</p>
<p>Zic1_0991.2_PWM_1.txt (K-afal)</p> 	<p>Zic1_0991.2_PWM_1.txt (K-afal)  [Z-Beta = 82.1121]  [Pearson Full 0.736441494, Pearson Zeroed 0.725159]</p> 	<p>Zic1_0991.2_combined_PWM_1.txt (K-afal)  [Z-Beta = -64.2176]  [Pearson Full 0.722802917, Pearson Zeroed 0.72023]</p> 

<p>Zic1_0991.2_PWM_2.txt (K-afal)</p> 	<p>Zic1_0991.2_PWM_2.txt (K-afal)  [Z-Beta = 108.613]  [Pearson Full 0.736441494, Pearson Zeroed 0.718271]</p> 	<p>Zic1_0991.2_combined_PWM_2.txt (K-afal)  [Z-Beta = 288.219]  [Pearson Full 0.722802917, Pearson Zeroed 0.57427]</p> 
<p>Zic1_0991.2_PWM_3.txt (K-afal)</p> 	<p>Zic1_0991.2_PWM_3.txt (K-afal)</p> 	<p>Zic1_0991.2_combined_PWM_3.txt (K-afal)  [Z-Beta = 9.10548]  [Pearson Full 0.722802917, Pearson Zeroed 0.722704]</p> 
<p>Zic1_0991.2_PWM_4.txt (K-afal)</p> 	<p>Zic1_0991.2_PWM_4.txt (K-afal)  [Z-Beta = 147.719]  [Pearson Full 0.736441494, Pearson Zeroed 0.692075]</p> 	<p>Zic1_0991.2_combined_PWM_4.txt (K-afal)  No PWM Available</p>
<p>Zic1_0991.2_PWM_5.txt (K-afal)  [Z-Beta = 74.23]  [Pearson Full 0.589871686, Pearson Zeroed 0.579124]</p> 	<p>Zic1_0991.2_PWM_5.txt (K-afal)  [Z-Beta = 64.2238]  [Pearson Full 0.736441494, Pearson Zeroed 0.730375]</p> 	<p>Zic1_0991.2_combined_PWM_5.txt (K-afal)  No PWM Available</p>
<p>Zic1_0991.2_PWM_6.txt (K-afal)  [Z-Beta = 78.3752]  [Pearson Full 0.589871686, Pearson Zeroed 0.546393]</p> 	<p>Zic1_0991.2_PWM_6.txt (K-afal)  [Z-Beta = 73.0984]  [Pearson Full 0.736441494, Pearson Zeroed 0.727281]</p> 	<p>Zic1_0991.2_combined_PWM_6.txt (K-afal)  No PWM Available</p>
<p>Zic1_0991.2_PWM_7.txt (K-afal)</p> 	<p>Zic1_0991.2_PWM_7.txt (K-afal)  [Z-Beta = -55.1471]  [Pearson Full 0.736441494, Pearson Zeroed 0.731106]</p> 	<p>Zic1_0991.2_combined_PWM_7.txt (K-afal)  No PWM Available</p>
<p>Zic1_0991.2_PWM_8.txt (K-afal)</p> 	<p>Zic1_0991.2_PWM_8.txt (K-afal)  [Z-Beta = 29.3211]  [Pearson Full 0.736441494, Pearson Zeroed 0.735399]</p> 	<p>Zic1_0991.2_combined_PWM_8.txt (K-afal)  No PWM Available</p>
<p>Zic1_0991.2_PWM_9.txt (K-afal)</p> 	<p>Zic1_0991.2_PWM_9.txt (K-afal)  [Z-Beta = -14.5142]  [Pearson Full 0.736441494, Pearson Zeroed 0.735956]</p> 	<p>Zic1_0991.2_combined_PWM_9.txt (K-afal)  No PWM Available</p>
<p>Zic1_0991.2_PWM_10.txt (Kafal)  No PWM Available</p>	<p>Zic1_0991.2_PWM_10.txt (Kafal)  [Z-Beta = 217.283]  [Pearson Full 0.736441494, Pearson Zeroed 0.641363]</p> 	<p>Zic1_0991.2_combined_PWM_10.txt (Kafal)  No PWM Available</p>

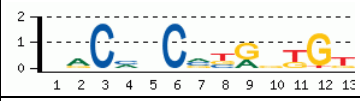
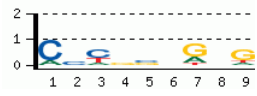

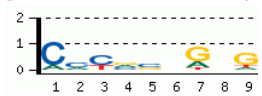

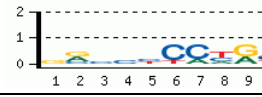
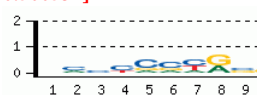

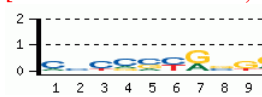
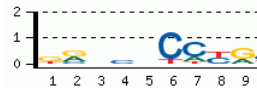

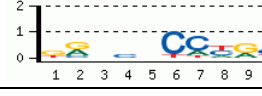




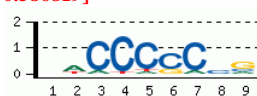




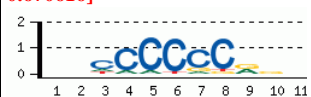


<p>Zic1_0991.2_PWM_11.txt (Kafal) No PWM Available</p>	<p>Zic1_0991.2_PWM_11.txt (Kafal) [Z-Beta = 80.228] [Pearson Full 0.736441494, Pearson Zeroed 0.724247]</p> 	<p>Zic1_0991.2_combined_PWM_11.txt (Kafal) No PWM Available</p>
<p>Zic1_0991.2_PWM_12.txt (Kafal) No PWM Available</p>	<p>Zic1_0991.2_PWM_12.txt (Kafal) [Z-Beta = 24.9839] [Pearson Full 0.736441494, Pearson Zeroed 0.735404]</p> 	<p>Zic1_0991.2_combined_PWM_12.txt (Kafal) No PWM Available</p>
<p>Zic1_0991.2_PWM_13.txt (Kafal) No PWM Available</p>	<p>Zic1_0991.2_PWM_13.txt (Kafal) [Z-Beta = 89.7718] [Pearson Full 0.736441494, Pearson Zeroed 0.720388]</p> 	<p>Zic1_0991.2_combined_PWM_13.txt (Kafal) No PWM Available</p>
<p>Zic1_0991.2_PWM_14.txt (Kafal) No PWM Available</p>	<p>Zic1_0991.2_PWM_14.txt (Kafal) [Z-Beta = 65.5807] [Pearson Full 0.736441494, Pearson Zeroed 0.728306]</p> 	<p>Zic1_0991.2_combined_PWM_14.txt (Kafal) No PWM Available</p>
<p>Zic1_0991.2_PWM_15.txt (Kafal) No PWM Available</p>	<p>Zic1_0991.2_PWM_15.txt (Kafal) [Z-Beta = 12.7461] [Pearson Full 0.736441494, Pearson Zeroed 0.736286]</p> 	<p>Zic1_0991.2_combined_PWM_15.txt (Kafal) No PWM Available</p>
<p>Zic1_0991.2_PWM_16.txt (Kafal) No PWM Available</p>	<p>Zic1_0991.2_PWM_16.txt (Kafal)</p> 	<p>Zic1_0991.2_combined_PWM_16.txt (Kafal) No PWM Available</p>
<p>Zic1_0991.2_PWM_17.txt (Kafal) No PWM Available</p>	<p>Zic1_0991.2_PWM_17.txt (Kafal)</p> 	<p>Zic1_0991.2_combined_PWM_17.txt (Kafal) No PWM Available</p>





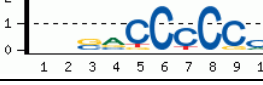



# Zic2\_2895.2

Array Version 1	Array Version 2	Combined
Zic2_2895.2_v1_prim_auc.txt (SW) 	Zic2_2895.2_v2_prim_auc.txt (SW) 	Zic2_2895.2_combined_prim_auc.txt (SW) 
Zic2_2895.2_v1_prim_ic.txt (SW) 	Zic2_2895.2_v2_prim_ic.txt (SW) 	Zic2_2895.2_combined_prim_ic_trimmed.txt (SW) [Z-Beta = -170.932] [Pearson Full 0.747920282, Pearson Zeroed 0.733211] 
Zic2_2895.2_v1_prim_untrimmed.txt (SW) 	Zic2_2895.2_v2_prim_untrimmed.txt (SW) 	Zic2_2895.2_combined_prim_untrimmed.txt (SW) [Z-Beta = 171.617] [Pearson Full 0.747920282, Pearson Zeroed 0.725426] 
Zic2_2895.2_v1_sec_auc.txt (SW) 	Zic2_2895.2_v2_sec_auc.txt (SW) 	Zic2_2895.2_combined_sec_auc.txt (SW) 
Zic2_2895.2_v1_sec_ic.txt (SW) 	Zic2_2895.2_v2_sec_ic.txt (SW) 	Zic2_2895.2_combined_sec_ic_trimmed.txt (SW) [Z-Beta = 52.1788] [Pearson Full 0.747920282, Pearson Zeroed 0.745494] 
Zic2_2895.2_v1_sec_untrimmed.txt (SW) 	Zic2_2895.2_v2_sec_untrimmed.txt (SW) [Z-Beta = 0.474127] [Pearson Full 0.668009684, Pearson Zeroed 0.66775] 	Zic2_2895.2_combined_sec_untrimmed.txt (SW) [Z-Beta = 107] [Pearson Full 0.747920282, Pearson Zeroed 0.73731] 
Zic2_2895.2_v1_prim_trimmed.txt (RM8) 	Zic2_2895.2_v2_prim_trimmed.txt (RM8) 	Zic2_2895.2_RM8_combined_prim_trimmed.txt (RM8) No PWM Available
Zic2_2895.2_v1_sec_trimmed.txt (RM8) 	Zic2_2895.2_v2_sec_trimmed.txt (RM8) 	Zic2_2895.2_RM8_combined_sec_trimmed.txt (RM8) No PWM Available
Zic2_2895.2_v1_prim_untrimmed.txt (RM8) [Z-Beta = 4.38982] [Pearson Full 0.601689067, Pearson Zeroed 0.599592] 	Zic2_2895.2_v2_prim_untrimmed.txt (RM8) 	Zic2_2895.2_RM8_combined_prim_untrimmed.txt (RM8) No PWM Available
Zic2_2895.2_v1_sec_untrimmed.txt (RM8) 	Zic2_2895.2_v2_sec_untrimmed.txt (RM8) [Z-Beta = 0.474127] [Pearson Full 0.668009684, Pearson Zeroed 0.66775] 	Zic2_2895.2_RM8_combined_sec_untrimmed.txt (RM8) No PWM Available

		
Zic2_2895.2_v1_RM35_prim_trimmed.txt (RM35) 	Zic2_2895.2_v2_RM35_prim_trimmed.txt (RM35) [Z-Beta = 67.1743] [Pearson Full 0.668009684, Pearson Zeroed 0.659121] 	Zic2_2895.2_RM_combined_prim_trimmed.txt (RM35) [Z-Beta = -61.2617] [Pearson Full 0.747920282, Pearson Zeroed 0.746362] 
Zic2_2895.2_v1_RM35_sec_trimmed.txt (RM35) 	Zic2_2895.2_v2_RM35_sec_trimmed.txt (RM35) 	Zic2_2895.2_RM_combined_sec_trimmed.txt (RM35) [Z-Beta = 122.422] [Pearson Full 0.747920282, Pearson Zeroed 0.739103] 
Zic2_2895.2_v1_RM35_prim_untrimmed.txt (RM35) [Z-Beta = 162.916] [Pearson Full 0.601689067, Pearson Zeroed 0.500534] 	Zic2_2895.2_v2_RM35_prim_untrimmed.txt (RM35) 	Zic2_2895.2_RM_combined_prim_untrimmed.txt (RM35) [Z-Beta = 545.706] [Pearson Full 0.747920282, Pearson Zeroed 0.493302] 
Zic2_2895.2_v1_RM35_sec_untrimmed.txt (RM35) 	Zic2_2895.2_v2_RM35_sec_untrimmed.txt (RM35) 	Zic2_2895.2_RM_combined_sec_untrimmed.txt (RM35) [Z-Beta = 66.0873] [Pearson Full 0.747920282, Pearson Zeroed 0.744964] 
Zic2_2895.2_PWM_10.txt (K-afal) 	Zic2_2895.2_PWM_10.txt (K-afal) 	Zic2_2895.2_combined_PWM_10.txt (K-afal) No PWM Available
Zic2_2895.2_PWM_11.txt (K-afal) 	Zic2_2895.2_PWM_11.txt (K-afal) [Z-Beta = 61.6386] [Pearson Full 0.668009684, Pearson Zeroed 0.666207] 	Zic2_2895.2_combined_PWM_11.txt (K-afal) No PWM Available
Zic2_2895.2_PWM_12.txt (K-afal) [Z-Beta = 94.9697] [Pearson Full 0.601689067, Pearson Zeroed 0.586819] 	Zic2_2895.2_PWM_12.txt (K-afal) 	Zic2_2895.2_combined_PWM_12.txt (K-afal) No PWM Available
Zic2_2895.2_PWM_13.txt (K-afal) 	Zic2_2895.2_PWM_13.txt (K-afal) 	Zic2_2895.2_combined_PWM_13.txt (K-afal) No PWM Available
Zic2_2895.2_PWM_14.txt (K-afal) 	Zic2_2895.2_PWM_14.txt (K-afal) [Z-Beta = 73.5251] [Pearson Full 0.668009684, Pearson Zeroed 0.670616] 	Zic2_2895.2_combined_PWM_14.txt (K-afal) No PWM Available

<p>Zic2_2895.2_PWM_15.txt (K-afal)  [Z-Beta = 132.483]  [Pearson Full 0.601689067, Pearson Zeroed 0.526498]</p> 	<p>Zic2_2895.2_PWM_15.txt (K-afal)  [Z-Beta = 36.3775]  [Pearson Full 0.668009684, Pearson Zeroed 0.669845]</p> 	<p>Zic2_2895.2_combined_PWM_15.txt (K-afal)  No PWM Available</p>
<p>Zic2_2895.2_PWM_16.txt (K-afal)</p> 	<p>Zic2_2895.2_PWM_16.txt (K-afal)  [Z-Beta = 102.488]  [Pearson Full 0.668009684, Pearson Zeroed 0.620927]</p> 	<p>Zic2_2895.2_combined_PWM_16.txt (K-afal)  No PWM Available</p>
<p>Zic2_2895.2_PWM_1.txt (K-afal)</p> 	<p>Zic2_2895.2_PWM_1.txt (K-afal)  [Z-Beta = 94.383]  [Pearson Full 0.668009684, Pearson Zeroed 0.634192]</p> 	<p>Zic2_2895.2_combined_PWM_1.txt (K-afal)  [Z-Beta = 413.622]  [Pearson Full 0.747920282, Pearson Zeroed 0.623645]</p> 
<p>Zic2_2895.2_PWM_2.txt (K-afal)</p> 	<p>Zic2_2895.2_PWM_2.txt (K-afal)</p> 	<p>Zic2_2895.2_combined_PWM_2.txt (K-afal)  [Z-Beta = 122.832]  [Pearson Full 0.747920282, Pearson Zeroed 0.741478]</p> 
<p>Zic2_2895.2_PWM_3.txt (K-afal)</p> 	<p>Zic2_2895.2_PWM_3.txt (K-afal)</p> 	<p>Zic2_2895.2_combined_PWM_3.txt (K-afal)  No PWM Available</p>
<p>Zic2_2895.2_PWM_4.txt (K-afal)</p> 	<p>Zic2_2895.2_PWM_4.txt (K-afal)</p> 	<p>Zic2_2895.2_combined_PWM_4.txt (K-afal)  No PWM Available</p>
<p>Zic2_2895.2_PWM_5.txt (K-afal)</p> 	<p>Zic2_2895.2_PWM_5.txt (K-afal)</p> 	<p>Zic2_2895.2_combined_PWM_5.txt (K-afal)  No PWM Available</p>
<p>Zic2_2895.2_PWM_6.txt (K-afal)</p> 	<p>Zic2_2895.2_PWM_6.txt (K-afal)</p> 	<p>Zic2_2895.2_combined_PWM_6.txt (K-afal)  No PWM Available</p>
<p>Zic2_2895.2_PWM_7.txt (K-afal)</p> 	<p>Zic2_2895.2_PWM_7.txt (K-afal)</p> 	<p>Zic2_2895.2_combined_PWM_7.txt (K-afal)  No PWM Available</p>
<p>Zic2_2895.2_PWM_8.txt (K-afal)</p> 	<p>Zic2_2895.2_PWM_8.txt (K-afal)</p> 	<p>Zic2_2895.2_combined_PWM_8.txt (K-afal)  No PWM Available</p>
<p>Zic2_2895.2_PWM_9.txt (K-afal)</p> 	<p>Zic2_2895.2_PWM_9.txt (K-afal)</p> 	<p>Zic2_2895.2_combined_PWM_9.txt (K-afal)  No PWM Available</p>
<p>Zic2_2895.2_PWM_10.txt (Kafal)  No PWM Available</p>	<p>Zic2_2895.2_PWM_10.txt (Kafal)</p> 	<p>Zic2_2895.2_combined_PWM_10.txt (Kafal)  No PWM Available</p>

<p>Zic2_2895.2_PWM_11.txt (Kafal) No PWM Available</p>	<p>Zic2_2895.2_PWM_11.txt (Kafal) [Z-Beta = 61.6386] [Pearson Full 0.668009684, Pearson Zeroed 0.666207]</p> 	<p>Zic2_2895.2_combined_PWM_11.txt (Kafal) No PWM Available</p>
<p>Zic2_2895.2_PWM_12.txt (Kafal) No PWM Available</p>	<p>Zic2_2895.2_PWM_12.txt (Kafal)</p> 	<p>Zic2_2895.2_combined_PWM_12.txt (Kafal) No PWM Available</p>
<p>Zic2_2895.2_PWM_13.txt (Kafal) No PWM Available</p>	<p>Zic2_2895.2_PWM_13.txt (Kafal)</p> 	<p>Zic2_2895.2_combined_PWM_13.txt (Kafal) No PWM Available</p>
<p>Zic2_2895.2_PWM_14.txt (Kafal) No PWM Available</p>	<p>Zic2_2895.2_PWM_14.txt (Kafal) [Z-Beta = 73.5251] [Pearson Full 0.668009684, Pearson Zeroed 0.670616]</p> 	<p>Zic2_2895.2_combined_PWM_14.txt (Kafal) No PWM Available</p>
<p>Zic2_2895.2_PWM_15.txt (Kafal) No PWM Available</p>	<p>Zic2_2895.2_PWM_15.txt (Kafal) [Z-Beta = 36.3775] [Pearson Full 0.668009684, Pearson Zeroed 0.669845]</p> 	<p>Zic2_2895.2_combined_PWM_15.txt (Kafal) No PWM Available</p>
<p>Zic2_2895.2_PWM_16.txt (Kafal) No PWM Available</p>	<p>Zic2_2895.2_PWM_16.txt (Kafal) [Z-Beta = 102.488] [Pearson Full 0.668009684, Pearson Zeroed 0.620927]</p> 	<p>Zic2_2895.2_combined_PWM_16.txt (Kafal) No PWM Available</p>
<p>Zic2_2895.2_PWM_17.txt (Kafal) No PWM Available</p>	<p>Zic2_2895.2_PWM_17.txt (Kafal) [Z-Beta = 115.93] [Pearson Full 0.668009684, Pearson Zeroed 0.648023]</p> 	<p>Zic2_2895.2_combined_PWM_17.txt (Kafal) No PWM Available</p>
<p>Zic2_2895.2_PWM_18.txt (Kafal) No PWM Available</p>	<p>Zic2_2895.2_PWM_18.txt (Kafal)</p> 	<p>Zic2_2895.2_combined_PWM_18.txt (Kafal) No PWM Available</p>

# Zic3\_3119.2

Array Version 1	Array Version 2	Combined
<p>Zic3_3119.2_v1_prim_auc.txt (SW)</p>	<p>Zic3_3119.2_v2_prim_auc.txt (SW)  [Z-Beta = 95.7081]  [Pearson Full 0.76648774, Pearson Zeroed 0.758918]</p>	<p>Zic3_3119.2_combined_prim_auc.txt (SW)</p>
<p>Zic3_3119.2_v1_prim_ic.txt (SW)</p>	<p>Zic3_3119.2_v2_prim_ic.txt (SW)  [Z-Beta = 96.3479]  [Pearson Full 0.76648774, Pearson Zeroed 0.758607]</p>	<p>Zic3_3119.2_combined_prim_ic_trimmed.txt (SW)  [Z-Beta = -3947.86]  [Pearson Full 0.633382402, Pearson Zeroed 0.332294]</p>
<p>Zic3_3119.2_v1_prim_untrimmed.txt (SW)  [Z-Beta = 15.7874]  [Pearson Full 0.642830195, Pearson Zeroed 0.63312]</p>	<p>Zic3_3119.2_v2_prim_untrimmed.txt (SW)  [Z-Beta = 38.4684]  [Pearson Full 0.76648774, Pearson Zeroed 0.765444]</p>	<p>Zic3_3119.2_combined_prim_untrimmed.txt (SW)  [Z-Beta = 1326.91]  [Pearson Full 0.633382402, Pearson Zeroed 0.00886076]</p>
<p>Zic3_3119.2_v1_sec_auc.txt (SW)</p>	<p>Zic3_3119.2_v2_sec_auc.txt (SW)  [Z-Beta = 179.166]  [Pearson Full 0.76648774, Pearson Zeroed 0.736094]</p>	<p>Zic3_3119.2_combined_sec_auc.txt (SW)</p>
<p>Zic3_3119.2_v1_sec_ic.txt (SW)</p>	<p>Zic3_3119.2_v2_sec_ic.txt (SW)  [Z-Beta = -110.989]  [Pearson Full 0.76648774, Pearson Zeroed 0.756664]</p>	<p>Zic3_3119.2_combined_sec_ic_trimmed.txt (SW)  [Z-Beta = 224.964]  [Pearson Full 0.633382402, Pearson Zeroed 0.575884]</p>
<p>Zic3_3119.2_v1_sec_untrimmed.txt (SW)</p>	<p>Zic3_3119.2_v2_sec_untrimmed.txt (SW)  [Z-Beta = 20.6106]  [Pearson Full 0.76648774, Pearson Zeroed 0.766069]</p>	<p>Zic3_3119.2_combined_sec_untrimmed.txt (SW)  [Z-Beta = -44.4695]  [Pearson Full 0.633382402, Pearson Zeroed 0.631464]</p>
<p>Zic3_3119.2_v1_prim_trimmed.txt (RM8)</p>	<p>Zic3_3119.2_v2_prim_trimmed.txt (RM8)  [Z-Beta = 58.771]  [Pearson Full 0.76648774, Pearson Zeroed 0.764156]</p>	<p>Zic3_3119.2_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Zic3_3119.2_v1_sec_trimmed.txt (RM8)</p>	<p>Zic3_3119.2_v2_sec_trimmed.txt (RM8)  [Z-Beta = 21.1191]  [Pearson Full 0.76648774, Pearson Zeroed 0.766063]</p>	<p>Zic3_3119.2_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>

<p>Zic3_3119.2_v1_prim_untrimmed.txt (RM8)</p>	<p>Zic3_3119.2_v2_prim_untrimmed.txt (RM8)</p> <p>[Z-Beta = 38.4684] [Pearson Full 0.76648774, Pearson Zeroed 0.765444]</p>	<p>Zic3_3119.2_RM8_combined_prim_untrimmed.txt (RM8)</p> <p>No PWM Available</p>
<p>Zic3_3119.2_v1_sec_untrimmed.txt (RM8)</p>	<p>Zic3_3119.2_v2_sec_untrimmed.txt (RM8)</p> <p>[Z-Beta = 20.6106] [Pearson Full 0.76648774, Pearson Zeroed 0.766069]</p>	<p>Zic3_3119.2_RM8_combined_sec_untrimmed.txt (RM8)</p> <p>No PWM Available</p>
<p>Zic3_3119.2_v1_RM35_prim_trimmed.txt (RM35)</p>	<p>Zic3_3119.2_v2_RM35_prim_trimmed.txt (RM35)</p> <p>[Z-Beta = -271.864] [Pearson Full 0.76648774, Pearson Zeroed 0.739938]</p>	<p>Zic3_3119.2_RM_combined_prim_trimmed.txt (RM35)</p> <p>[Z-Beta = -339.133] [Pearson Full 0.633382402, Pearson Zeroed 0.605436]</p>
<p>Zic3_3119.2_v1_RM35_sec_trimmed.txt (RM35)</p> <p>[Z-Beta = 67.3036] [Pearson Full 0.642830195, Pearson Zeroed 0.624129]</p>	<p>Zic3_3119.2_v2_RM35_sec_trimmed.txt (RM35)</p> <p>[Z-Beta = -66.789] [Pearson Full 0.76648774, Pearson Zeroed 0.763172]</p>	<p>Zic3_3119.2_RM_combined_sec_trimmed.txt (RM35)</p> <p>[Z-Beta = -22.9351] [Pearson Full 0.633382402, Pearson Zeroed 0.632957]</p>
<p>Zic3_3119.2_v1_RM35_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = 80.2675] [Pearson Full 0.642830195, Pearson Zeroed 0.628079]</p>	<p>Zic3_3119.2_v2_RM35_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = 398.429] [Pearson Full 0.76648774, Pearson Zeroed 0.632919]</p>	<p>Zic3_3119.2_RM_combined_prim_untrimmed.txt (RM35)</p> <p>[Z-Beta = 766.638] [Pearson Full 0.633382402, Pearson Zeroed 0.0316546]</p>
<p>Zic3_3119.2_v1_RM35_sec_untrimmed.txt (RM35)</p>	<p>Zic3_3119.2_v2_RM35_sec_untrimmed.txt (RM35)</p> <p>[Z-Beta = 59.3848] [Pearson Full 0.76648774, Pearson Zeroed 0.763973]</p>	<p>Zic3_3119.2_RM_combined_sec_untrimmed.txt (RM35)</p> <p>[Z-Beta = 215.025] [Pearson Full 0.633382402, Pearson Zeroed 0.589747]</p>
<p>Zic3_3119.2_PWM_1.txt (K-afal)</p> <p>[Z-Beta = 58.9487] [Pearson Full 0.642830195, Pearson Zeroed 0.636055]</p>	<p>Zic3_3119.2_PWM_1.txt (K-afal)</p> <p>[Z-Beta = 56.5304] [Pearson Full 0.76648774, Pearson Zeroed 0.763824]</p>	<p>Zic3_3119.2_combined_PWM_1.txt (K-afal)</p> <p>[Z-Beta = 103.696] [Pearson Full 0.633382402, Pearson Zeroed 0.625126]</p>
<p>Zic3_3119.2_PWM_2.txt (K-afal)</p>	<p>Zic3_3119.2_PWM_2.txt (K-afal)</p> <p>[Z-Beta = -42.6809] [Pearson Full 0.76648774, Pearson Zeroed 0.765216]</p>	<p>Zic3_3119.2_combined_PWM_2.txt (K-afal)</p> <p>No PWM Available</p>



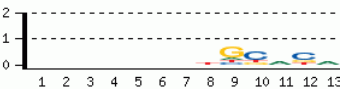
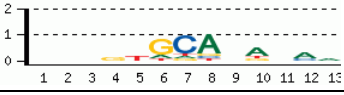



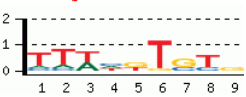

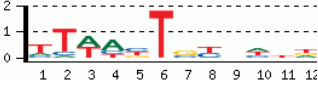

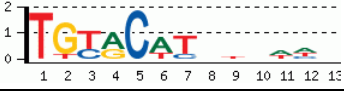


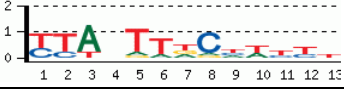
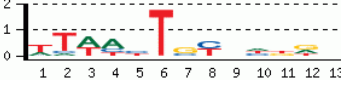






<p>Zic3_3119.2_PWM_3.txt (K-afal)  [Z-Beta = 106.05]  [Pearson Full 0.642830195, Pearson Zeroed 0.650703]</p> 	<p>Zic3_3119.2_PWM_3.txt (K-afal)  [Z-Beta = 91.1635]  [Pearson Full 0.76648774, Pearson Zeroed 0.761485]</p> 	<p>Zic3_3119.2_combined_PWM_3.txt (K-afal)  No PWM Available</p>
<p>Zic3_3119.2_PWM_4.txt (K-afal)  [Z-Beta = 154.708]  [Pearson Full 0.642830195, Pearson Zeroed 0.583991]</p> 	<p>Zic3_3119.2_PWM_4.txt (K-afal)  [Z-Beta = 126.817]  [Pearson Full 0.76648774, Pearson Zeroed 0.751426]</p> 	<p>Zic3_3119.2_combined_PWM_4.txt (K-afal)  No PWM Available</p>
<p>Zic3_3119.2_PWM_5.txt (Kafal)  No PWM Available</p>	<p>Zic3_3119.2_PWM_5.txt (Kafal)  [Z-Beta = 12.8147]  [Pearson Full 0.76648774, Pearson Zeroed 0.766362]</p> 	<p>Zic3_3119.2_combined_PWM_5.txt (Kafal)  No PWM Available</p>
<p>Zic3_3119.2_PWM_6.txt (Kafal)  No PWM Available</p>	<p>Zic3_3119.2_PWM_6.txt (Kafal)  [Z-Beta = 128.956]  [Pearson Full 0.76648774, Pearson Zeroed 0.754595]</p> 	<p>Zic3_3119.2_combined_PWM_6.txt (Kafal)  No PWM Available</p>
<p>Zic3_3119.2_PWM_7.txt (Kafal)  No PWM Available</p>	<p>Zic3_3119.2_PWM_7.txt (Kafal)  [Z-Beta = 107.142]  [Pearson Full 0.76648774, Pearson Zeroed 0.757448]</p> 	<p>Zic3_3119.2_combined_PWM_7.txt (Kafal)  No PWM Available</p>
<p>Zic3_3119.2_PWM_8.txt (Kafal)  No PWM Available</p>	<p>Zic3_3119.2_PWM_8.txt (Kafal)  [Z-Beta = 103.523]  [Pearson Full 0.76648774, Pearson Zeroed 0.757081]</p> 	<p>Zic3_3119.2_combined_PWM_8.txt (Kafal)  No PWM Available</p>
<p>Zic3_3119.2_PWM_9.txt (Kafal)  No PWM Available</p>	<p>Zic3_3119.2_PWM_9.txt (Kafal)  [Z-Beta = 94.1081]  [Pearson Full 0.76648774, Pearson Zeroed 0.758135]</p> 	<p>Zic3_3119.2_combined_PWM_9.txt (Kafal)  No PWM Available</p>
<p>Zic3_3119.2_PWM_10.txt (Kafal)  No PWM Available</p>	<p>Zic3_3119.2_PWM_10.txt (Kafal)  [Z-Beta = 151.482]  [Pearson Full 0.76648774, Pearson Zeroed 0.744385]</p> 	<p>Zic3_3119.2_combined_PWM_10.txt (Kafal)  No PWM Available</p>
<p>Zic3_3119.2_PWM_11.txt (Kafal)  No PWM Available</p>	<p>Zic3_3119.2_PWM_11.txt (Kafal)  [Z-Beta = 98.164]  [Pearson Full 0.76648774, Pearson Zeroed 0.760423]</p>	<p>Zic3_3119.2_combined_PWM_11.txt (Kafal)  No PWM Available</p>










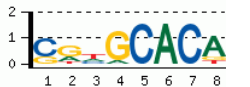
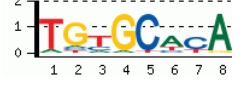








<b>Zic3_3119.2_PWM_12.txt (Kafal)</b> No PWM Available	<b>Zic3_3119.2_PWM_12.txt (Kafal)</b> <b>[Z-Beta = 116.718]</b> <b>[Pearson Full 0.76648774, Pearson Zeroed 0.757573]</b> 	<b>Zic3_3119.2_combined_PWM_12.txt (Kafal)</b> No PWM Available
<b>Zic3_3119.2_PWM_13.txt (Kafal)</b> No PWM Available	<b>Zic3_3119.2_PWM_13.txt (Kafal)</b> 	<b>Zic3_3119.2_combined_PWM_13.txt (Kafal)</b> No PWM Available

# Zscan4\_2667.2

Array Version 1	Array Version 2	Combined
<p>Zscan4_2667.2_v1_prim_auc.txt (SW)</p>	<p>Zscan4_2667.2_v2_prim_auc.txt (SW)</p>	<p>Zscan4_2667.2_combined_prim_auc.txt (SW)</p>
<p>Zscan4_2667.2_v1_prim_ic.txt (SW)</p>	<p>Zscan4_2667.2_v2_prim_ic.txt (SW)  [Z-Beta = 58.7765]  [Pearson Full 0.70938045, Pearson Zeroed 0.685091]</p>	<p>Zscan4_2667.2_combined_prim_ic_trimmed.txt (SW)  [Z-Beta = -16.967]  [Pearson Full 0.699515903, Pearson Zeroed 0.697541]</p>
<p>Zscan4_2667.2_v1_prim_untrimmed.txt (SW)</p>	<p>Zscan4_2667.2_v2_prim_untrimmed.txt (SW)</p>	<p>Zscan4_2667.2_combined_prim_untrimmed.txt (SW)  [Z-Beta = 52.2577]  [Pearson Full 0.699515903, Pearson Zeroed 0.672094]</p>
<p>Zscan4_2667.2_v1_sec_auc.txt (SW)</p>	<p>Zscan4_2667.2_v2_sec_auc.txt (SW)</p>	<p>Zscan4_2667.2_combined_sec_auc.txt (SW)</p>
<p>Zscan4_2667.2_v1_sec_ic.txt (SW)</p>	<p>Zscan4_2667.2_v2_sec_ic.txt (SW)</p>	<p>Zscan4_2667.2_combined_sec_ic_trimmed.txt (SW)  [Z-Beta = 1204.3]  [Pearson Full 0.699515903, Pearson Zeroed 0.0449197]</p>
<p>Zscan4_2667.2_v1_sec_untrimmed.txt (SW)</p>	<p>Zscan4_2667.2_v2_sec_untrimmed.txt (SW)  [Z-Beta = 38.4288]  [Pearson Full 0.70938045, Pearson Zeroed 0.706466]</p>	<p>Zscan4_2667.2_combined_sec_untrimmed.txt (SW)  [Z-Beta = 30.0309]  [Pearson Full 0.699515903, Pearson Zeroed 0.688519]</p>
<p>Zscan4_2667.2_v1_prim_trimmed.txt (RM8)  [Z-Beta = 1.24551]  [Pearson Full 0.419376646, Pearson Zeroed 0.418837]</p>	<p>Zscan4_2667.2_v2_prim_trimmed.txt (RM8)</p>	<p>Zscan4_2667.2_RM8_combined_prim_trimmed.txt (RM8)  No PWM Available</p>
<p>Zscan4_2667.2_v1_sec_trimmed.txt (RM8)</p>	<p>Zscan4_2667.2_v2_sec_trimmed.txt (RM8)  [Z-Beta = 5.71519]  [Pearson Full 0.70938045, Pearson Zeroed 0.709962]</p>	<p>Zscan4_2667.2_RM8_combined_sec_trimmed.txt (RM8)  No PWM Available</p>
<p>Zscan4_2667.2_v1_prim_untrimmed.txt (RM8)</p>	<p>Zscan4_2667.2_v2_prim_untrimmed.txt (RM8)</p>	<p>Zscan4_2667.2_RM8_combined_prim_untrimmed.txt (RM8)  No PWM Available</p>

<p>Zscan4_2667.2_v1_sec_untrimmed.txt (RM8)</p> 	<p>Zscan4_2667.2_v2_sec_untrimmed.txt (RM8)  [Z-Beta = 38.4288]  [Pearson Full 0.70938045, Pearson Zeroed 0.706466]</p> 	<p>Zscan4_2667.2_RM8_combined_sec_untrimmed.txt (RM8)  No PWM Available</p>
<p>Zscan4_2667.2_v1_RM35_prim_trimmed.txt (RM35)  [Z-Beta = 9.39417]  [Pearson Full 0.419376646, Pearson Zeroed 0.413595]</p> 	<p>Zscan4_2667.2_v2_RM35_prim_trimmed.txt (RM35)  </p>	<p>Zscan4_2667.2_RM_combined_prim_trimmed.txt (RM35)  [Z-Beta = -135.344]  [Pearson Full 0.699515903, Pearson Zeroed 0.626563]</p> 
<p>Zscan4_2667.2_v1_RM35_sec_trimmed.txt (RM35)  [Z-Beta = 28.0831]  [Pearson Full 0.419376646, Pearson Zeroed 0.342849]</p> 	<p>Zscan4_2667.2_v2_RM35_sec_trimmed.txt (RM35)  [Z-Beta = 14.609]  [Pearson Full 0.70938045, Pearson Zeroed 0.695933]</p> 	<p>Zscan4_2667.2_RM_combined_sec_trimmed.txt (RM35)  [Z-Beta = 144.766]  [Pearson Full 0.699515903, Pearson Zeroed 0.440886]</p> 
<p>Zscan4_2667.2_v1_RM35_prim_untrimmed.txt (RM35)  </p>	<p>Zscan4_2667.2_v2_RM35_prim_untrimmed.txt (RM35)  [Z-Beta = 1.37681]  [Pearson Full 0.70938045, Pearson Zeroed 0.709058]</p> 	<p>Zscan4_2667.2_RM_combined_prim_untrimmed.txt (RM35)  [Z-Beta = 153.648]  [Pearson Full 0.699515903, Pearson Zeroed 0.380763]</p> 
<p>Zscan4_2667.2_v1_RM35_sec_untrimmed.txt (RM35)  [Z-Beta = 7.3474]  [Pearson Full 0.419376646, Pearson Zeroed 0.410753]</p> 	<p>Zscan4_2667.2_v2_RM35_sec_untrimmed.txt (RM35)  [Z-Beta = 23.6877]  [Pearson Full 0.70938045, Pearson Zeroed 0.686007]</p> 	<p>Zscan4_2667.2_RM_combined_sec_untrimmed.txt (RM35)  [Z-Beta = -79.5962]  [Pearson Full 0.699515903, Pearson Zeroed 0.642721]</p> 
<p>Zscan4_2667.2_PWM_1.txt (K-afal)  [Z-Beta = 25.6464]  [Pearson Full 0.419376646, Pearson Zeroed 0.38553]</p> 	<p>Zscan4_2667.2_PWM_1.txt (K-afal)  </p>	<p>Zscan4_2667.2_combined_PWM_1.txt (K-afal)  [Z-Beta = 23.7708]  [Pearson Full 0.699515903, Pearson Zeroed 0.695582]</p> 
<p>Zscan4_2667.2_PWM_2.txt (K-afal)  </p>	<p>Zscan4_2667.2_PWM_2.txt (K-afal)  </p>	<p>Zscan4_2667.2_combined_PWM_2.txt (K-afal)  [Z-Beta = -4.08295]  [Pearson Full 0.699515903, Pearson Zeroed 0.699383]</p> 
<p>Zscan4_2667.2_PWM_3.txt (K-afal)  </p>	<p>Zscan4_2667.2_PWM_3.txt (K-afal)  [Z-Beta = 17.4288]  [Pearson Full 0.70938045, Pearson Zeroed 0.708087]</p> 	<p>Zscan4_2667.2_combined_PWM_3.txt (K-afal)  [Z-Beta = 59.0953]  [Pearson Full 0.699515903, Pearson Zeroed 0.660943]</p> 
<p>Zscan4_2667.2_PWM_4.txt (K-afal)</p>	<p>Zscan4_2667.2_PWM_4.txt (K-afal)  [Z-Beta = 38.3969]  [Pearson Full 0.70938045, Pearson Zeroed 0.700607]</p>	<p>Zscan4_2667.2_combined_PWM_4.txt (K-afal)  [Z-Beta = 45.8877]  [Pearson Full 0.699515903, Pearson Zeroed 0.677448]</p>

		
<p>Zscan4_2667.2_PWM_5.txt (K-afal)</p> 	<p>Zscan4_2667.2_PWM_5.txt (K-afal)</p> 	<p>Zscan4_2667.2_combined_PWM_5.txt (K-afal)  [Z-Beta = 46.6428]  [Pearson Full 0.699515903, Pearson Zeroed 0.681199]</p> 
<p>Zscan4_2667.2_PWM_6.txt (K-afal)  [Z-Beta = 37.4454]  [Pearson Full 0.419376646, Pearson Zeroed 0.356449]</p> 	<p>Zscan4_2667.2_PWM_6.txt (K-afal)</p> 	<p>Zscan4_2667.2_combined_PWM_6.txt (K-afal)  [Z-Beta = 15.6518]  [Pearson Full 0.699515903, Pearson Zeroed 0.698222]</p> 
<p>Zscan4_2667.2_PWM_7.txt (K-afal)</p> 	<p>Zscan4_2667.2_PWM_7.txt (K-afal)</p> 	<p>Zscan4_2667.2_combined_PWM_7.txt (K-afal)  [Z-Beta = 29.8053]  [Pearson Full 0.699515903, Pearson Zeroed 0.691365]</p> 
<p>Zscan4_2667.2_PWM_8.txt (K-afal)  [Z-Beta = 2.18372]  [Pearson Full 0.419376646, Pearson Zeroed 0.41753]</p> 	<p>Zscan4_2667.2_PWM_8.txt (K-afal)</p> 	<p>Zscan4_2667.2_combined_PWM_8.txt (K-afal)  [Z-Beta = 5.52255]  [Pearson Full 0.699515903, Pearson Zeroed 0.699248]</p> 
<p>Zscan4_2667.2_PWM_9.txt (Kafal)  No PWM Available</p>	<p>Zscan4_2667.2_PWM_9.txt (Kafal)  [Z-Beta = 95.6336]  [Pearson Full 0.70938045, Pearson Zeroed 0.672057]</p> 	<p>Zscan4_2667.2_combined_PWM_9.txt (Kafal)  No PWM Available</p>
<p>Zscan4_2667.2_PWM_10.txt (Kafal)  No PWM Available</p>	<p>Zscan4_2667.2_PWM_10.txt (Kafal)  [Z-Beta = 51.6858]  [Pearson Full 0.70938045, Pearson Zeroed 0.688773]</p> 	<p>Zscan4_2667.2_combined_PWM_10.txt (Kafal)  No PWM Available</p>